UNIVERSITY OF MUMBAI
No. UG/43 of 2018-19

CIRCULAR:-

Attention of the Principals of the affiliated Colleges and Directors of the recognized Institutions in Science & Technology Faculty is invited to this office Circular No. UG/243 of 2010, dated 12th August, 2010 relating to syllabus of the Bachelor of Engineering (B.E.) degree course.

They are hereby informed that the recommendations made by the Ad-hoc Board of Studies in Electronics Engineering at its meeting held on 9th April, 2018 have been accepted by the Academic Council at its meeting held on 5th May, 2018 **vide** item No. 4.54 and that in accordance therewith, the revised syllabus as per the (CBCS) for the T.E. & B.E. in Electronics Engineering (Sem - V to VIII) has been brought into force with effect from the academic year 2018-19 and 2019-2020, accordingly. (The same is available on the University's website www.mu.ac.in).

(Dr. Dinesh Kamble)
I/c REGISTRAR

MUMBAI – 400 032
25th June, 2018

To

The Principals of the affiliated Colleges & Directors of the recognized Institutions in Science & Technology Faculty. (Circular No. UG/334 of 2017-18 dated 9th January, 2018.)

A.C/4.54/05/05/2018

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No. UG/ 43 -A of 2018 MUMBAI-400 032 25th June, 2018

Copy forwarded with Compliments for information to:-

1) The I/c Dean, Faculty of Science & Technology,
2) The Chairman, Ad-hoc Board of Studies in Electronics Engineering,
3) The Director, Board of Examinations and Evaluation,
4) The Director, Board of Students Development,
5) The Co-Ordinator, University Computerization Centre,

(Dr. Dinesh Kamble)
I/c REGISTRAR

AC19.04.2017
Item No. X.XX

UNIVERSITYOFMUMBAI

Revised syllabus (Rev- 2016) from Academic Year 2016 -17
Under

FACULTY OF TECHNOLOGY

Electronics Engineering

Second Year with Effect from **AY 2017-18**
Third Year with Effect from **AY 2018-19**
Final Year with Effect from **AY 2019-20**

As per **Choice Based Credit and Grading System**
with effect from the AY 2016–17

Co-ordinator, Faculty of Technology's Preamble:

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty of Technology, University of Mumbai, in one of its meeting unanimously resolved that, each Board of Studies shall prepare some Program Educational Objectives (PEO's) and give freedom to affiliated Institutes to add few (PEO's). It is also resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. It was also resolved that, maximum senior faculty from colleges and experts from industry to be involved while revising the curriculum. I am happy to state that, each Board of studies has adhered to the resolutions passed by Faculty of Technology, and developed curriculum accordingly. In addition to outcome based education, semester based credit and grading system is also introduced to ensure quality of engineering education.

Choice based Credit and Grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. University of Mumbai has taken a lead in implementing the system through its affiliated Institutes and Faculty of Technology has devised a transparent credit assignment policy and adopted ten points scale to grade learner's performance. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 12-13 weeks and remaining 2-3 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

Choice based Credit and grading system is implemented from the academic year 2016-17 through optional courses at department and institute level. This will be effective for SE, TE and BE from academic year 2017-18, 2018-19 and 2019-20 respectively.

Dr. S. K. Ukarande
Co-ordinator,
Faculty of Technology,
Member - Academic Council
University of Mumbai, Mumbai

Chairman's Preamble:

Engineering education in India is expanding and is set to increase manifold. Themajor challenge in the current scenario is to ensure quality to the stakeholders along with expansion. To meet this challenge, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education and reflects the fact that in achieving recognition, the institution or program of study is committed and open to external review to meet certain minimum specified standards.The major emphasis of this accreditation process is to measure the outcomes of the program that is being accredited. Program outcomes are essentially a range of skills and knowledge that a student will have at the time of graduation from the program. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating the philosophy of outcome based education in the process of curriculum development.

As the Chairman, Board of Studies in Electronics Engineering of the University of Mumbai, I am happy to state here that, the Program Educational Objectives for Undergraduate Program were finalized in a brain storming session, which was attended by more than 40 members from different affiliated Institutes of the University. They are either Heads of Departments or their senior representatives from the Department of Electronics Engineering. The Program Educational Objectives finalized for the undergraduate program in Electronics Engineering are listed below;

1. To prepare the Learner with a sound foundation in the mathematical, scientific and engineering fundamentals
2. To motivate the Learner in the art of self-learning and to use modern tools for solving real life problems
3. To inculcate a professional and ethical attitude, good leadership qualities and commitment to social responsibilities in the Learner's thought process
4. To prepare the Learner for a successful career in Indian and Multinational Organisations

In addition to Program Educational Objectives, for each course of the program, objectives and expected outcomes from a learner's point of view are also included in the curriculum to support the philosophy of outcome based education. I strongly believe that even a small step taken in the right direction will definitely help in providing quality education to the major stakeholders.

Dr.Sudhakar S. Mande

Chairman, Board of Studies in Electronics Engineering, University of Mumbai

S.E. (Electronics Engineering) – Semester III

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX301	Applied Mathematics III	04	---	01@	04	---	01	05
ELX302	Electronic Devices and Circuits I	04	---	---	04	---	---	04
ELX303	Digital Circuit Design	04	---	---	04	---	---	04
ELX304	Electrical Network Analysis and Synthesis	04	---	---	04	---	---	04
ELX305	Object Oriented Programming Methodology	04	---	---	04	---	---	04
ELXL301	Electronic Devices and Circuits I Lab		02	---	---	01	---	01
ELXL302	Digital Circuit Design Lab.		02	---	---	01	---	01
ELXL303	Electrical Network Analysis and Synthesis Lab		02	---	---	01	---	01
ELXL304	Object Oriented Programming Methodology Lab.		02+02#	---	---	02	---	02
	Total	**20**	**08**	**02**	**20**	**04**	**01**	**26**

@1 hour tutorial class-wise

#02 hours class-wise and 02 hours batch-wise

Course Code	Course Name	Examination Scheme – Semester III							
		Theory					Term Work	Oral /Prac	Total
		Internal Assessment (IA)			End Sem Exam Marks	Exam Duration (Hours)			
		Test I	Test II	AVG.					
ELX301	Applied Mathematics III	20	20	20	80	03	25	---	125
ELX302	Electronic Devices and Circuits I	20	20	20	80	03	--	---	100
ELX303	Digital Circuit Design	20	20	20	80	03	---	---	100
ELX304	Electrical Network Analysis and Synthesis	20	20	20	80	03	---	---	100
ELX305	Object Oriented Programming Methodology	20	20	20	80	03	---	---	100
ELXL301	Electronic Devices and Circuits I Lab						25	25	50
ELXL302	Digital Circuit Design Lab.						25	25	50
ELXL303	Object Oriented Programming Methodology Lab.						25	25	50
ELXL304	Electrical Network Analysis and Synthesis Lab						25	---	25
Total		**100**	**100**	**100**	**400**	**15**	**125**	**75**	**700**

T.E. (Electronics Engineering) – Semester V

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX501	Microcontrollers and Applications	04	--	---	04	---	---	04
ELX 502	Digital Communication	04	-	--	04	---	---	04
ELX 503	Engineering Electromagnetics	04	-	@01	04	---	01	05
ELX 504	Design with Linear Integrated Circuits	04	02	---	04	---	---	04
ELX 505	Business Communication & Ethics	02	02#		---	02	---	02
ELXDLO501X	Department Level optional courses I	04	02	---	04		---	04
ELXL501	Microcontrollers and Applications Lab.					01	---	01
ELXL502	Digital Communication Lab.					01	---	01
ELXL503	Design with Linear Integrated Circuits Lab.					01	---	01
ELX DLOl50X	Department Level optional course-I Lab					01	---	01
	TOTAL	**20**	**08**	**04**	**20**	**06**	**01**	**27**

1 hour tutorial class-wise #02 hours batch-wise

Course Code	Course Name	Examination Scheme – Semester V							
		Theory					Term Work	Oral /Prac	Total
		Internal Assessment (IA)			End Sem Exam Marks	Exam Duration (Hours)			
		Test I	Test II	AVG.					
ELX501	Micro-controllers and Applications	20	20	20	80	03	---	---	100
ELX 502	Digital Communication	20	20	20	80	03	---	---	100
ELX 503	Engineering Electromagnetics	20	20	20	80	03	25	---	125
ELX 504	Design with Linear Integrated Circuits	20	20	20	80	03	---	---	100
ELX 505	Business Communication & Ethics	---	---	---	---	---	50	---	50
ELX DLO501X	Department Level Elective-I	20	20	20	80	03	---	---	100
ELXL501	Micro-controllers and Applications Lab.						25	25	50
ELXL 502	Digital Communication Lab.						25	---	25
ELXL 503	Design with Linear Integrated Circuits Lab.						25	25	50
ELXL DLO501X	Department Elective I lab						25	25	50
Total		**100**	**100**	**100**	**400**	**15**	**175**	**75**	**750**

Course Code	Department Level Optional Course I
ELXDLO5011	Database and Management System
ELXDLO5012	Digital Control system
ELXDLO5013	ASIC Verification
ELXDLO5014	Biomedical Instrumentation

T.E. (Electronics Engineering) – Semester VI

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX601	Embedded System and RTOS	04	--	---	04	---	---	04
ELX 602	Computer Communication Network	04	--	---	04	---	---	04
ELX 603	VLSI Design	04	--	---	04	---	---	04
ELX 604	Signals and systems	04	--	@01	04	---	01	05
ELXDLO502X	Department Level Optional courses II	04	--	---	04	---	---	04
ELXL601	Embedded System and RTOS Lab.	--	02	--	--	01	---	01
ELXL 602	Computer Communication Network Lab.	--	02	--	--	01	--	01
ELXL 603	VLSI Design Lab.	--	02	--		01	---	01
ELXLDLO601X	Department Level Optional courses IILab.	--	02	--		01	---	01
TOTAL		**20**	**08**	**01**	**20**	**04**	**01**	**25**

Course Code	Course Name	Examination Scheme – Semester VI							
		Theory					Term Work	Oral /Prac	Total
		Internal Assessment (IA)			End Sem Exam Marks	Exam Duration (Hours)			
		Test I	Test II	AVG.					
ELX601	Embedded System and RTOS	20	20	20	80	03	---	---	100
ELX 602	Computer Communication Network	20	20	20	80	03	---	---	100
ELX 603	VLSI Design	20	20	20	80	03	---	---	100
ELX 604	Signals and systems	20	20	20	80	03	25	25	100
ELXDLO602X	Department Level Optional courses II*	20	20	20	80	03	---	---	100
ELXL601	Embedded System and RTOS Lab.						25	25	50
ELXL 602	Computer Communication Network Lab.						25	25	50
ELXL 603	VLSI Design Lab.						25	25	50
ELXLDLO602X	Department Level Optional Courses II*Lab.						25	25	50
Total		**100**	**100**	**100**	**400**	**15**	**125**	**125**	**750**

Course Code	Department Level Optional Course II
ELXDLO6021	Microwave Engineering
ELXDLO6022	Electronics Product Design
ELXDLO6023	Wireless Communication
ELXDLO6024	Computer Organization and Architecture

B.E. (Electronics Engineering) – Semester VII

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX701	Instrumentation System Design	04	--	---	04	---	---	04
ELX702	Power Electronics	04	--	---	04	---	---	04
ELX703	Digital signal processing	04	--	---	04	---	---	04
ELXDLO703X	Department Level Optional course III	04	--	---	04	---	---	04
ILO701X	Institute Level Optional Course I#	03	---	---	03	---	---	03
ELXL701	Instrumentation System Design Lab.		02			01	---	01
ELXL702	Power Electronics Lab.		02			01	---	01
ELXL703	Digital signal processing Lab.		02			01	---	01
ELXL704	Project-I	---	06	---	---	03	---	03
ELXLDLO703X	Dept. Level Optional course III Lab.		02			01	---	01
	TOTAL	**19**	**14**	**---**	**19**	**07**	**---**	**26**

Course Code	Course Name	Examination Scheme – Semester VII							
		Theory					Term Work	Oral /Prac	Total
		Internal Assessment (IA)			End Sem Exam Marks	Exam Duration (Hours)			
		Test I	Test II	AVG.					
ELX701	Instrumentation System Design	20	20	20	80	03	---	---	100
ELX 702	Power Electronics	20	20	20	80	03	---	---	100
ELX 703	Digital signal processing	20	20	20	80	03	---	---	100
ELXDLO703X	Department Level Optional courses III*	20	20	20	80	03	---	---	100
ILO701X	Institute Level Optional Subject	20	20	20	80	03	---	---	100
ELXL701	Instrumentation System Design Lab.						25	25	50
ELXL702	Power Electronics Lab.						25	25	50
ELXL703	Digital signal processing Lab.						25	25	50
ELXL704	Project-I	---	---	---	---	---	50	50	100
ELXLDLO703X	Dept. Level Optional courses III Lab.						25	25	50
Total		**100**	**100**	**100**	**400**	**15**	**150**	**150**	**800**

B.E. (Electronics Engineering) – Semester VIII

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX801	Internet of Things	04	--	---	04	---	---	04
ELX 802	Analog and Mixed VLSI Design	04	--	---	04	---	---	04
ELXDLO804X	Department Level Optional course IV	04	--	---	04	---	---	04
ILO802X	Institute Level Optional course II#	03	---	---	03	---	---	03
ELXL801	Internet of Things Lab.		02			01	---	01
ELXL802	Analog and Mixed VLSI Design Lab.		02			01	---	01
ELXL803	Project-II	---	12	---	---	06	---	06
ELXLDLO804X	Department Level Optional Courses IV Lab.		02			01	---	01
	TOTAL	**15**	**18**	**---**	**15**	**9**	---	**24**

Course Code	Course Name	Examination Scheme – Semester VIII							
		Theory					Term Work	Oral /Prac	Total
		Internal Assessment (IA)			End Sem Exam Marks	Exam Duration (Hours)			
		Test I	Test II	AVG.					
ELX801	Internet of Things	20	20	20	80	03	---	---	100
ELX 802	Analog and Mixed VLSI Design	20	20	20	80	03	---	---	100
ELXDLO804X	Department Level Optional course IV	20	20	20	80	03	---	---	100
ILO802X	Institute Level Optional course II	20	20	20	80	03	---	---	100
ELXL801	Internet of Things Lab.						25	25	50
ELXL802	Analog and Mixed VLSI Design Lab.						25	25	50
ELXL803	Project-II	---	---	---	---	---	100	50	150
ELXLDLO804X	Department Level Optional Courses IV Lab.						25	25	50
Total		**80**	**80**	**80**	**320**	**15**	**150**	**150**	**700**

Course Code	Department Level Optional Course III	Course Code	Institute Level Optional Course I*
ELXDLO7031	Neural Network and Fuzzy Logic	ILO7011	Product Lifecycle Management
ELXDLO7032	Advance Networking Technologies	ILO7012	Reliability Engineering
ELXDLO7033	Robotics	ILO7013	Management Information System
ELXDLO7034	Integrated Circuit Technology	ILO7014	Design of Experiments
		ILO7015	Operation Research
		ILO7016	Cyber Security and Laws
		ILO7017	Disaster Management and Mitigation Measures
		ILO7018	Energy Audit and Management

Course Code	Department Level Elective Course IV	Course Code	Institute Level Elective Course II#
ELXDLO8041	Advanced Power Electronics	ILO8021	Project Management
ELXDLO8042	MEMS Technology	ILO8022	Finance Management
ELXDLO8043	Virtual Instrumentation	ILO8023	Entrepreneurship Development and Management
ELXDLO8044	Digital Image Processing	ILO8024	Human Resource Management
		ILO8025	Professional Ethics and CSR
		ILO8026	Research Methodology
		ILO8027	IPR and Patenting
		ILO8028	Digital Business Management
		ILO8029	Environmental Management

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELX 501	Microcontrollers and Applications	04	--	--	04	--	--	04

Course Code	Course Name	Examination Scheme									
		Theory					Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem	Dura-tion (hrs)					
		Test 1	Test 2	Avg.							
ELX 501	Microcontrollers &Applications	20	20	20	80	03	--	--	-	--	100

Course Code	Course Name	Credits
ELX 501	Microcontrollers and Applications	04
Course Objectives	To study 8-bit microcontroller architecture for system design along with exposure to advanced 32-bit architecture.	
Course Outcomes	1. Explain 8051 microcontroller architecture. 2. Develop assembly language programmes for 8051 microcontroller. 3. Design and implement 8051 based systems. 4. Explain advanced features of Cortex-M3 architecture.	

Module		Contents	Time
1.		**8051 Microcontroller Architecture**	04
	1.1	Introduction to microcontroller.	
	1.2	Overview of MCS51 family.	
	1.3	8051 architectural features.	
	1.4	Memory organisation.	
2.		**8051 Microcontroller assembly language programming**	10
	2.1	Addressing modes of 8051.	
	2.2	Instruction Set: Data transfer, Arithmetic, Logical, Branching.	
	2.3	Assembly Language Programming.	
3.		**8051 Internal Hardware & Programming**	10
	3.1	I/O port structure and programming.	
	3.2	Interrupts and programming.	
	3.3	Timer/Counter and programming.	
	3.4	Serial port and programming.	
4.		**8051 Interfacing & Applications**	12
	4.1	Display interfacing: 7-segment LED display, 16x2 generic alphanumeric	

		LCD display.	
	4.2	Keyboard interfacing: 4x4 matrix keyboard.	
	4.3	Analog devices interfacing: 8-bit ADC/DAC, temperature sensor (LM35).	
	4.4	Motor interfacing: Relay, dc motor, stepper motor and servo motor.	
5.		**ARM CORTEX-M3 Architecture**	**12**
	5.1	Comparison of CISC & RISC architectures, overview of ARM family.	
	5.2	ARM Cortex-M3 architecture, Programmer's model: Operation Modes and States, registers, special registers, Application Program Status Register-Integer status flags, Q status flag, GE bits.	
	5.3	Memory system: Features and memory map	
	5.4	Exceptions and Interrupts-Nested vectored interrupt controller	
		Total	**48**

Text books:

1.M. A. Mazidi, J. C. Mazidi, Rolin D. McKinlay,"The 8051 Microcontroller and Embedded Systems Using Assembly and C", Pearson Education, 2nd Edition.

2.Joseph Yiu,"The Definitive guide to ARM CORTEX-M3 & CORTEX-M4 Processors", Elsevier, 2014, 3rd Edition.

Reference Books:

1.Kenneth J. Ayala,"The 8051 Microcontroller", Cengage Learning India Pvt. Ltd, 3rdEdition.

2.David Seal, "ARM Architecture", Reference Manual (2nd Edition), Publisher Addison Wesley.

3.Andrew Sloss, Dominic Symes, Chris Wright,"ARMSystem Developers Guide: Designing and Optimising System Software", Publisher Elsevier Inc. 2004.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total of 4 questions.
3. Question No.1 will be compulsory and based on the entire syllabus.
4. Remaining question (Q.2 to Q.6) will be set from all the modules.
5. Weightage of marks, commensurate with the time allocated to the respective module.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELX 502	Digital Communication	4	--	--	4	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX 502	Digital Communication	20	20	20	80	-	--	--	100

Course Pre-requisite: ELX405 Principles of Communication Engineering

Course Objectives:

The objectives of this course are to:

1. Understand the typical subsystems of a digital communication system
2. Understand the significance of the trade-off between SNR and Bandwidth
3. Understand the effect of ISI in Baseband transmission of a digital signal.
4. Analyze various Digital modulation techniques
5. Identify the necessity of Source encoding and Channel encoding in Digital communication

Course Outcomes:

On successful completion of the course the students will be able to:

1. Comprehend the advantages of digital communication over analog communication and explain need for various subsystems in Digital communication systems
2. Realize the implications of Shannon-Hartley Capacity theorem while designing the efficient Source encoding technique.
3. Understand the impact of Inter Symbol Interference in Baseband transmission and methods to mitigate its effect
4. Analyze various Digital modulation methods and assess them based on parameters such as spectral efficiency , Power efficiency, Probability of error in detection
5. Explain the concept and need for designing efficient Forward Error Correcting codes.
6. Realize the areas of application of Digital communication.

Module No.	Unit No.	Topics	Hrs.
1.		**Introduction to Digital communication system:**	06
	1.1	A typical Digital communication system, Advantages and disadvantages of Digital transmission, significance of digitization: PCM encoding of voice and image signals.	
	1.2	**Concept of Probability Theory in Communication Systems:** Random variables, Mean and Variance of Random variables and sum of random variables ,Definition with examples,	
	1.3	**Useful PDFs & CDFs** :Gaussian, Rayleigh pdf & Rician Distribution, Binomial Distribution, Poisson Distribution, Central-Limit Theorem, Binary Synchronous Channel(BSC), development of Optimal receiver	
2.		**Information Theory and Source Coding**	06
	2.1	Measure of Information, Entropy, Information rate, Channel capacity, Shannon – Hartley Capacity Theorem and its Implications.	
	2.2	Shannon-Fano encoding, Huffman encoding , Code Efficiency & Redundancy.	
3.		**Pulse Shaping for Optimum Transmission:**	08
	3.1	Line codes and their desirable properties, PSD of digital data	
	3.2	Baseband PAM transmission: Concept of Inter symbol interference(ISI),Raised Cosine filter , Nyquist Bandwidth. Concept of equalizer to overcome ISI	
	3.3	Correlative coding: Duo-binary encoding and modified duo-binary encoding	
4.0		**Digital Modulation Techniques**	14
	4.1	Concept of Binary and M-ary transmission, Coherent and Non- Coherent reception, Power spectral density of Pass-band signal, Signal space Representation and Euclidian distance	
	4.2	Pass Band Amplitude modulation & Demodulation: BASK , M-ary PAM ,Digital Phase Modulation & Demodulation: BPSK, OQPSK, QPSK, M-ary PSK, QAM , Digital Frequency Modulation &Demodulation :BFSK, MSK , M-ary FSK	
	4.3	Comparison of all techniques based on Spectral efficiency, Power efficiency, Probability of error in detection	
	4.4	**Optimal Reception of Digital Data**: A baseband signal receiver and its Probability of error, The Optimum receiver, Matched filter, & its properties.	
5.0		**Error Control codes:**	10
	5.1	Need for channel encoding, Concept of Error detection and correction , Forward Error	

		correction	
	5.2	**Linear block codes** : Hamming Distance, Hamming Weight, Systematic codes ,Syndrome Testing	
	5.3	**Cyclic codes**; Generator polynomial for Cyclic codes, Systematic cyclic codes, Feedback shift register for Polynomial division	
	5.4	**Convolution codes**: Convolution encoder , Impulse response of encoder, State diagram, trellis diagram Representations	
6.0		**Applications of Digital communication**	**06**
	6.1	**Satellite communication system** : Satellite communication System model, Transponder ,Satellite Orbits : LEO, MEO, GEO , Link analysis	
	6.2	**Optical Communication system** : Advantages of Optical communication ,Signal transmission in Optical fibres, Optical sources and Optical Detectors, Optical Digital Communication system.	
		Total	**48**

Recommended Text Books:

1. Simon Haykin, "*Communication System*", John Wiley And Sons ,4th Ed
2. Taub Schilling & Saha, "*Principles Of Communication Systems*", Tata Mc-Graw Hill, Third Ed
3. B P Lathi & Zhi Ding ,"*Modern Digital and Analog communication systems" -4E,* Oxford University Press , Indian Ed.
4. R N Mutagi, "*Digital Communication"*, Oxford University Press, 2nd Ed.

Reference Books:

1. Bernad Sklar,- "Digital communication", Pearson Education, 2nd Ed.
2. Simon Haykin, "Digital communication", John wiley and sons
3. PROAKIS & SALEHI, "Communication system Engineering", Pearson Education.
4. Anil K.Maini & Varsha Agarwal, "Satellite communications", Wiley publication.
5. Amitabha Bhattacharya, "*Digital Communication*", Tata Mcgraw Hill

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to marks will be asked.
4: Remaining question will be selected from all the modules.

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX503	Electromagnetic Engineering	20	20	20	80	--	--	--	100

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX503	Electromagnetic Engineering	20	20	20	80	--	--	--	100

Course Objectives:

1. To study correlation between electrostatics, steady magnetic field and time varying fields using Maxwell's equations for different media.
2. To calculate energy transported by means of electromagnetic waves from one point to another and to study polarization of waves.
3. To solve electromagnetic problems using different numerical methods.
4. To extend the students' understanding about the propagation of the waves of different types.
5. To understand the radiation concepts.

Course Outcomes:

After successful completion of the course, students will be able to:

1. Analyze the behaviour of electromagnetic waves in different media.
2. Evaluate various parameters of transmission lines and radiating systems.
3. Apply computational techniques to analyze electromagnetic field distribution.
4. Understand different mechanisms of radio wave propagation.

Module No.	Unit No.	Topics	Hrs.
1.0		**Basic Laws of Electromagnetic and Maxwell's Equations**	10
	1.1	Coulomb's law, Gauss's law, Bio-Savart's law, Ampere's law, Poisson's and Laplace equations	
	1.2	**Maxwell's Equations:** Integral and differential form for static and time varying fields and its interpretations	
	1.3	Boundary conditions for Static electric and magnetic fields	
2.0		**Electromagnetic Waves**	12
	2.1	Wave Equation and its solution in partially conducting media(lossy dielectric), perfect dielectrics, free space and good conductors, Skin Effect and concept of Skin depth	
	2.2	**Polarization of wave:** Linear, Circular and Elliptical	
	2.3	**Electromagnetic Power:** Poynting Vector and Power Flow in free space, dielectric and conducting media	
	2.4	**Propagation in different media:** Behavior of waves for normal and oblique incidence in dielectrics and conducting media, propagation in dispersive media	

3.0		**Computational Electromagnetics**	**06**
	3.1	**Finite Difference Method (FDM):** Neumann type and mixed boundary conditions, Iterative solution of finite difference equations, solutions using band matrix method	
	3.2	**Finite Element Method (FEM**): triangular mesh configuration, finite element discretization, element governing equations, assembling all equations and solving resulting equations	
	3.3	**Method of Moment (MOM):** Field calculations of conducting wire	
4.0		**Fundamentals of Radiating Systems**	**06**
	4.1	Concept of retarded potentials, Lorentz Condition	
	4.2	Radiation from an alternating current element, half-wave dipole and quarter-wave monopole	
	4.3	**Antenna Parameters:** Radiation Patterns, beam-width, Radiation intensity, directivity, power gain, band-width, radiation resistance and efficiency, effective length and effective area	
5.0		**Radio wave propagation**	**06**
	5.1	**Types of wave propagation:** Ground, space, and surface wave propagation	
	5.2	**Space wave propagation:** Effect of imperfection of earth, curvature of earth, effect of interference zone, Line of sight propagation, troposphere propagation and fading	
	5.3	**Sky wave propagation:** Reflection and refraction of waves, structure of Ionosphere	
	5.4	**Measures of ionosphere propagation:** Critical frequency, Angle of incidence, Maximum usable frequency, Skip distance, Virtual height	
6.0		**Transmission Lines**	**08**
	6.1	Transmission Line parameters and equivalent circuit Transmission line equation and solution	
	6.2	**Secondary Parameters:** Propagation constant, characteristic impedance, reflection and transmission coefficient, Input Impedance, SWR, introduction to Smith chart	
		Total	**48**

Recommended Books:

1. W.H. Hayt, and J.A. Buck, "*Engineering Electromagnetics*", McGraw Hill Publications, 7th Edition, 2006
2. R.K. Shevgaonkar, "*Electromagnetic Waves*", TATA McGraw Hill Companies, 3rd Edition, 2009
3. Edward C. Jordan and Keth G. Balmin, "*Electromagnetic Waves and Radiating Systems*", Pearson Publications, 2nd Edition, 2006
4. Matthew N.D. Sadiku, "*Principles of Electromagnetics*", Oxford International Student 4th Edition, 2007
5. J.D. Kraus, R.J. Marhefka, and A.S. Khan, "*Antennas & Wave Propagation*", McGraw Hill Publications, 4th Edition, 2011

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to marks will be asked.
4: Remaining question will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pract	Tutorial	Total
ELX504	Design with Linear Integrated Circuits	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Prac.	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ELX504	Design with Linear Integrated Circuits	20	20	20	80	--	--	--	100

Course Pre-requisite:

- Electronic Devices and Circuits I and II

Course Objectives:

1. To teach fundamental principles of standard linear integrated circuits.
2. To develop a overall approach for students from selection of integrated circuit, study its specification, the functionality, design and practical applications

Course Outcomes:

After successful completion of the course student will be able to

1. demonstrate an understanding of fundamentals of integrated circuits.
2. analyze the various applications and circuits based on particular linear integrated circuit.
3. select and use an appropriate integrated circuit to build a given application.
4. design an application with the use of integrated circuit

Module No.	Unit No.	Topics	Hrs.
1	**Fundamentals of Operational Amplifier**		04
	1.1	Ideal Op Amp, characteristics of op-amp, op-amp parameters, high frequency effects on op-amp gain and phase, slew rate limitation, practical determination of op-amp parameters, single supply versus dual supply op-amp	
	1.2	Operational amplifier open loop and closed loop configurations, Inverting and non-inverting amplifier	
2	**Applications of Operational Amplifier**		12
	2.1	**Amplifiers:** Adder, subtractor, integrator, differentiator, current amplifier, difference amplifier, instrumentation amplifier and application of Op-Amp in Transducer Measurement System with detail design Procedure. Single supply dc biasing techniques for inverting, non inverting and differential amplifiers.	
	2.2	**Converters:** Current to voltage converters, voltage to current converters, generalized impedance converter	
	2.3	**Active Filters:** First order filters, Second order active finite and infinite gain low pass, high pass, band pass and band reject filters.	
	2.4	**Sine Wave Oscillators:** RC phase shift oscillator, Wien bridge oscillator,	

		Quadrature oscillator.	
3	**Non-Linear Applications of Operational Amplifier**		10
	3.1	**Comparators:** Inverting comparator, non-inverting comparator, zero crossing detector, window detector and level detector.	
	3.2	**Schmitt Triggers:** Inverting Schmitt trigger, non-inverting Schmitt trigger with adjustable threshold levels.	
	3.3	**Waveform Generators:** Square wave generator and triangular wave generator with duty cycle modulation**.**	
	3.4	**Precision Rectifiers:** Half wave and full wave precision rectifiers and their applications.	
	3.5	Peak Detectors, Sample & Hold Circuits, voltage to frequency converter, frequency to voltage converter, logarithmic converters and antilog converters	
4	**Data Converters**		06
	4.1	**Analog to Digital**: Performance parameters of ADC, Single Ramp ADC, ADC using DAC, Dual Slope ADC, Successive Approximation ADC, Flash ADC, ADC0808/0809 and its interfacing	
	4.2	**Digital to Analog**: Performance parameters of DAC, Binary weighted register DAC, R/2R ladder DAC, Inverted R/2R ladder DAC, DAC0808 and its interfacing	
5	**Special Purpose Integrated Circuits**		08
	5.1	Functional block diagram, working, design and applications of Timer 555.	
	5.2	Functional block diagram, working and applications of VCO 566, PLL 565, multiplier 534, waveform generator XR 2206, power amplifier LM380.	
6	**Voltage Regulators**		08
	6.1	Functional block diagram, working and design of three terminal fixed (78XX, 79XX series) and three terminal adjustable (LM 317, LM 337) voltage regulators.	
	6.2	Functional block diagram, working and design of general purpose 723 (LVLC, LVHC, HVLC and HVHC) with current limit and current fold-back protection, Switching regulator topologies, Functional block diagram and working of LT1070 monolithic switching regulator.	
		Total	**48**

Recommended Books:

1. Sergio Franco, "*Design with operational amplifiers and analog integrated circuits*", Tata McGraw Hill, 3rd Edition.
2. William D. Stanley, "*Operational Amplifiers with Linear Integrated Circuits* ", Pearson, 4th Edition
3. D. Roy Choudhury and S. B. Jain, "*Linear Integrated Circuits*", New Age International Publishers, 4th Edition.
4. David A. Bell, "*Operation Amplifiers and Linear Integrated Circuits*", Oxford University Press, Indian Edition.
5. Ramakant A. Gayakwad, "*Op-Amps and Linear Integrated Circuits*", Pearson Prentice Hall, 4th Edition.
6. R. P. Jain, "*Modern Digital Electronics*," Tata McGraw Hill, 3rd Edition.
7. Ron Mancini, "*Op Amps for Everyone*", Newnes, 2nd Edition.
8. J. Millman and A. Grabel, "*Microelectronics*", Tata McGraw Hill, 2nd Edition.
9. R. F. Coughlin and F. F. Driscoll, "*Operation Amplifiers and Linear Integrated Circuits*", Prentice Hall, 6th Edition.
10. J. G. Graeme, G. E. Tobey and L. P. Huelsman, "*Operational Amplifiers- Design & Applications*", NewYork: McGraw-Hill, Burr-Brown Research Corporation.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered for final internal assessment.

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory preferably objective type and based on entire syllabus.
4. Remaining questions (Q.2 to Q.6) will be selected from all the modules.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELX DLO5011	Database Management System	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ELX DLO5011	Database Management System	20	20	20	80	--	--	--	100

Prerequisite:

Basic knowledge of Data structure.

Course objectives:

1. Learn and practice data modelling using the entity-relationship and developing database designs.
2. Understand the use of Structured Query Language (SQL) and learn SQL syntax.
3. Apply normalization techniques to normalize the database
4. Understand the needs of database processing and learn techniques for controlling the consequences of concurrent data access.

Course outcomes: On successful completion of course learner will be able to:

1. Understand the fundamentals of a database systems
2. Design and draw ER and EER diagram for the real life problem.
3. Convert conceptual model to relational model and formulate relational algebra queries.
4. Design and querying database using SQL.
5. Analyze and apply concepts of normalization to relational database design.
6. Understand the concept of transaction, concurrency and recovery.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction Database Concepts:**	**4**
	1.1	Introduction, Characteristics of databases File system v/s Database system Users of Database system	4

	1.2	Data Independence DBMS system architecture Database Administrator	
2.0		**Entity–Relationship Data Model**	**8**
	2.1	The Entity-Relationship (ER) Model: Entity types : Weak and strong entity sets, Entity sets, Types of Attributes, Keys, Relationship constraints : Cardinality and Participation, Extended Entity-Relationship (EER) Model : Generalization, Specialization and Aggregation	
3.0		**Relational Model and relational Algebra**	**8**
	3.1	Introduction to the Relational Model, relational schema and concept of keys. Mapping the ER and EER Model to the Relational Model	
	3.2	Relational Algebra – unary and set operations , Relational Algebra Queries.	
4.0		**Structured Query Language (SQL)**	**12**
	4.1	Overview of SQL Data Definition Commands, Data Manipulation commands, Data Control commands, Transaction Control Commands.	
	4.2	Set and string operations, aggregate function - group by, having.Views in SQL, joins , Nested and complex queries, Integrity constraints :- key constraints, Domain Constraints, Referential integrity , check constraints	
	4.3	Triggers	
5.0		**Relational–Database Design**	**8**
	5.1	Pitfalls in Relational-Database designs , Concept of normalization Function Dependencies , First Normal Form, 2nd , 3rd , BCNF, multi valued dependencies , 4NF.	
6.0		**Transactions Management and Concurrency**	**12**
	6.1	Transaction concept, Transaction states, ACID properties Concurrent Executions, Serializability – Conflict and View, Concurrency Control: Lock-based, Timestamp-based protocols.	

	6.2	Recovery System: Failure Classification, Log based recovery, ARIES, Checkpoint, Shadow paging. Deadlock handling	
		Total	**52**

Text Books:

1. G. K. Gupta "Database Management Systems", McGraw – Hill.
2. Korth, Slberchatz,Sudarshan, "Database System Concepts", 6th Edition, McGraw – Hill
3. Elmasri and Navathe, "Fundamentals of Database Systems", 5th Edition, Pearson education.
4. Peter Rob and Carlos Coronel, "Database Systems Design, Implementation and Management", Thomson Learning, 5th Edition.

Reference Books:

1. Dr. P.S. Deshpande, SQL and PL/SQL for Oracle 10g, Black Book, Dreamtech Press.
2. Gillenson, Paulraj Ponniah, " Introduction to Database Management", Wiley Publication.
3. Sharaman Shah, "Oracle for Professional", SPD.
4. Raghu Ramkrishnan and Johannes Gehrke, " Database Management Systems ",TMH.

Internal Assessment:

Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approx. 40% syllabus is completed and second class test when additional 40% syllabus is completed. Duration of each test shall be one hour.

End Semester Theory Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELX DLO5012	Digital Control Systems	04	--	--	04	--	--	04

Course Code	Course Name	Examination Scheme								
		Theory					Term work	Pract.	Oral	Total
		Internal Assessment			End sem	Duration (hrs)				
		Test 1	Test 2	Avg						
ELX DLO5012	Digital Control Systems	20	20	20	80	03	--	--	--	100

Course Pre-requisite: ELX301: Mathematics III , ELX401: Mathematics IV, ELX406: Linear Control Systems

Course Objectives:

1. To introduce the discrete-time systems theory.
2. To introduce Z-transform methods in digital systems design.
3. To introduce modern state-space methods in digital systems design.

Course Outcomes : At the end of the course, the learner will have the ability to

1. Justify the need for digital control systems as well as understand sampling and reconstruction of analog signals.
2. Model the digital systems using various discretization methods and understand the concept of Pulse Transfer Function.
3. Analyze the digital control systems using classical techniques.
4. Analyze the digital control systems using modern state-space techniques.
5. Understand the concept of controllability and design the state feedback controllers.
6. Understand the concept of observability and design the state observers.

Module		Contents	Time
1.		**Basics of discrete-time signals and discretization**	06
	1.1	Why digital control system? Advantages and limitations, comparison of continuous and discrete data control, block diagram of digital control system.	
	1.2	Impulse sampling. Nyquist-Shannon sampling theorem, reconstruction of discrete-time signals (ideal filter)	
	1.3	Realizable reconstruction methods (ZOH and FOH). Transfer function of ZOH and FOH.	
2.		**Modelling of Digital Control System**	10
	2.1	Discretization Approaches: Impulse invariance, step invariance, bilinear transformation, finite difference approximation of derivative.	
	2.2	Z-transform revision and its equivalence with starred Laplace transform.	
	2.3	The pulse transfer function (PTF) and general procedures to obtain PTF.	
3.		**Stability Analysis and Controller Design via Conventional Methods**	12
	3.1	Mapping between s-plane and z-plane, stability analysis of digital systems	

		in z-plane. Effects of sampling frequency on stability.	
	3.2	Transient and steady-state analysis of time response, digital controller design using root-locus method.	
	3.3	Digital controller design using bode plots, digital PID controller.	
	3.4	Realization of digital controllers: direct programming, standard programming, series programming, parallel programming, ladder programming,	
4.		**State Space Analysis of Discrete-time Systems**	**08**
	4.1	Revision of continuous-time state-space models. Solution of continuous-time state-space equation. Discretization of continuous-time state-space solution and discrete-time state-space model.	
	4.2	Various canonical state-space forms for discrete-time systems and transformations between state-space representations.	
	4.3	Solution of discrete-time state-space equation. Computation of state-transition matrix (z-transforms, Caley-Hamilton theorem, Diagonalization).	
5.		**Controllability and State Feedback Controller Design**	**06**
	5.1	Concept of controllability. Distinction between reachability and controllability in discrete-time systems.	
	5.2	Digital controller design using pole-placement methods. (Similarity transforms, Ackerman's formula).	
6.		**Observability and Observer Design**	**06**
	6.1	Concept of observability. Distinction between detectability and observability in discrete-time systems.	
	6.2	Observer design (prediction observer and current observer). Output feedback controller design. Introduction to separation principle.	
	6.3	Dead-beat controller design, dead-beat observer design.	
Total			**48**

Text books:

1. **Ogata Katsuhiko**, "Discrete-time Control Systems", Pearson, 2nd Edition, 1995.
2. **M. Gopal**, "Digital Control and State Variable Methods", Tata McGrow-Hill, 3rd Edition, 2003.

Reference Books:

1. **Gene Franklin, J. David Powell, Michael Workman**, "Digital Control of Dynamic Systems", Addison Wesley, 3rd Edition, 1998.
2. **B. C. Kuo**, "Digital Control Systems", Oxford University press, 2nd edition, 2007.
3. **Chi-Tsong Chen**, "Linear System Theory and Design", Oxford University Press, USA, 1998.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered for final Internal Assessment.

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining questions will be selected from all the modules.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELX DLO5013	ASIC Verification	04	--	--	04	--	--	04

Course Code	Course Name	Examination Scheme								
		Theory					Term work	Pract.	Oral	Total
		Internal Assessment			End sem	Duration (hrs)				
		Test 1	Test 2	Avg						
ELX DLO5013	ASIC Verification	20	20	20	80	03	--	--	--	100

Course Pre-requisite: EXC303: Digital Circuits and Design, ELXL304: Object Oriented Programming Methodology Laboratory, ELX 404: Digital System Design

Course Objectives

1. To introduce the learner System Verilog concepts for verification.
2. To introduce the learner advanced verification features such as practical use of classes, randomization, checking and coverage.
3. To highlight the significance of verification in VLSI industry.

Course Outcomes

At the end of the course, the learner will have the ability to

1. Demonstrate an understanding of programmable devices and verification methodologies.
2. Exploit new constructs in SV and advanced ASIC verification techniques.
3. Create test benches for digital designs in system verilog.
4. Carry out verification of design successfully using simulators

Module		Contents	Time
1.		**Programmable Devices and Verilog**	08
	1.1	**Programmable Devices:** Architecture of FPGA, CPLD with an example of Virtex-7 and Spartan -6 family devices	
	1.2	**Verilog HDL:** Data types, expressions, assignments, behavioural, gate and switch level modelling, tasks and functions	
2.		**Verification Basics and Data Types**	12
	2.1	**Verification Basics:** Technology challenges, Verification methodology options, Test bench creation, test bench migration, Verification languages, Verification IP reuse, Verification approaches, Layered Testbench, Verification plans	
	2.2	**Data Types:** Built in, Fixed size array, dynamic array, queues, associative array, linked list, array methods, choosing a storage type, creating new types with typedef, creating user defined structures, type conversion, enumerated types, constants, strings, expression width	
3.		**Procedural statements, test bench and Basic OOP**	12
	3.1	**Procedural Statements and Routines:** Procedural statements, tasks, functions and void functions, task and function overview, routine arguments, returning from a	

		routine, local data storage, time values **Connecting the Test bench and Design:** Separating the test bench and design, the interface construct, stimulus timing, interface driving and sampling, connecting it all together, top level scope, program-module interactions	
	3.2	**Basic OOP:** Class, Creating new objects, Object deal location, using objects, variables, class methods, defining methods outside class, scoping rules, using one class inside another, understanding dynamic objects, copying objects, public vs. local, building a test bench	
4.		**Randomization and IPC**	**10**
	4.1	**Randomization:** Randomization in system Verilog, constraint details, solution probabilities, controlling multiple constraint blocks, valid constraints, In-line constraints, The pre-randomize and post-randomize functions, Random number functions, Constraints tips and techniques	
	4.2	**Threads and Inter process Communication:** working with threads, disabling threads, inter process communication, events, semaphores, mailboxes, building a test bench with threads and IPC	
5.		**Assertions and Functional Coverage**	**06**
	5.1	**System Verilog Assertions:** Assertions in verification methodology, Understanding sequences and properties	
	5.2	**Functional Coverage:** Coverage types, strategies, examples, anatomy of a cover group, triggering a cover group, data sampling, cross coverage, generic cover groups, coverage options	
		Total	**48**

Text books:

1. **Chris Spear**, "System Verilog for Verification: A guide to learning the testbench language features", Springer, 3rd Edition.
2. **Janick Bergeron**, "Writing Testbenches Using System Verilog", Springer 2006.
3. **Stuart Sutherland, Simon Davidmann, and Peter Flake**, "System Verilog for Design:

   A guide to using system verilog for hardware design and modeling", Springer, 2nd Edition.

Reference Books:

1. Ben Cohen, Srinivasan Venkataramanan, Ajeetha Kumari and Lisa Piper, "SystemVerilog Assertions Handbook", VhdlCohen Publishing, 3rd edition
2. S Prakash Rashinkar, Peter Paterson and Leena Singh, "System on Chip Verification Methodologies and Techniques", Kluwer Academic, 1st Edition.
3. System Verilog Language Reference manual
4. Samir Palnitkar, "Verilog HDL: A guide to Digital Design and Synthesis" second edition,Pearson – IEEE 1364-2001 compliant.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered for final Internal Assessment.

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining questions will be selected from all the modules.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELX DLO5014	Biomedical Instrumentation	04	02	--	04	--	--	04

Course Code	Course Name	Examination Scheme									
		Theory					Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem	Duration (hrs)					
		Test 1	Test 2	Avg							
ELX DLO5014	Biomedical Instrumentation	20	20	20	80	03	--	--	--	--	100

Course Objectives

1. Introduce the learners to basic physiology and function of various systems in human body.

2. Introduce the learners to Diagnostic, Pathology, Life supportive equipment and latest imaging modalities in hospitals and healthcare industry.

3. Motivate learners to take up live projects with medical applications which will benefit the society at large.

Course Outcomes

- Have basic knowledge about the basic structure and functions of parts of cell, generation of action potential and various bioelectric potentials.
- Builds foundation of knowledge of physiological processes such as respiratory, cardiovascular, nervous and muscular systems in human body.
- Compare various methods used for measurement of various cardiac parameters such as blood pressure, blood flow, blood volume, cardiac output and heart sounds.
- Know the basic principle of analytical instruments and will have an over view of pathology laboratory equipments such as colorimeter, spectrophotometer, blood cell counter and auto-analyser.
- Have knowledge of life support equipments such as pacemaker, defibrillator, Heart lung machine, Haemodialysis machine and baby incubator along with safety limits of micro and macro shocks and understand the importance of electrical safety in hospital equipments.
  Have knowledge of imaging modalities such as X-ray, CT, MRI and Ultrasound.

Module		Contents	Time
1.		**Bio-Potential measurements**	06
	1.1	**Human Cell** Structure of Cell, Origin of Bio-potentials, Generation of Action Potentials,.	
	1.2	**Electrodes** Electrode-Electrolyte interface and types of bio-potential electrodes	
2.		**Physiological Systems and Related Measurement**	12
	2.1	**Cardiovascular system** Structure of Heart, Electrical and Mechanical activity of Heart, ECG measurements and Cardiac arrhythmias, Design of ECG amplifier, Heart sounds measurement.	

	2.2	**Nervous system** CNS and PNS: Nerve cell, Neuronal Communication, Generation of EEG and its measurement. Normal and abnormal EEG, Evoked potential. Electroencephalography: EEG measurements, Electrode-placement and Block diagram of EEG machine	
	2.3	**Respiratory system** Physiology of respiration and measurements of respiratory related parameters like respiration rate, Lung Volumes and capacities	
	2.4	**Muscular system** Typical Muscle fibre Action potential Electromyography: EMG measurement and block diagram.	
3.		**Cardio-Vascular measurements**	**08**
	3.1	Blood Pressure- Direct and Indirect types.	
	3.2	Blood Flow- Electromagnetic and Ultrasonic type.	
	3.3	Blood Volume- Plethysmography: Impedance, Capacitive and Photoelectric type	
	3.4	Cardiac Output- Fick's method, Dye-dilution and Thermo-dilution type.	
4.		**Analytical equipment**	**05**
	4.1	Beer Lambert's law, Principle of photometry.	
	4.2	Photo-colorimeter : Optical diagram	
	4.3	Spectrophotometer : Optical diagram	
	4.5	Blood cell counter : Coulter’s counter	
	4.6	Auto-analyser : Schematic diagram	
5.		**Life-saving and Support equipment**	**09**
	5.1	Pacemaker- Types of Pacemaker, Modes of pacing and its applications.	
	5.2	Defibrillator-Types of fibrillations, Modes of operation, DC Defibrillators and their applications.	
	5.3	Heart-Lung machine: System-flow diagram and its Application during surgery.	
	5.4	Haemodialysis machine: Principle of operation and System-flow diagram.	
	5.5	Baby Incubator and its applications	
	5.6	**Patient safety** Physiological effects of electrical current, Shock Hazards from electrical equipments and methods of accident prevention	
6.		**Imaging techniques**	**08**
	6.1	X-Ray- Generation, X-ray tube and its control, X-ray machine and its applications	
	6.2	CT Scan- CT Number, Block Diagram, scanning system and applications.	
	6.3	MRI- Concepts and image generation, block diagram and its applications	
	6.4	Ultrasound Imaging- Modes of scanning and their applications	
		Total	**48**

Text books:

1. Handbook of Biomedical Instrumentation: R S. Khandpur. (PH Pub)
2. Medical Instrumentation, Application and Design: J G. Webster. (John Wiley)
3. Introduction to Biomedical Equipment Technology: Carr –Brown. (PH Pub)

Reference Books:

1. Encyclopedia of Medical Devices and Instrumentation: J G. Webster. Vol I- IV (PH Pub)
2. Various Instruments Manuals.
3. Various internet resources.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered for final Internal Assessment.

End Semester Examination:

Question paper will comprise of 6 questions, each carrying 20 marks.
The Learners need to solve total 4 questions.
Question No.1 will be compulsory and based on entire syllabus.
Remaining question (Q.2 to Q.6) will be selected from all the modules.

Course Code	Course Name	Teaching scheme			Credit assigned			
ELXL 501	Microcontrollers & Applications Laboratory	Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
		--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme									
		Theory					Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem	Duration (hrs)					
		Test 1	Test 2	Avg.							
ELXL501	Microcontrollers &Applications Laboratory	--	--	--	--	--	25	--	--	25	50

Assessment:

Term Work:

At least **SIX** experiments based on the entire syllabus of **ELX 501 (Microcontrollers and Applications)** should be set to have well predefined inference and conclusion. Computation/simulation based experiments are also encouraged. The experiments should be students' centric and attempt should be made to make experiments more meaningful, interesting and innovative. Term work assessment must be based on the **overall performance** of the student with **every experiment graded from time to time. Term work must include a mini project in addition to the number of experiments. The course mini-project is to be undertaken in a group of two to three students.**

The grades should be converted into marks as per the **Credit and Grading System** manual and should be **added and averaged**. The grading and term work assessment should be done based on this scheme.

The final certification and acceptance of term work ensures satisfactory performance of laboratory work, mini project and minimum passing marks in term work. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed students well in advanced. Practical and Oral exam will be based on the entire syllabus.

Suggested experiments:

- Maximum three experiments in X – 51 assembly programming involving arithmetic, logical, Boolean, code-conversion etc operations.
- Minimum three experiments on interfacing of X – 51 based system with peripheral IC's (ADCs, DACs etc) peripheral actuators (relays, motors etc.) sensors (temperature, pressure etc.).

Suggested mini projects:

- Interfacing single LED/seven-segment display(SSD)/multiple-SSD with refreshing along-with some additional functional feature.
- Interfacing dot matrix LED for message display/ rolling message display.

- Interfacing IR emitter/receiver pair for time-period/speed calculations.
- Interfacing single key/4 – key/4 X 4 matrix keyboard with some additional functional feature.
- Motors – continuous, stepper, servo interfacing with speed(RPM) indication.
- Multi-function alarm clock using buzzer and LCD.
- Interfacing DAC and generating various waveforms.
- Ambient temperature indicator using LM 35 and 8-bit ADC 0808.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL 502	Digital Communication Laboratory	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXL 502	Digital Communication Laboratory	-	-	-	-	25	--	25	50

Laboratory Experiments:

Lab session includes Seven experiments and a Case study(Power point Presentation) on any one of the suggested topics.

1. The experiments will be based on the syllabus contents.

2. Minimum Seven experiments need to be conducted, out of which at least THREE should be software-based

(Scilab, MATLAB, LabVIEW, etc).

3. Each student (in groups of 3/4) has to present a Case study (Power point Presentation) as a part of the laboratory work.

The topics for Presentation / Case-study may be chosen to be any relevant topic on emerging technology.

("Beyond the scope of the syllabus".) Power point presentation should contain minimum of 15 slides and students should submit a report , (PPT+REPORT carry minimum of 10 marks

The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed students well in advanced.

Suggested experiments based on Laboratory setups:

1. Line codes
2. Binary modulation techniques: BASK,BPSK,BFSK
3. M-ary modulation techniques: QPSK ,QAM
4. MSK

Suggested experiments based on software:

1. Simulation of PDF& CDF of Raleigh / Normal/ Binomial Distributions
2. Simulation of Eye pattern for PAM signal
3. Source encoding: Huffman coding for Binary symbols

4. Simulation of Shannon-Hartley equation to find the upper limit on the Channel Capacity
5. Channel Encoding: Linear Block code : code generation, Syndrome
6. Cyclic code-code generation, Syndrome
7. Channel encoding: Convolutional code-code generation from generator sequences
8. Simulation of BPSK/QPSK/BFSK Modulation
9. Simulation of Duo-binary encoder-decoder
10. Plot and compare BER curves for Binary/ M-ary modulation schemes
11. Simulation of error performance of a QPSK/BPSK/MSK Modulator

Suggested topics for presentation:

1. DTH
2. Digital Multiplexing
3. Satellite Launching vehicles: PSLV, GSLV
4. Digital TV
5. Digital Satellite system: VSAT
6. RFID

Any other related and advanced topics.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL504	Design With Linear Integrated Circuits Laboratory	-	2	--	-	01	--	01

Course Code	Course Name	Examination Scheme							
		Theory Marks				Term Work	Practical and Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ELXL504	Design With Linear Integrated Circuits Laboratory	--	--	--	--	25	25		50

Term Work:

At least Six experiments based on the entire syllabus of Course ELX504 **(Design with Linear Integrated Circuits)** should be set to have well predefined inference and conclusion. Few computation/simulation based experiments are encouraged. The experiments should be students' centric and attempt should be made to make experiments more meaningful, interesting and innovative. Term work assessment must be based on the **overall performance** of the student with **every experiment graded from time to time**. The grades should be converted into marks as per the **Credit and Grading System** manual and should be **added and averaged**. The grading and term work assessment should be done based on this scheme.

A mini project based on the following topic or additional real time applications are encouraged. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed students well in advanced. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus.

Suggested List of Experiments:

1. Experiment on op amp parameters
2. Experiment on design of application using op amp (Linear)
3. Experiment on implementation of op amp application e.g. oscillator
4. Experiment on non linear application (e.g. comparator) of op amp
5. Experiment on non linear application (e.g. peak detector) of op amp
6. Experiment on ADC interfacing
7. Experiment on DAC interfacing
8. Experiment on IC 555

9. Experiment on voltage regulator (Design)
10. Experiment on implementation of instrumentation system (e.g. data acquisition).
    The topic for the mini project in the course based on the syllabus of ELX505(Design with Linear Integrated Circuits)need to be application oriented.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELXL DLO5011	Database Management Systems Laboratory	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme								
		Theory				Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem					
		Test 1	Test 2	Avg						
ELXL DLO5011	Database Management Systems Laboratory	--	--	--	--	25	--	25		50

At least **eight experiments** based on the entire syllabus of **ELXDLO5011 (Data Base Management System)** should be set to have well-defined inference and conclusion. The experiments should be student-centric, and attempt should be made to make experiments more meaningful, interesting and innovative. Experiment must be graded from time to time. Additionally, each student (in group of 2/3) must perform a Mini Project as a part of the laboratory and report of mini project should present in laboratory journal. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Oral exam will be based on the entire syllabus. Equal weightage should be given to laboratory experiments and project while assigning term work marks. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed students well in advanced.

Suggested List of Experiments

Expt. No	Title of the Experiments
1	To analyse the sampling and reconstruction of analog signal.
2	To study various discretization approaches (Impulse Invariance, Step Invariance, Bilinear Transformation)
3	Study of time domain transient and steady-state performance and performance specifications.
4	Digital controller design using Root-locus method.
5	Modelling of discrete-time systems in state-space and conversion to various canonical forms.
6	Discrete-time system simulation in Simulink.
7	Study digital PID controller and its implementation in MATLAB and Simulink.
8	Controllability and Observability of discrete-time systems.

9	Pole placement controller design for discrete-time systems.
10	Design of deadbeat controller and observer.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELXL DLO5013	ASIC Verification	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme								
		Theory				Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem					
		Test 1	Test 2	Avg						
ELXL DLO5013	ASIC Verification	--	--	--	--	25	--	25		50

At least **eight** experiments based on the entire syllabus of **ELXDLO5013 (ASIC Verification)** should be set to have well-defined inference and conclusion. The experiments should be student-centric and attempt should be made to make experiments more meaningful, interesting and innovative. Experiment must be graded from time to time. Additionally, each student (in group of 2/3) has to perform a Mini Project as a part of the laboratory and report of mini project should present in laboratory journal. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Oral exam will be based on the entire syllabus. Equal weightage should be given to laboratory experiments and project while assigning term work marks. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed students well in advanced.

List of Experiments:

1. Implementation of 4:1 Multiplexer in Verilog with
    a. Gate level Modeling
    b. Structural/ Dataflow Modeling
    c. Behavioral Modeling
2. Implementation of D flip flop (Asynchronous/ Synchronous/latch) using Verilog.
3. Experiment to practice creating dynamic arrays, associative arrays, and queues (Test a synchronous 8-bit x64K (512kBit) RAM).
4. Write a test plan and test bench for ALU Design.
5. Experiment to practice Procedural Statements and Routines using tasks, functions and do-while loops.
6. Create Interfaces to connect the Test bench and Design.
7. Threads & IPC: Implement the following counters
    i. UP counter
    ii. DOWN counter
    iii. Divide by 2 count As threads. Use Fork join, fork join_none, fork_joinany.
8. Threads & IPC - create dynamic processes (threads) and get familiar with interprocess communication using events, semaphore and mailb
9. Functional Coverage - write cover groups and get familiar with the coverage repor
   Verification of FIFO

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELXL DLO5013	Biomedical Instrumentation	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme								
		Theory				Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem					
		Test 1	Test 2	Avg						
ELXL DLO5013	Biomedical Instrumentation	--	--	--	--	25	--	25		50

At least **eight** experiments based on the entire syllabus of **ELXDLO5014 (Biomedical Instrumentation)** should be set to have well-defined inference and conclusion. The experiments should be student-centric and attempt should be made to make experiments more meaningful, interesting and innovative. Experiment must be graded from time to time. Additionally, each student (in group of 2/3) has to perform a Mini Project as a part of the laboratory and report of mini project should present in laboratory journal. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Oral exam will be based on the entire syllabus. Equal weightage should be given to laboratory experiments and project while assigning term work marks. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed students well in advanced.

Suggested List of Experiments

Expt. No.	Title of the Experiments
1	Study of X-ray Tubes
2	Design of active notch filter for line frequency
3	Design of general purpose amplifier for Bio potential measurement.
4	Design of Pacemaker using 555 timer.
5	Demonstration of Blood pressure measurement.
6	Demonstration of Electrocardiogram recording.
7	Demonstration of Electroencephalogram recording.
8	Demonstration of Electromyogram recording.
9	Demonstration of Photo-Colorimeter.
10	Demonstration of Spectrophotometer.

11	Demonstration of Auto-analyser.
12	Demonstration of Blood Cell counter.
13	Demonstration of D C Defibrillator (proto type).
14	Demonstration of Baby Incubator.
15	Demonstration of X Ray machine.
16	Demonstration of CT scanner.
17	Demonstration of MRI machine.
18	Demonstration of Ultrasound machine.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELX 601	Embedded Systems& Real Time Operating System	04	--	--	04	--	--	04

Course Code	Course Name	Examination Scheme									
		Theory					Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem	Duration (hrs)					
		Test 1	Test 2	Avg							
ELX 601	Embedded Systems& Real Time Operating System	20	20	20	80	03	--	--	--	--	100

Course Objectives

To study concepts involved in embedded hardware and software for systems realisation.

Course Outcomes At the end of the course, the learner will have the ability to

1. Identify and describe various characteristic features and applications of embedded systems.
2. Analyse and identify hardware for embedded systems implementation.
3. Analyse and identify various software issues involved in Embedded systems for real time requirements.
4. Analyse and explain the design life-cycle for embedded system implementation.

Module		Contents	Time
1.		**Introduction to Embedded Systems**	**04**
	1.1	Characteristics and Design metrics of Embedded system.	
	1.2	Real time systems:Need for Real-time systems, Hard-Soft Real-time systems.	
	1.3	Challenges in Embedded system Design: Power, Speed and Code density.	
		Embedded Hardware	**12**
2.	2.1	Embedded cores, Types of memories, Sensors (Optical encoders, Resistive) and Actuators (Solenoid valves, Relay/switch, Opto-couplers)	
	2.2	Power supply considerations in Embedded systems: Low power features- Idle & Power down mode, Sleep mode, Brown-out detection.	
	2.3	Communication Interfaces: Comparative study of serial communication interfaces (RS-232, RS-485), I2C, CAN, USB (v2.0), Bluetooth, Zig-Bee. Selection criteria of above interfaces. **(Frame formats of above protocols are not expected)**	
3.		**Embedded Software**	**14**
	3.1	Program Modelling concepts: DFG,FSM,UML	
	3.2	Embedded C-programming concepts (from Embedded system point of view): Data types, Modifiers, Qualifiers, Functions, Macros, Interrupt service routine, Device drivers.	
	3.3	Real-time Operating system: Need of RTOS in Embedded system software and comparison with GPOS, Foreground/Background processes, Interrupt latency, Task, Task states, Multi-tasking, Context switching, Task scheduling, Scheduling algorithms-Rate Monotonic Scheduling, Earliest Deadline First (with numericals), Inter-process communication: Semaphore, Mailbox, Message queues, Event timers, Task synchronisation- Shared data, Priority inversion, Deadlock. Memory Management	
	3.4	Introduction to µCOS II RTOS: Study of Kernel structure of µCOS II, µCOS II functions for Initialisation, Task creation, Inter-task communication and Resource management, Memory management	**08**
4.		**System Integration , Testing and Debugging Methodology**	**04**
	4.1	Embedded Product Design Life-Cycle (EDLC)	
	4.2	Hardware-Software Co-design	
	4.3	Testing & Debugging: Boundary-scan/JTAG interface concepts, Black-Box testing, White-Box testing, Hardware emulation, Logic analyser.	
5.		**Case Studies**	**06**
	5.1	Soft Real-time: Automatic Chocolate Vending machine using µCOS II RTOS- Requirements study, Specification study using UML, Hardware architecture, Software architecture	
	5.2	Hard Real-time: Car Cruise-Control using µCOS II RTOS- Requirements study, specification study using UML, Hardware architecture, Software Architecture	

Text books:

1.Dr. K.V. K. K. Prasad, "Embedded Real Time System: Concepts, Design and Programming", Dreamtech, New Delhi, Edition 2014.
2.Jean J. Labrosse,"MicroC / OS-II The Real-Time Kernel", CMP Books, 2011, Edition 2nd.
3. Rajkamal, "Embedded Systems: Architecture, Programming and Design", McGraw Hill Education (India) Private Limited, New Delhi, 2015, Edition 3rd.
4. SriramIyer, Pankaj Gupta,"Embedded Real Time Systems Programming", Tata McGraw Hill Publishing Company ltd., 2003.

Reference Books:

1.DavidSimon,"An Embedded Software Primer", Pearson, 2009.
2.Jonathan W. Valvano, "Embedded Microcomputer Systems – Real Time Interfacing", Publisher - Cengage Learning, 2012 Edition 3rd.
3.AndrewSloss, DomnicSymes, Chris Wright,"ARM System Developers Guide Designing and Optimising System Software", Elsevier, 2004
4.FrankVahid, Tony Givargis,"Embedded System Design – A Unified Hardware/Software Introduction", John Wiley & Sons Inc., 2002.
5.Shibu K V, "Introduction to Embedded Systems", Tata McGraw Hill Education Private Limited, New Delhi, 2009.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total of 4 questions.
3. Question No.1 will be compulsory and based on the entire syllabus.
4. Remaining question (Q.2 to Q.6) will be set from all the modules.
5. Weightage of marks, commensurate with the time allocated to the respective module.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELX 602	Computer Communication and Networks	4	2	--	4	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX 602	Computer Communication and Networks	20	20	20	80	-	--	--	100

Course Pre-requisite: ELX405 Principles of Communication Engineering
ELX502 Digital Communication

Course Objectives:

The objectives of this course are to:

1. Introduce networking architecture and protocols
2. Understand the various layers and protocols in the TCP/IP model
3. Recognize different addressing schemes, connecting devices and routing protocols
4. Select the required protocol from the application layer protocols

Course Outcomes:

On successful completion of the course the students will be able to:

1.Demonstrate understanding of networking concepts and required protocols
2. Analyze the various layers and protocols of the layered architecture
3. Evaluate different addressing schemes, connecting devices and routing protocols
4.Appreciate the application layer protocols

Module No.	Unit No.	Topics	Hrs.
1.		**Introduction to Network Architectures, Protocol Layers, and Service models**	06
	1.1	Uses of computer networks. Topologies, LAN, MAN, WAN, Network topologies, **Addressing :** Physical / Logical /Port addressing, Protocols and Standards.	
	1.2	**Protocol Architecture:** Need of layered protocol architecture, Layers details of OSI, , Protocol Layers and Their Service Models	
	1.3	TCP/IP Model: Protocol suite, Comparison of OSI and TCP/IP	
2.		**Physical Layer**	08
	2.1	**Transmission Media:** Guided media like Coaxial, fiber, twisted pair, and Wireless media, Transmission Impairments. Interconnecting Devices: Hub, Bridges, Switches, Router, Gateway	
	2.2	**Data communication model :** DTE, DCE, RS-232D Interface , Null Modem , **Multiplexing :** FDM , Synchronous TDM , Statistical TDM, ADSL , xDSL, Cable Modem	
3.		**Data Link Control**	08
	3.1	**Data link services:** Framing, Flow control, Error control, ARQ methods, Piggybacking	
	3.2	**High Level Data Link Control (HDLC):** HDLC configurations, Frame formats, Typical frame exchanges.	
	3.3	**Medium Access Control Protocols**: ALOHA, Slotted ALOHA, CSMA, CSMA/CD	
4.		**Network Layer**	14
	4.1	**Switching**: Switched Communication networks, Circuit switching Networks, , Circuit switching Concepts, Packet switching Principles: Virtual circuit switching and Datagram switching	
	4.2	**Routing in Packet Switching Networks:** Characteristics, Routing strategies, Link state Routing versus Distance vector Routing. Least-Cost Routing Algorithms: Dijkstra's Algorithm, Bellman Ford Algorithm.	
	4.3	**Internet Protocol:** Principles of Internetworking: Requirements, Connectionless Operation Internet Protocol Operation: IP packet, IP addressing, subnet addressing , IPv4, ICMP, ARP, RARP IPv6 (IPv6 Datagram format, comparison with IPv4, and transition from IPv4 to IPv6)	
5.		**Transport Layer & Application Layer**	08
	5.1	**Connection –oriented Transport Protocol Mechanisms: Transmission Control Protocol (TCP):** TCP Services, TCP Header format, TCP three way handshake, TCP state transition diagram.	

		User datagram Protocol (UDP)	
	5.2	**Congestion:** Effects of congestion, Congestion control methods, Traffic management, Congestion control in Packet switching Networks	
	5.3	**Application layer Protocols** : HTTP, FTP, DNS,SMTP, SSH	
6.		**LANs.** High speed Ethernet	**04**
	6.1	LAN Protocol architecture , LAN topologies, Hub, Bridges, Virtual LANs **Traditional Ethernet and IEEE 802.3 LAN Standard**: Ethernet protocol, Frame structure, Physical layers,	
	6.2	**High Speed Ethernet** : Fast Ethernet, Gigabit Ethernet & 10- Gigabit Ethernet	
		Total	**48**

Recommended Text Books

1. William Stallings, "Data and Computer communications", Pearson Education, 10th Edition.
2. Behrouz A. Forouzan, "Data communication and networking ", McGraw Hill Education, Fourth Edition.
3. Alberto Leon Garcia, "Communication Networks" , McGraw Hill Education, Second Edition

Reference books :

1. S. Tanenbaum, "Computer Networks", Pearson Education, Fourth Edition.
2. J. F. Kurose and K. W. Ross ,"Computer Networking: A Top-Down Approach", Addison Wesley, 5th Edition.

Internal Assessment (IA):
Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to marks will be asked.
4: Remaining question will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELX 603	VLSI Design	4	2	--	4	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX 603	VLSI Design	20	20	20	80	-	--	--	100

Prerequisite Subject:

- ELX302: Electronics Devices and Circuits- I
- ELX304: Digital Circuit Design
- ELX404: Digital System Design
- ELX504: Design with Linear Integrated Circuits

Course Objectives:

1. To study MOS based circuit realization using different design styles
2. To highlight the fundamental issues in data path and system level design

Course Outcomes: After successful completion of the course student will be able to …

1. Demonstrate a clear understanding of choice of technology, scaling, MOS models and system level design issues.
2. Design and analyze MOS based inverters.
3. Design MOS based circuits with different design styles.
4. Design semiconductor memories, adders and multipliers.

Unit No.	Details	Teaching Hours
1	**Technology Trend :** **1.1 Technology Comparison:** Comparison of BJT and MOS technology **1.2 MOSFET Scaling:** Types of scaling, Level 1 and Level 2 MOSFET Models, MOSFET capacitances	06
2	**MOSFET Inverters:** **2.1 Types of MOS inverters:** Active and passive load and their comparison. **2.2 Circuit Analysis of MOS Inverters:** Static Analysis resistive and CMOS inverter: Calculation of all critical voltages and noise margins. Design of symmetric CMOS inverter. Dynamic Analysis of CMOS inverter: Calculation of rise time, fall time and propagation delay **2.3Logic Circuit Design:** Analysis and design of 2-I/P NAND,NOR and complex Boolean function using equivalent CMOS inverter for simultaneous switching.	10
3	**MOS Circuit Design Styles:**	10

	3.1 Design Styles: Static CMOS, pass transistor logic, transmission gate, Pseudo NMOS, C^2MOS, Dynamic, Domino,NORA and Zipper. **3.2Circuit Realization:** Basic gates,SR Latch, JK FF, D FF, 1 Bit Shift Register, MUX using above design styles.	
4	**Semiconductor Memories:** **4.1 SRAM:** 6T SRAM, operation, design strategy, leakage currents, read/write circuits, sense amplifier. **4.2DRAM**: 1T_DRAM, operation modes, leakage currents, refresh operation, physical design. 4.3 **ROM Array**: NAND and NOR PROM, Nonvolatile read/write memories-classification and programming techniques	08
5	**Data Path Design:** **5.1 Adder:** CLA adder, MODL, Manchester carry chainand high speed adders like carryskip, carry select and carry save. **5.2 Multipliers and shifter:** Array multiplier and barrel shifter	04
6	**VLSI Clocking and System Design:** **6.1Clocking:** CMOS clocking styles, Clock generation, stabilization and distribution **6.2Low Power CMOS Circuits:** Various components of power dissipation in CMOS, Limits on low power design, low power design through voltage scaling **6.3I/O pads and Power Distribution:** ESD protection, input circuits, output circuits, simultaneous switching noise, power distribution scheme **6.4Interconnect:** Interconnect delay model, interconnect scaling and crosstalk.	10

Text and Reference Books

1.Sung-Mo Kang and Yusuf Leblebici, "*CMOS Digital Integrated Circuits Analysis and Design*", Tata McGraw Hill, 3rd Edition.

2. John P. Uyemura, "Introduction to VLSI CIRCUITS AND SYSTEMS",Wiley India Pvt. Ltd.

3. Jan M. Rabaey, Anantha Chandrakasan and BorivojeNikolic, "*Digital Integrated Circuits: A Design Perspective*", Pearson Education, 2nd Edition.

4. Etienne Sicard and Sonia Delmas Bendhia, "*Basics of CMOS Cell Design*", Tata McGraw Hill, First Edition.

5. Neil H. E. Weste, David Harris and Ayan Banerjee, "*CMOS VLSI Design: A Circuits and Systems Perspective*", Pearson Education, 3rd Edition.

6. Debaprasad Das, "*VLSI Design*", Oxford, 1st Edition.

7. Kaushik Roy and Sharat C. Prasad, "*Low-Power CMOS VLSI Circuit Design*", Wiley, Student Edition.

8. David A Hodges, Horace G Jackson and Resve A Saleh, "Analysis and Design of Digital Integrated Cicuits", TMH, 3rd Edition

Additional Study Material & e-Books

1.Douglas A Pucknell, Kamran Eshraghian , "Basic VLSI Design", Prentice Hall of India Private Ltd.

2.Samir Palnitkar, " A Guide to Digital Design and Synthesis", Pearson Education

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX604	Signals and Systems	04	--	#01	04	--	01	05

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX604	Signals and Systems	20	20	20	80	25	-	-	125

#Class wise

Course Objectives:

1. To provide a comprehensive coverage of continuous time and discrete time Signals and Systems.
2. To introduce various time domain and frequency domain methods for analysis of Signals and systems.

Course Outcomes:

After successful completion of this course student will be able to

1. Differentiate between continuous time and discrete time Signals and Systems.
2. Understand various transforms for time domain to frequency domain conversion
3. Apply frequency domain techniques for analysis of LTI systems
4. Apply frequency domain techniques for analysis of continuous and discrete signals

Module No.	Unit No.	Topics	Hrs.
1.		**Continuous and Discrete Time Signals**	8
	1.1	Mathematical Representation and Classification of CT and DT signals, Orthogonality of signals	
	1.2	Arithmetic operations on the signals, Time Shifting, Time scaling, Time Reversal of signals	
	1.3	Sampling and Reconstruction, Aliasing effect	
2		**Continuous and Discrete Systems**	8
	2.1	Mathematical Representation and classification of CT and DT systems	
	2.2	Properties of LTI systems, impulse and step response.	
	2.3	Use of convolution integral, convolution sum and correlation for analysis of LTI systems	
	2.4	Properties of convolution integral and convolution sum	
3		**Frequency Domain Analysis of Continuous Time System using Laplace Transform**	6
	3.1	Concept of Complex frequency, Region of Convergence for Causal, Non-causal and Anti-causal systems, Poles and Zero of transfer function	
	3.2	Unilateral Laplace Transform	
	3.3	Analysis and characterization of LTI system using Laplace Transform: Impulse and Step Response, Causality, Stability, Stability of Causal system	
4		**Frequency Domain Analysis of Discrete Time System using Z Transform**	12
	4.1	Need for Z transform, definition, properties of unilateral and bilateral Z Transform, mapping with s plane, relationship with Laplace transform	
	4.2	Z transform of standard signals, ROC, poles and zeros of transfer function, Inverse Z transform	
	4.3	Analysis and characterization of LTI system using Z transform: impulse and step response, causality, stability, stability of causal system	
	4.4	System realization-Direct, Direct Canonic, Cascade and Parallel forms	
5		**Frequency Domainc Analysis of Continuous Signals**	6
	5.1	Frequency Domain Analysis of periodic non-sinusoidal signals	
	5.2	Frequency Domain Analysis of aperiodic Signals-Introduction, Properties of Fourier Transform, Fourier Transform based amplitude and phase response of standard signals, Relationship with Laplace and Z transform, Energy Spectral	
6		**Frequency Domain Analysis of Discrete Signals**	8
	6.1	Discrete Time Fourier Series, Evaluation of DTFS coefficients, Magnitude and Phase Spectrum of Discrete time periodic signals, Power Spectral Density	
	6.2	Discrete Time Fourier Transform – Concept of discrete time signal in frequency domain, definition of DTFT, determination of magnitude and phase functions using DTFT	
		Total	**48**

Text Books:

1. Tarun Kumar Rawat, "*Signals and Systems*", Oxford University*Press 2016.*
2. A. NagoorKani, *"Signals and Systems"*, Tata McGraw-Hill Education

Reference Books:

1. John Proakis and DimitrisMonolakis, "*Digital Signal Processing*", Pearson Publication, 4th Edition
2. Alan V. Oppenheim, AlanS. Willsky, and S.Hamid Nawab, "*Signals and Systems*", 2nd Edition, PHIlearning,2010.
3. B. P. Lathi, "Linear Systems and Signals", Oxford University Press,

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to marks will be asked.
4: Remaining question will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX DLO6021	Microwave Engineering	04	--	#01	04	--	01	05

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX6021	Microwave Engineering	20	20	20	80	25	-	-	125

Prerequisites: Knowledge of basic Engineering Electromagnetics

Course Objectives:

1. To introduce the students to various concepts of Microwave Engineering.
2. To teach the students the working principles and applications of different microwave devices.

Course Outcomes (CO):

After successful completion of the course, students will be able to:

1. Understand the importance and applications of microwaves.
2. Explain the process of generation and amplification of microwaves.
3. Analyse the electromagnetic field distribution in various microwave components.
4. Measure various microwave parameters.

Module	Contents	Hours
1	**Introduction to microwave communication** 1.1 Microwave spectrum and bands 1.2 Limitations of conventional circuit theory concepts at microwave frequencies 1.3 Applications of microwaves 1.4 Limitations of conventional vacuum tubes at microwave frequencies	4
2	**Generation and amplification of microwaves** 2.1 **Two cavity Klystron amplifiers:** Construction , Process of velocity modulation and bunching , Apple gate diagram Output power and efficiency , Applications 2.2 **Reflex Klystron:** Construction ,Process of velocity modulation and bunching	12

	Apple gate diagram , Output power and efficiency Applications 2.3 **Cylindrical Magnetron** Construction and working principle Hull cut-off magnetic equation , Cyclotron angular frequency Applications 2.4 **Traveling wave tube**: construction and working principle applications 2.5 numerical examples based on the above topics	
3	**Waveguides:** 3.1 Rectangular and circular waveguides 3.2 solution of Maxwell's equation for distribution of fields in the waveguides 3.3 characteristic equation 3.4 Dominant and degenerate modes 3.5 group and phase velocities 3.6 cut-off frequency 3.7 numerical examples based on the above topics	10
4	**Waveguide components and analysis**: 4.1 Definition and significance of s-parameters 4.2 Properties of s-parameters 4.3 Construction, working principle and s-matrix representation of cavity resonators, waveguide attenuators, waveguide phase shifters, waveguide multiport junctions, E-plane and H-plane Tees, Magic Tee, Hybrid Ring, direction couplers 4.4 Microwave ferrite components: Faraday rotation isolator, Circulator, Gyrator Numerical examples based on the above topics	12
5	Microwave solid state devices: 5.1Principle of operation and characteristics of: Gunn Diode, TRAPATT and IMPATT diodes, Microwave Transistors 5.2 Introduction to Strip Lines	5
6	Microwave Measurement: Measurement of 6.1 Power 6.2 Attenuation 6.3 Frequency 6.4 VSWR 6.5 Cavity Q 6.6 Impedance	5

Text Books:

1. " Microwave Devices and Circuits" by Samuel Liao, PHI
2. "Microwave circuits and Passive Devices" by M L Sisodia, G S Raghuvanshi, New Age International(P) Ltd

Reference Books:

1. " Electronic Communication Systems" by Kennedy, Davis, 4e TMH
2. " Microwave Engineering: Passive Circuits" by Peter Rizzi, PHI
3. " Foundations for Microwave Engineering" by Robert E Collin, 2e, John Wiley
4. " Basic Microwave Techniques & Laboratory Manual" by M L Sisodia, G S Raghuvanshi, 2001 New Age International(P) Ltd
5. Microwave Engineering, Annapurna Das, TMH\

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to marks will be asked.
4: Remaining question will be selected from all the modules.

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELX DLO6022	Electronic Product Design	04	---	---	04	---	---	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Examination			
		Test I	Test II	Average				
ELX DLO6022	Electronic Product Design (EPD)	20	20	20	80	---	---	100

Rationale :- The aim of this course is to enable students to gain practical experience & nurture their creativity in electronic product design & the objective is to provide students with a clear understanding of the practical design problems of the electronic products at an introductory level. With this course, students are expected to become familiar with the concept of designing a product as per the requirements (non-technical) & given specifications (technical), component tolerances, production constraints, safety requirements & EMC standards.

Course Objectives:-

1. To understand the stages of product (hardware / software) design & development
2. To learn different considerations of analog, digital & mixed circuit design
3. To be acquainted with methods of PCB design & different tools used for the same
4. To be aware of the importance of testing in product design cycle
5. To gain knowledge about various processes & importance of documentation

Course Outcomes :-

At the end of the course, students should gain the ability to :-

- **CO-1 :-** Design electronic products using user-centered designing processes
- **CO-2 :-** Identify & recognize essential design & production procedures of electronic products
- **CO-3 :-** Implement a prototype for meeting a particular requirement / specification
- **CO-4 :-** Demonstrate problem solving & troubleshooting skills in electronic product design
- **CO-5 :-** Prepare the relevant set of design documentation & present it as a case study

Module No.	Topics	Hours
1	**INTRODUCTION TO ELECTRONIC PRODUCT DESIGN** Man-machine dialog & industrial design, user-centered design, elements of successful design, cognition, ergonomics, packaging & factors; design for manufacture, assembly & disassembly wiring, temperature, vibration & shock; safety, noise, energy coupling, grounding, earthing, filtering & shielding	06
2	**HARDWARE DESIGN & TESTING METHODS** Design process, identifying the requirements, formulating specifications, design specifications, system partitioning, functional design, architectural design, functional model v/s architectural model, prototyping, performance & efficiency measures, formulating a test plan, writing all the specifications, test procedures & test cases, design reviews, module debug & testing – black box testing, white box testing, grey box testing	10
3	**SOFTWARE DESIGN & TESTING METHODS** Types of software, the waterfall model of software development, models, metrics & software limitations, risk abatement & failure prevention, software bugs & testing, good programming practice, user interface, embedded & real-time software	10
4	**PRINTED CIRCUIT BOARD (PCB) DESIGNING** Fundamental definitions, standards, routing topology configuration, layer stack up assignment, grounding methodologies, aspect ratio, image planes, functional partitioning, critical frequency & bypassing, decoupling; design techniques for ESD protection, guard-band & guard-rings	08
5	**PRODUCT DEBUGGING & TESTING** Steps of debugging, the techniques for troubleshooting, characterization, electromechanical components, passive components, active components, active devices, operational amplifier, analog-to-digital conversion, digital components, inspection & testing of components, process of simulation, prototyping & testing, integration, validation & verification, EMI & EMC issues	08
6	**THE DOCUMENTATION PROCESS** Definition, needs & types of documentation, records, accountability & liability, audience, steps in preparation, presentation & preservation of documents, methods of documentation, visual techniques, layout of documentation, bills of materials, manuals – instructional or operating manual, service and maintenance manual, fault finding tree, software documentation practices	06
1 – 6	**TOTAL**	48

Recommended Books :-

1. R. G. Kaduskar & V. B. Baru, Electronic Product Design, 3rd edition, Wiley India
2. Kim Fowler, Electronic Instrument Design, 2nd edition, Oxford University Press
3. Robert J. Herrick, PCB Design Techniques for EMC Compliance, 2nd edition, IEEE Press
4. G. C. Loveday, Electronic Testing & Fault Diagnosis, 4th edition, A. H. Wheeler Publishing
5. James K. Peckol, Embedded Systems – A Contemporary Design Tool, 1st edition, Wiley Publication
6. J. C. Whitaker, The Electronics Handbook, CRC Press

Internal Assessment (IA) :-

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered as final IA marks.

End Semester Examination :-

1. Question paper will comprise of 6 questions, each carrying 20 marks.

2. The students need to solve total 4 questions.

3. Q.1 will be compulsory and based on entire syllabus.

4. Remaining questions (Q.2 to Q.6) will be set from all modules.

5. Weightage of each module in question paper will be proportional to the number of respective lecture hours mentioned in the syllabus

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELX DLO6023	Wireless Communication	4	2	--	4	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX DLO6023	Wireless Communication	20	20	20	80	-	--	--	100

Course Objectives:

The objectives of this course are to:

1. To introduce the Concepts of basic Cellular communication systems , mobile Radio propagation
2. To understand the various Cellular processes such as handoff strategies, interference, Trunking theory
3. To study the features and services of 2G cellular technologies: GSM and CDMA
4. To study the features of evolving technological advances in 2G, 3G & 4G Cellular systems.

Course Outcomes:

After successful completion of the course, students will be able to:

1. Understand the concepts of basic cellular system, frequency reuse, channel assignment
2. Understand the fundamentals radio propagation , Path loss and comprehend the effect of Fading .
3. Acquire the Knowledge about multiple access technologies and different of different spread spectrum techniques.
4. Acquire the Knowledge about overall GSM cellular concept and analyse its services and features
5. Comprehend the features of CDMA technology
6. Analyse the evolution of cellular technology from 2G to 4G Cellular systems .

Module No.	Unit No.	Topics	Hrs.
1.		**Concept of Cellular Communication**	**08**
	1.1	Introduction to cellular communications, Frequency reuse, Channel assignment strategies	
	1.2	**Cellular Processes:** Call setup, Handoff strategies, interference and system capacity, Co-channel Interference reduction with the use of Directional Antenna	
	1.3	**Traffic Theory:** Trunking and Grade of service, Improving Coverage and capacity in Cellular systems: Cell splitting, Sectoring, Micro-cell Zone concept	
2.		**Mobile Radio Propagation**	**08**

	2.1	Introduction to Radio wave propagation, Free space propagation model, the three basic Propagation mechanisms, The Ground Reflection (two-ray) model, Practical Link budget design using Path-Loss models:Log-distance Path –loss model.	
	2.2	Small scale Multipath Propagation: Factors influencing small scale fading, Doppler shift, Parameters of mobile multipath channels,	
	2.3	Types of small scale fading, Fading effects due to Doppler spread, Fading effects due to Multipath Time delay spread, Raleigh and Rician distributions	
3.0		Multiple access techniques & Spread spectrum Modulation	**08**
	3.1	Multiplexing and Multiple Access:Time Division Multiple Access, Frequency Division Multiple Access, Spread-spectrum multiple-access:Code Division Multiple Access	
	3.2	Spread spectrum Modulation :Need for and concept of spread spectrum modulation, PN-sequence generation, properties of PN-sequence, Gold sequence generation, Direct-sequence SS, Frequency-hopping SS,	
4.0		**GSM**	**12**
	4.1	GSM network architecture, Signalling protocol architecture, Identifiers, Physical and Logical Channels, Frame structure, Speech coding, Authentication and security, Call procedure, Hand-off procedure, Services and features	
5.0		**IS-95**	**06**
	5.1	Frequency and channel specifications of IS-95, Forward and Reverse CDMA channel, Packet and Frame formats, Mobility and Resource management	
6.0		**Evolution from 2G to 4G**	**06**
	6.1	GPRS, EDGE technologies, 2.5G CDMA-One cellular network, W-CDMA (UMTS), CDMA2000, LTE, Introduction to 5G Networks	
		Total	**48**

Recommended Books:

6. Theodore Rappaport, "Wireless Communications: Principles and Practice, 2nd Edition, Pearson Publication
7. ITI Saha Misra, "Wireless Communication and Networks: 3G and Beyond", Publication
8. Vijay Garg, "IS-95 CDMA and cdma 2000: Cellular/PCS System Implementation", Pearson Publication.

Reference Books:

1. T.L Singal , "Wireless Communication", Tata McGraw Hill ,2010
2. Upena Dalal , "Wireless Communication", Oxford University Press, 2009
3. Andreas F Molisch, "Wireless Communication", John Wiley, India 2006.
4. Vijay Garg, "Wireless communication and Networking", Pearson Publication

Internal Assessment (IA):
Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to marks will be asked.
4: Remaining question will be selected from all the modules.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELX DLO6024	Computer Organization and Architecture	04	--	--	04	--	--	04

Course Code	Course Name	Examination Scheme									
		Theory									
		Internal Assessment			End sem	Duration (hrs)	Term work	Pract.	Oral	Pract / Oral	Total
		Test 1	Test 2	Avg							
ELX DLO6024	Computer Organization and Architecture	20	20	20	80	03	--	--	--	--	100

Course Objectives	1. To introduce the learner to the design aspects which can lead to maximized performance of a Computer. 2. To introduce the learner to various concepts related to Parallel Processing 3.To highlight the various architectural enhancements in modern processors.
Course Outcomes	At the end of the course, the learner will have the ability to 1. Define the performance metrics of a Computer 2.Explain the design considerations of Processor, Memory and I/O in Computer systems 3. Explain the advantages and limitations of Parallelism in systems 4. Explain the various architectural enhancements in modern processors

Module		Contents	Time
1.		**Introduction to Computer Organization**	**[06]**
	1.1	Fundamental Units of a Computer	01
	1.2	Introduction to Buses	01
	1.3	Number Representation methods- Integer and Floating-point, Booth's Multiplier, Restoring and Non-Restoring Division	03
	1.4	Basic Measures of Computer Performance - Clock Speed, CPI, MIPs and MFlops	01
2.		**Processor Organization and Architecture**	**10**
	2.1	CPU Architecture , Register Organization, Instruction cycle, Instruction Formats	04
	2.2	Control Unit Design- Hardwired and Micro-programmed Control: Vertical and Horizontal Micro-Instructions, Nano-programming	04
	2.3	Comparison between CISC and RISC architectures	02
3.		**Memory Organization**	**12**
	3.1	Classification of Memories-Primary and Secondary Memories, RAM (SRAM and DRAM) and ROM (EPROM , EEPROM), Memory Inter-leaving	02
	3.2	Memory Hierarchy, Cache Memory Concepts, Mapping Techniques, Write Policies, Cache Coherency (* Numerical Problems expected)	06
	3.3	Virtual Memory Management-Concept, Segmentation , Paging, Page Replacement policies	04
4.		**Input/Output Organization**	**06**
	4.1	Types of I/O devices and Access methods, Types of Buses , Bus Arbitration	03
	4.2	Expansion Bus Concept, PCI Bus	03
5.		**Parallelism**	**06**
	5.1	Introduction to Parallel Processing Concepts, Flynn's classification, Amdahl's law	02
	5.2	Pipelining - Concept, Speedup, Efficiency , Throughput, Types of Pipeline hazards and solutions (* Numerical Problems expected)	04
6.		**Architectural Enhancements**	**08**
		Superscalar Architectures, Out-of-Order Execution, Multi-core processors, Clusters, Non-Uniform Memory Access (NUMA) systems, Vector Computation , GPU	08

Text books:

1.William Stallings, "*Computer Organization and Architecture: Designing for Performance*", Eighth Edition, Pearson.

2. C. Hamacher, Z. Vranesic and S. Zaky, "Computer Organization", McGraw Hill, 2002.

Reference Books:

1. J.P. Hayes, "Computer Architecture and Organization", McGraw-Hill, 1998.

2. B. Govindarajulu, "*Computer Architecture and Organization: Design Principles and Applications*", Second Edition, Tata McGraw-Hill.

3. D. A. Patterson and J. L. Hennessy, "Computer Organization and Design - The Hardware/Software Interface", Morgan Kaufmann,1998.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered for final Internal Assessment.

End Semester Examination:
Question paper will comprise of 6 questions, each carrying 20 marks.

The Learner need to solve total 4 questions.

Question No.1 will be compulsory and based on entire syllabus. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELXL 601	Embedded Systems& Real Time Operating System Laboratory	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme									
		Theory					Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem	Duration (hrs)					
		Test 1	Test 2	Avg							
ELXL 601	Embedded Systems& Real Time Operating System Laboratory	--	--	--	--	--	25	--	--	25	50

Assessment:

Term Work:

At least **SIX** experiments based on the entire syllabus of **ELX 601 (Embedded System & Real Time Operating System)** should be set to have well predefined inference and conclusion. Computation/simulation based experiments are also encouraged. The experiments should be students' centric and attempt should be made to make experiments more meaningful, interesting and innovative. Term work assessment must be based on the **overall performance** of the student with **every experiment graded from time to time. Term work must include a mini project in addition to the number of experiments. The course mini-project is to be undertaken in a group of two to three students.** The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

The grades should be converted into marks as per the **Credit and Grading System** manual and should be **added and averaged**. The grading and term work assessment should be done based on this scheme.

The final certification and acceptance of term work ensures satisfactory performance of laboratory work, mini project and minimum passing marks in term work.

Practical and Oral exam will be based on the entire syllabus.

Suggested Experiments:

- Simulation experiments using KeilC–cross complier to: evaluate basic C program for X-51 assembly; evaluating various C data types; evaluating and understanding iterative C constructs translated into x51's assembly; evaluating and understanding interrupt implementation.
- Simulate and understand working of µCOS-II functions using example programs from recommended text,"MicroC / OS-II The Real-Time Kernel", by Jean J. Labrosse.
- Porting of µCOS-II on X-51/AVR/CORTEX M3 platform.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL 602	Computer Communication and Networks Laboratory	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXL 602	Computer Communication and Networks Laboratory	-	-	-	-	25	--	25	50

Laboratory Experiments:

Lab session includes Seven experiments and a Case study(Power point Presentation) on any one of the suggested topics.

1. The experiments will be based on the syllabus contents.

2. Minimum **Seven experiments** need to be conducted, out of which **at least Four Experiments** should be software-based (C/C++ , Scilab, MATLAB, LabVIEW, etc).

3. Each student (in groups of 3/4) has to present a Case study (Power point Presentation) as a part of the laboratory work. The topics for Presentation / Case-study may be chosen to be any relevant topic on emerging technology. ("Beyond the scope of the syllabus".)

Power point presentation should contain minimum of 15 slides and students should submit a report (PPT+Report)carry minimum of 10 marks . The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Suggested List of experiments:

1. Study of transmission media and interconnecting devices of communication networks.
2. Implementation of serial transmission using RS232
3. Implementing bit stuffing algorithm of HDLC using C/C++
4. Implementation of Routing protocols using C/C++
5. Study of NS2 simulation software
6. Implementation of TCP/UDP session using NS2
7. Implementation of ARQ methods using NS2
8. Study of WIRESHARK and analyzing Packet using WIRESHARK
9. Study and implementation of IP commands
10. Study of GNS software and implementation of routing protocols using GNS

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELXL 603	VLSI Design Laboratory	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme									
		Theory									
		Internal Assessment			End sem	Duration (hrs)	Term work	Pract.	Oral	Pract. / Oral	Total
		Test 1	Test 2	Avg							
ELXL 603	VLSI Design Laboratory	--	--	--	--	--	25	--	--	25	50

Assessment:

Term Work:

At least **SIX** experiments based on the entire syllabus of **ELX 603 (VLSI Design)** should be set to have well predefined inference and conclusion. Computation/simulation based experiments are also encouraged. The experiments should be students' centric and attempt should be made to make experiments more meaningful, interesting and innovative. Term work assessment must be based on the **overall performance** of the student with **every experiment graded from time to time. Term work must include a mini project in addition to the number of experiments. The course mini-project is to be undertaken in a group of two to three students.** The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

The grades should be converted into marks as per the **Credit and Grading System** manual and should be **added and averaged**. The grading and term work assessment should be done based on this scheme.

The final certification and acceptance of term work ensures satisfactory performance of laboratory work, mini project and minimum passing marks in term work.

Practical and Oral exam will be based on the entire syllabus.

Suggested Experiments:

MOSFET Scaling using circuit simulation software like Ngspice
Static and transient performance analysis of various inverter circuits
Implementation of NAND and NOR gate using various logic design styles
Design and verification of CMOS Inverter for given static and transient performance
Implementation of ROM, SRAM, DRAM
Interconnect analysis

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELXL DLO6021	Microwave Engineering Laboratory	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme									
		Theory									
		Internal Assessment			End sem	Duration (hrs)	Term work	Pract.	Oral	Pract. / Oral	Total
		Test 1	Test 2	Avg							
ELXL DLO6021	Microwave Engineering Laboratory	--	--	--	--	--	25	--	--	25	50

Assessment:

Term Work:

At least **SIX** experiments based on the entire syllabus of **ELXDLO 6021 (Microwave Engineering)** should be set to have well predefined inference and conclusion. Computation/simulation based experiments are also encouraged. The experiments should be students' centric and attempt should be made to make experiments more meaningful, interesting and innovative. Term work assessment must be based on the **overall performance** of the student with **every experiment graded from time to time. Term work must include a mini project in addition to the number of experiments. The course mini-project is to be undertaken in a group of two to three students.** The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

The grades should be converted into marks as per the **Credit and Grading System** manual and should be **added and averaged**. The grading and term work assessment should be done based on this scheme.

The final certification and acceptance of term work ensures satisfactory performance of laboratory work, mini project and minimum passing marks in term work.

Practical and Oral exam will be based on the entire syllabus.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELXL DLO6022	Electronic Product Design	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme									
		Theory					Term work	Pract.	Oral	Pract. / Oral	Total
		Internal Assessment			End sem	Duration (hrs)					
		Test 1	Test 2	Avg							
ELXL DLO6022	Electronic Product Design	--	--	--	--	--	25	--	--	25	50

At least **Six** experiments based on the entire syllabus of **ELXDLO6022 (**Electronic Product Design**)** should be set to have well-defined inference and conclusion. The experiments should be student-centric and attempt should be made to make experiments more meaningful, interesting and innovative. Experiment must be graded from time to time. Additionally, each student (in group of 2/3) has to perform a Mini Project as a part of the laboratory and report of mini project should present in laboratory journal. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Oral exam will be based on the entire syllabus. Equal weightage should be given to laboratory experiments and project while assigning term work marks. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Lab session includes **six experiments plus one presentation on case study.**

Suggested Experiments:

1. Experiment based on Ground and Supply bounce
2. PCB design steps involved in product design
3. Simulation based on use of Simulator software
4. Working of an Emulator in Design step
5. Role of Pattern Generator in Design step
6. Debugging of the digital circuit based on Logic Analyzer
7. Application of the Spectrum analyzer
8. Demonstration of usefulness of the Arbitrary waveform generator
9. Setup for EMI and EMC test
10. Experiment based on calibration of the product.

Suggested topics for Case Study:

Faculty members can suggest topics pertaining above syllabus and ask students to submit complete report covering design issues, hardware and software details and applications.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL DLO6023	Wireless Communication Laboratory	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXL DLO6023	Wireless Communication Laboratory	-	-	-	-	25	--	25	50

Laboratory Experiments:

Lab session includes seven experiments and a Case study(Power point Presentation)on any one of the suggested topics.

Note:

1. The experiments will be based on the syllabus contents.

2. Minimum seven experiments need to be conducted.(Scilab, MATLAB, LabVIEW, NS2/NS3 etc can be used for simulation).

3. Each student (in groups of 3/4) has to present a Case study (Power point Presentation) as a part of the laboratory work.

The topics for Presentation / Case-study may be chosen to be any relevant topic on emerging technology.

("Beyond the scope of the syllabus".)

Power point presentation should contain minimum of 15 slides and students should submit a report , (PPT+Report) carry minimum of 10 marks The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Course Code	Course Name	Teaching scheme			Credit assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
ELXL DLO6024	Computer Organization and Architecture	--	02	--	--	01	--	01

Course Code	Course Name	Examination Scheme									
		Theory									
		Internal Assessment			End sem	Duration (hrs)	Term work	Pract.	Oral	Pract. / Oral	Total
		Test 1	Test 2	Avg							
ELXL DLO6024	Computer Organization and Architecture	--	--	--	--	--	25	--	--	25	50

At least **six** experiments based on the entire syllabus of **ELX DLO6024 (Computer Organization and Architecture)** should be set to have well-defined inference and conclusion. Computation/simulation based experiments are also encouraged. The experiments should be student-centric and attempt should be made to make experiments more meaningful, interesting and innovative. Additionally, a **Seminar on IEEE/ACM paper** focussing on key areas of research in Computer Architecture/Organization to be part of the term-work which is duly graded. **Suggested List of Experiments:**

Expt. No.	Title of the Experiments
1	Implementation of Booth's Algorithm (using VHDL)
2	To create a control store for micro-programmed control unit (using VHDL)
3	Using a cache simulator , calculate the cache miss-rate for various mapping schemes
4	Implement various page replacement policies (LRU, FIFO,LFU)
5	Program to detect the type of hazard (RAW,WAR,WAW)for a set of instructions
6	Using a performance analyzer tool, extract various performance metrics

B.E. (Electronics Engineering)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX701	Instrumentation System Design	04	--	---	04	---	---	04
ELX702	Power Electronics	04	--	---	04	---	---	04
ELX703	Digital signal processing	04	--	---	04	---	---	04
ELXDLO703X	Department Level Optional course III	04	--	---	04	---	---	04
ILO701X	Institute Level Optional Course I#	03	---	---	03	---	---	03
ELXL701	Instrumentation System Design Lab.		02			01	---	01
ELXL702	Power Electronics Lab.		02			01	---	01
ELXL703	Digital signal processing Lab.		02			01	---	01
ELXL704	Project-I	---	06	---	---	03	---	03
ELXLDLO703X	Dept. Level Optional course III Lab.		02			01	---	01
	TOTAL	**19**	**14**	**---**	**19**	**07**	**---**	**26**

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX801	Internet of Things	04	--	---	04	---	---	04
ELX 802	Analog and Mixed VLSI Design	04	--	---	04	---	---	04
ELXDLO804X	Department Level Optional course IV	04	--	---	04	---	---	04
ILO802X	Institute Level Optional course II#	03	---	---	03	---	---	03
ELX801	Internet of Things Lab.		02			01	---	01
ELXL802	Analog and Mixed VLSI Design Lab.		02			01	---	01
ELXL803	Project-II	---	12	---	---	06	---	06
ELXLDLO804X	Department Level Optional Courses IV Lab.		02			01	---	01
	TOTAL	**15**	**18**	**---**	**15**	**9**	---	**24**

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELX 701	Instrumentation System Design	04	---	---	04	---	---	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Examination			
		Test I	Test II	Average				
ELX 701	Instrumentation System Design (ISD)	20	20	20	80	---	---	100

Rationale :- For optimum operation & satisfactory performance of any industrial process control system, it is necessary to have a reliably engineered system with a thorough knowledge of the process conditions & requirements as per the system or design specifications. This subject introduces various nuances in the design of instrumentation systems, which is itself a synergy of sensors, transducers, actuators, process control & electronic systems to achieve the desired operation of a plant or the proper control of an industrial process. Students are exposed to principles of designing which enable them to design, build & implement such electronically controlled systems for measurement, signal conditioning & final control.

Course Objectives :-

1. To learn basic functions & working of pneumatic, hydraulic & electrical components used in process control
2. To understand principles of process parameter conversion & transmission in various forms
3. To gain familiarity with control system components & their applications in process control
4. To study various types of controllers used in process control & their tuning for different applications
5. To be aware of recent advances & technological developments in industrial instrumentation & process control

Course Outcomes :-

At the end of the course, students should gain the ability to :-

- **ELX 701.1 :-** Demonstrate the needs of advancement in instrumentation systems
- **ELX 701.2 :-** Select the proper components for pneumatic & hydraulic systems
- **ELX 701.3 :-** Choose the transmitter / controller for given process application
- **ELX 701.4 :-** Analyze the controller parameters for discrete or continuous type
- **ELX 701.5 :-** Design the controller (electronic) for a given process or application

Module No.	Topics	Hours
1	**ACTUATORS & PROCESS CONTROL VALVES**	**08**
1.1	Electrical actuators – relays, solenoids & electrical motors (DC, AC & stepper motor)	
1.2	Pneumatic actuators – basic pneumatic system, pneumatic compressors (piston, vane, screw) flapper nozzle, single & double acting cylinder, rotary actuator, filter-regulator-lubricator (FRL)	
1.3	Hydraulic actuator – hydraulic pumps, control valves types (globe, ball, needle, butterfly, gate, diaphragm & pinch), cavitation & flashing with their remedies, pressure drop across valve & leakage, valve noise, flow characteristics on load changes, control valves parameters, control valves sizing, valve calibration, digital control valves, selecting control valves & applications	
2	**DESIGN OF SIGNAL CONDITIONING CIRCUITS**	**08**
2.1	Principles of analog & digital signal conditioning – signal level & bias change, linearization, conversion, filtering & impedance matching, concept of loading, comparators & converters	
2.2	Design of operational amplifier based circuits in instrumentation – analysis of voltage divider circuits, bridge circuits, RC filters, inverting & non-inverting amplifier, instrumentation amplifier, V to I & I to V converter, integrator, differentiator & linearization (with numerical examples)	
2.3	Transmitters – Introduction to telemetry & its basic block diagram, 2 wire, 3 wire & 4 wire transmitters, 4 mA to 20 mA current transmitter, electronic transmitters for temperature, level, pressure & flow, current to pressure (I to P) & pressure to current (P to I) converters	
3	**PROCESS CONTROLLER PRINCIPLES**	**08**
3.1	Discontinuous controller – two position mode, multi-position mode & floating mode	
3.2	Continuous controller – single mode (P, I & D) & composite mode (PD, PI & PID), split range, auto select, ratio & cascaded controllers, selection criterion of controller for a process mode	
3.3	Tuning of PID controller – process loop tuning, open loop transient response method, Ziegler – Nichols tuning method, frequency response methods (numerical examples on PID tuning)	
4	**PROGRAMMABLE LOGIC CONTROLLERS (PLC)**	**10**
4.1	Discrete state process controller – discrete state variables, process specifications & event sequence description	
4.2	Relay controller & ladder diagram – introduction to relay ladder diagram logic, ladder diagram elements & ladder diagram programming examples	

4.3	PLC – relay sequencers, programmable logic controller design, PLC operation, programming the PLC, PLC software functions (application examples on relay ladder logic programming)	
5	**DIGITAL BASED PROCESS CONTROL**	**08**
5.1	Data acquisition system (DAS) – objectives, signal conditioning of inputs, single channel DAS, multi-channel DAS, computer based DAS, data logger, difference between DAS & data logger	
5.2	Computer aided process control – architecture, human machine interface (HMI), supervisory control & data acquisition (SCADA), standard interfaces (RS-232C, RS-422A & RS-485)	
5.3	Supervisory control system (SCS), introduction to the Fieldbus & Profibus process controlled networks, overview of distributed control system (DCS), features & advantages of DCS	
6	**CALIBRATION STANDARDS & ADVANCES IN INSTRUMENTATION**	**06**
6.1	PC & microcomputer based instrumentation, virtual instrumentation & LabVIEW introduction	
6.2	Calibration of instrumentation systems, representation of instrumentation control process with SAMA & ISA symbols, ISO/IEC 17025 General requirements for calibration standards	
6.3	Instrumentation standards, ISA S82.01 – Safety Standard for Electrical and Electronic Test, Measuring, Controlling Related Equipment, ISA S84.01 – Application of Safety Instrumented Systems for the Process Industries, ANSI/NEMA 250 – Enclosures for Electrical Equipment	
1 – 6	**TOTAL**	**48**

Recommended Books :-

1. Curtis D. Johnson, Process Control Instrumentation Technology, 7th edition, PHI
2. S. K. Singh, Industrial Instrumentation & Control, 3rd edition, McGraw Hill
3. B.C. Nakra & K. K. Chaudhary, Instrumentation Measurement & Analysis, 3rd edition, McGraw Hill
4. Andrew Parr, Pneumatics & Hydraulics, 2nd edition, Jaico Publishing Co.
5. B. G. Liptak, Handbook of Process Control & Instrumentation, 4th edition, CRC Press
6. William C. Dunn, Fundamentals of Industrial Instrumentation & Process Control, 1st edition, McGraw Hill

Internal Assessment (IA) :-Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered as final IA marks.

End Semester Examination :-

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Q.1 will be compulsory and based on entire syllabus.
4. Remaining questions (Q.2 to Q.6) will be set from all modules.
5. Weightage of each module in question paper will be proportional to the number of respective lecture hours mentioned in the syllabus.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX702	Power Electronics	04	02	--	04	--	--	04

Subject Code	Subject Name	ExaminationScheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ELX702	Power Electronics	20	20	20	80	03	--	--	--	100

\Course Pre-requisite:

1. ENAS
2. EDC-1
3. EDC-2

Course Objectives:

1. To teach power electronic devices and their characteristics.
2. To highlight power electronics based rectifiers, inverters and choppers.

Course Outcomes:

After successful completion of the course students will be able to:

1. Discuss trade-offs involved in power semiconductor devices.
2. Design of triggering, commutation and protection circuits for SCRs.
3. Analyse different types of single-phase rectifiers and DC-DC converters.
4. Analyse different types of DC-AC converters (inverters).
5. Analyse different types of AC Voltage Controllers and Cycloconvertors.

Module No.	Unit No.	Contents	Hrs.
1		**Power semiconductor devices**	8
	1.1	Principle of operation of SCR, static and dynamic characteristics, gate Characteristics,	
	1.2	Principle of operation, characteristics, ratings and applications of: TRIAC, DIAC, MOSFET and power BJT. IGBT: basic structure, principle of operation, equivalent circuit, latch-up in IGBT's and V-I characteristics.	
2		**SCR: Triggering, commutation and Protection Circuits**	8
	2.1	Methods of turning ON SCR (types of gate signal), firing circuits (using R, RC, UJT, Ramp and pedestal, inverse cosine),	
	2.2	Design of commutation circuits,	
	2.3	Protection of SCR	
3		**Single-phase Controlled Rectifiers**	8
	3.1	Introduction to uncontrolled rectifiers, Half wave controlled rectifiers with R, RL load, effect of free-wheeling diode	
	3.2	Full wave fully controlled rectifiers (centre-tapped, bridge configurations), full-wave half controlled (semi-converters) with R, RL load, effect of freewheeling diode and effect of source inductance.	
	3.3	Calculation of performance parameters, input performance parameters (input power factor, input displacement factor (DF), input current distortion factors (CDF), input current harmonic factor (HF/THD), Crest Factor (CF)), output performance parameters.	
4		**Inverters**	10
	4.1	Introduction to basic and improved series/parallel inverters, limitations.	
	4.2	Introduction, principle of operation, performance parameters of Single phase half / full bridge voltage source inverters with R and R-L load,	
	4.3	Voltage control of single phase inverters using PWM techniques, harmonic neutralization of inverters, applications	
5		**DC-DC converters**	8
	5.1	Basic principle of step up and step down DC-DC converters, DC-DC switching mode regulators: Buck, Boost, Buck-Boost, Cuk Regulators (CCM mode only)	
	5.2	Voltage commutated, current commutated and load commutated DC-DC	

		converters	
	5.3	Applications in SMPS, Battery charging systems.	
6		**AC Voltage Controllers and Cycloconvertors**	**6**
	6.1	Principle of On-Off control, principle of phase control, single phase bidirectional control with R and RL load	
	6.2	Introduction, single phase and three phase Cyclo-converters, applications	
		Total	**48**

Recommended Books:

1. M. H. Rashid, "*Power Electronics*", Prentice-Hall of India
2. Ned Mohan, "*Power Electronics*", Undeland, Robbins, John Wiley Publication
3. P. S. Bhimbra, "Power Electronics", Khanna Publishers, 2012
4. M.D. Singh and K. B. Khanchandani, "*Power Electronics*", Tata McGraw Hill
5. Ramamurthy, " *Thyristors and Their Applications*"
6. P. C. Sen, "*Modern Power Electronics*", Wheeler Publication
7. S. Shrivastava, "Power Electronics", Nandu Publication, Mumbai.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3. Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4. Remaining questions will be selected from all the modules

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal Assessment			End Sem. Exam				
		Test 1	Test 2	Ave. of Test 1 and Test 2					
EXC703	Digital Signal Processing	20	20	20	80	--	--	--	100

Prerequisite Courses: Signals and Systems

Course Objectives:

1. To teach the design techniques and performance analysis techniques of digital filters
2. To introduce the students to advanced signal processing techniques, digital signal processors and applications

Course Outcomes:

After successful completion of this course students will be able to

1. Demonstrate an understanding of the discrete-time Fourier transform and the concept of digital frequency.
2. Design FIR and IIR digital filters to meet arbitrary specifications and Develop algorithms for implementation
3. Understand the effect of hardware limitations on performance of digital filters
4. Use advanced signal processing techniques and digital signal processors in various applications

Module No.	Unit No.	Topics	Hrs.
1.0		**Discrete Fourier Transform and Fast Fourier Transform**	10
	1.1	Definition and Properties of DFT,IDFT, circular convolution of sequences using DFT and IDFT, Relation between Z-transform and DFT Filtering of long data sequences: Overlap Save and Overlap Add Method Computation of DFT	
	1.2	Fast Fourier transforms(FFT),Radix-2decimationintime and decimation in frequency FFT algorithms, inverse FFT, and Introduction to composite FFT	
2.0		**IIR Digital Filters**	10
	2.1	Types of IIR Filters (Low Pass, High Pass, Band Pass, Band stop and All Pass) Analog filter approximations: Butterworth, Chebyshev I and II	
	2.2	MappingofS-planetoZ-plane,impulseinvariancemethod,bilineartransformation method, Design of IIR digital filters from analog filters with examples	
	2.3	Analog and digital frequency transformations with design examples	
3.0		**FIR Digital Filters**	10
	3.1	Characteristics of FIR digital filters, Minimum Phase, Maximum Phase, Mixed Phase and Linear Phase Filters Frequency response, location of the zero sof linear phase FIR filters	

	3.2	Design of FIR filter susing window techniques (Rectangular, Hamming, Hanning,Blackmann, Barlet) Design of FIR filter susing Frequency Sampling technique Comparison of IIR and FIR filters	
4.0		**Finite Word Length Effects in Digital Filters**	**06**
	4.1	Quantization, truncation and rounding, Effects due to truncation and rounding, Input quantization error, Product quantization error, Co-efficient quantization error, Zero-input limit cycle oscillations, Overflow limit cycle oscillations, Scaling	
	4.2	Quantization in Floating Point realization of IIR digital filtersFinite word length effects in FIR digital filters	
5.0		**Multirate DSPand FilterBanks**	**06**
	5.1	Introduction and concept of Multirate Processing, Block Diagram of Decimator and Interpolator, Decimation and Interpolation by Integer numbers Multistage Approach to Sampling rate converters	
	5.2	Sample rate conversion using Polyphase filter structure, Type I and Type II Polyphase Decomposition	
6.0		**DSP Processors and Applications**	**06**
	6.1	Introduction to General Purpose and Special Purpose DSP processors, fixed point and floating point DSP processor, Computer architecture for signal processing, Harvard Architecture, Pipelining, multiplier and accumulator(MAC), Special Instructions, Replication, On-chip memory, Extended Parallelism	
	6.2	General purpose digital signal processors, Selecting digital signal processors, Special purpose DSP hardware	
	6.3	Applications of DSP: Radar Signal Processing and Speech Processing	
Total			**48**

Text Books:

1. Emmanuel C. Ifeachor, Barrie W. Jervis, "*Digital Signal Processing"*, A Practical Approach by, Pearson Education
2. Tarun Kumar Rawat, " *Digital Signal Processing"*, Oxford University Press, 2015

Reference Books:

1. ProakisJ., Manolakis D., "*Digital Signal Processing*", 4th Edition, Pearson Education
2. Sanjit K. Mitra , Digital Signal Processing – A Computer Based Approach – edition 4e
3. McGraw Hill Education (India) Private Limited
4. OppenheimA.,SchaferR.,BuckJ.,"*DiscreteTimeSignalProcessing*",2ndEdition, Pearson Education..
5. B. VenkataRamaniand, M. Bhaskar, "*Digital Signal Processors, Architecture, Programming andApplications*", Tata McGraw Hill, 2004.
6. L.R.RabinerandB.Gold,"*TheoryandApplicationsofDigitalSignalProcessi*ng",Prentice-HallofIndia, 2006.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5markswill be asked.
4: Remaining questions will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO7031	NEURAL NETWORKS & FUZZY LOGIC	4	2	--	4	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXDLO7031	NEURAL NETWORKS & FUZZY LOGIC	20	20	20	80	-	--	--	100

Pre-requisite

- Knowledge of linear algebra, multivariate calculus, and probability theory
- Knowledge of a programming language (MATLAB /C/C ++ recommended)

Course Objectives:

- To study basics of biological Neural Network.
- To understand the different types of Artificial Neural Networks
- To know the applications of ANN .
- To study fuzzy logic and fuzzy systems.

Course outcomes:

At the end of completing the course of Neural Networks & Fuzzy Logic, a student will be able to:

1. Choose between different types of neural networks
2. Design a neural network for a particular application
3. Understand the applications of neural networks
4. Appreciate the need for fuzzy logic and control

Module	Contents	Hours
1	**Introduction:**1.1 Biological neurons, McCulloch and Pitts models *of* neuron, Types of activation function, Network architectures, Knowledge representation, Hebb net 1.2 Learning processes: Supervised learning, Unsupervised learning and Reinforcement learning 1.3 Learning Rules : Hebbian Learning Rule, Perceptron Learning Rule, Delta Learning Rule, Widrow-Hoff Learning Rule, Correlation Learning Rule, Winner-Take-All Learning Rule 1.4 Applications and scope of Neural Networks	**10**
2	**Supervised Learning Networks :** 2.1 Perception Networks – continuous & discrete, Perceptron convergence theorem, Adaline, Madaline, Method of steepest descent, – least mean square algorithm, Linear & non-linear separable classes & Pattern classes, 2.2 Back Propagation Network, 2.3 Radial Basis Function Network.	**12**
3	**Unsupervised learning network:** 3.1 Fixed weights competitive nets, 3.2 Kohonen Self-organizing Feature Maps, Learning Vector Quantization, 3.3 Adaptive Resonance Theory – 1	**06**
4	**Associative memory networks:** 4.1 Introduction, Training algorithms for Pattern Association, 4.2 Auto-associative Memory Network, Hetero-associative Memory Network, Bidirectional Associative Memory, 4.3 Discrete Hopfield Networks.	**08**
5	Fuzzy Logic: 5.1 Fuzzy Sets, Fuzzy Relations and Tolerance and Equivalence 5.2 Fuzzification and Defuzzification 5.3 Fuzzy Controllers	**12**

TOTAL	**48**

Text- Books:

- Dr. S. N. Sivanandam, Mrs S.N. Deepa, *"Principles of Soft computing"*, Wiley Publication.
- Jacek M. Zurada, *"Introduction to Artificial Neural Systems,* Jaico publishing house.

Reference books :

- Simon Haykin, "*Neural Network* a - *Comprehensive Foundation",* Pearson Education.
- S. Rajsekaran, Vijaylakshmi Pai, *"Neural Networks, Fuzzy Logic, and Genetic Algorithms"*, PHI.
- Thimothy J. Ross, "*Fuzzy Logic with Engineering Applications",* Wiley Publication.
- Christopher M Bishop, *"Neural Networks For Pattern Recognition"* ,Oxford Publication

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of total 6 questions, each of 20 marks.
2. Only 4 questions need to be solved.
3. Question number 1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4. Remaining questions will be selected from all the modules.
5. No question should be asked from pre-requisite module

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO7032	Advanced Networking Technologies	4	2	--	4	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXDLO7032	Advanced Networking Technologies	20	20	20	80	-	--	--	100

Course Pre-requisite: ELX405 Principles of Communication Engineering
ELX602 Computer Communication Network
ELXDLO-2 Wireless Communication

Course Objectives:
The objectives of this course are to:

1. Understand the characteristic features of Various Wireless networks
2. Understand Optical networking and significance of DWDM.
3. Introduce the need for network security and safeguards
4. Understand the principles of network management

Course Outcomes:

On successful completion of the course the students will be able to:

1. Appreciate the need for Wireless networks and study the IEEE 802.11 Standards
2. Comprehend the significance of Asynchronous Transfer Mode(ATM)
3. Understand the features of emerging wireless Networks: Bluetooth Networks,ZIGBEE, WSN
4. Analyze the importance of Optical networking
5. Demonstrate knowledge of network design and security and management
6. Understand the concept of Cloud Computing and its applications.

Module No.	Unit No.	Topics	Hrs.
1.		**Wireless LAN and WAN technologies**	**08**
	1.1	Introduction to Wireless networks : Infrastructure networks, Ad-hoc networks, IEEE 802.11 architecture and services, Medium Access Control sub-layers, CSMA/CA Physical Layer, 802.11 Security considerations .	
	1.2	Asynchronous Transfer Mode (ATM): Architecture, ATM logical connections, ATM	

		cells , ATM Functional Layers, Congestion control and Quality of service	
2.		**Emerging Wireless Technologies**	**10**
	2.1	**Wireless Personnel Area Network(WPAN):** WPAN 802.15.1 architecture ,Bluetooth Protocol Stack, Bluetooth Link Types, Bluetooth Security, Network Connection Establishment in Bluetooth, Network Topology in Bluetooth, Bluetooth Usage Models	
	2.2	802.15.3- Ultra Wide Band , 802.15.4- Zigbee , RFID	
	2.3	**Wireless Sensor Networks: Introduction and Applications,** Wireless Sensor Network Model, Sensor Network Protocol Stack,	
3.0		**Optical Networking**	**08**
	3.1	SONET : SONET/SDH, Architecture, Signal, SONET devices, connections, SONET layers, SONET frames, STS Multiplexing, SONET Networks	
	3.2	DWDM: Frame format, DWDM architecture ,Optical Amplifier , Optical cross connect Performance and design considerations	
4.0		**Network Design, Security and Management**	**10**
	4.1	3 tier Network design layers: Application layer, Access layer, Backbone layers, Ubiquitous computing and Hierarchical computing	
	4.2	Network Security: Security goal, Security threats, security safeguards, firewall types and design.	
	4.3	Network management definitions, functional areas (FCAPS), SNMP,RMON	
5.0		**Routing in the Internet:**	**06**
	5.1	Intra and inter domain Routing, Unicast Routing Protocols: RIP, OSPF, BGP	
	5.2	Multicast Routing Protocols ,Drawbacks of traditional Routing methods	
6.0		**Cloud computing:**	**06**
	6.1	Cloud Computing Evolution, Definition, SPI framework of Cloud Computing, Cloud service delivery models,	
	6.2	Cloud deployment models, key drivers to adoption of cloud, impact of cloud computing on users, examples of cloud service providers: Amazon, Google, Microsoft, Salesforce etc.	
		Total	**48**

Recommended Text Books:

1. Behrouz A. Forouzan, "Data communication and networking ", McGraw Hill Education, Fourth Edition.
2. Darren L. Spohn , "Data Network Design" , McGraw Hill Education ,Third edition
3. William Stallings, "Data and Computer communications", Pearson Education, 10th Edition.
4. Tim Mather , Subra Kumaraswamy & Shahed Latif, "Cloud security & Privacy: an enterprise Perspective", O'Reilly Media Inc.Publishers

Reference Books:

1. William Stallings, "Wireless Communications and Networks", Pearson Ed., 2nd Edition.

2. Vijay Garg ,"Wireless Communication and networking" , Morgan Kaufmann Publishers
3. Carr and Snyder, " Data communication and network security" , McGraw Hill ,1ST edition.
4. Upena Dalal & Manoj Shukla , " Wireless Communication and Networks", Oxford Press
5. Deven Shah , Ambavade, "Advanced Communication Networking"
6. Beherouz A Forouzan , "TCP /IP Protocol Suite" , Tata McGraw Hill Education ,4th edition.

Internal Assessment (IA):
Two tests must be conducted which should cover at least 80% of the syllabus. The average marks of both the tests will be considered as final IA marks.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4: Remaining questions will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO7033	Robotics	4	2	--	4	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXDLO7033	Robotics	20	20	20	80	-	--	--	100

Pre-requisite: Applied Mathematics III, Applied Mathematics IV ,Linear Control Systems

Course Objectives:

1. To study basics of robotics
2. To familiarize students with kinematics & dynamics of robots
3 To familiarize students with Trajectory & task planning of robots.
4 To familiarize students with robot vision

Course outcomes:

At the end of completing the course of Robotics, a student will be able to:

1. understand the basic concepts of robotics
2. perform the kinematic and the dynamic analysis of robots
3. perform trajectory and task planning of robots
4. describe importance of visionary system in robotic manipulation

Module	Contents	Hours
1	**Fundamentals of Robotics:** 1.1 Robot Classification, Robot Components, Robot Specification, Joints, Coordinates, Coordinate frames, Workspace, Languages, Applications.	**04**
2	**Kinematics of Robots:** 2.1 Homogeneous transformation matrices, Inverse transformation matrices, Forward and inverse kinematic equations – position and orientation 2.2 Denavit-Hatenberg representation of forward kinematics, Forward and inverse kinematic solutions of three and four axis robot	**10**
3	**Velocity Kinematics & Dynamics:** 3.1 **Differential motions and velocities :** Differential relationship, Jacobian, Differential motion of a frame and robot, Inverse Jacobian, Singularities, 3.2 **Dynamic Analysis of Forces :** Lagrangian mechanics, Newton Euler formulation, Dynamic equations of two axis robot	**10**
4	**Trajectory planning:** 4.1 Basics of Trajectory planning , Joint-space trajectory planning, Cartesian-space trajectories	**08**
5	**Robot Vision:** 5.1 Image representation, Template matching, Polyhedral objects, Shape analysis, Segmentation, Iterative processing, Perspective transform, Camera Calibration	**08**
6	**Task Planning:** 6.1 Task level programming, Uncertainty, Configuration Space, Gross motion Planning; Grasp planning, Fine-motion Planning, Simulation of Planer motion, Source and goal scenes, Task planner simulation.	**08**
	TOTAL	**48**

Text- Books :

- Robert Shilling, “Fundamentals of Robotics - Analysis and contro”l, Prentice Hall of India, 2009
- Saeed Benjamin Niku, “Introduction to Robotics – Analysis, Control, Applications”, Wiley India Pvt. Ltd., Second Edition, 2011

Reference books :

- John J. Craig, “Introduction to Robotics – Mechanics & Control”, Third Edition, Pearson Education, India, 2009
- Mark W. Spong , Seth Hutchinson, M. Vidyasagar, “Robot Modeling & Control ”, Wiley India Pvt. Ltd., 2006
- Mikell P. Groover et.al, ”Industrial Robots-Technology, Programming & applications”, McGraw Hill , New York, 2008

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of the syllabus. The average marks of both the tests will be considered as final IA marks.

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4: Remaining questions will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO7034	IC Technology	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Avg. of Test 1 and Test 2					
ELXDLO7034	IC Technology	20	20	20	80	--	--	--	100

Course Pre-requisite:

- ELX302:Electronic Devices and Circuits I
- ELX303:Digital Circuit Design
- ELX603:VLSI Design

Course Objectives:

1. To provide knowledge of IC fabrication processes and advanced IC technologies.
2. To disseminate knowledge about novel VLSI devices and materials.

Course Outcomes:

After successful completion of the course student will be able to

1. Demonstrate a clear understanding of various MOS fabrication processes & CMOS fabrication flow.
2. Design layout of MOS based Circuits.
3. Demonstrate a clear understanding of Semiconductor Measurements & Testing.
4. Understand advanced technologies, Novel Devices and materials in Modern VLSI Technology.

Module No.	Unit No.	Topics	Hrs.
1. 0		**Crystal Growth, Wafer preparation and fabrication for VLSI Technology**	8
	1.1	**Semiconductor Manufacturing:** Semiconductor technology trend, Clean rooms, Wafer cleaning and Gettering.	
	1.2	**Semiconductor Substrate:** Crystal structure, Crystal defects, Czochralski growth, Float Zone growth, Bridgman growth of GaAs, Wafer Preparation and specifications	
2.0		**Fabrication Processes Part 1**	12
	2.1	**Epitaxy:** Classification**,** Molecular Beam Epitaxy	
	2.2	**Silicon Oxidation:** Thermal oxidation process, Kinetics of growth, Properties of	
		Silicon Dioxide, Oxide Quality.	
	2.3	**Device Isolation:** LOCOS, Shallow Trench Isolation (STI).	
	2.4	**Deposition: Physical Vapor Deposition-**Evaporation and Sputtering, **Chemical Vapor Deposition:** APCVD, LPCVD,PECVD	
	2.4	**Diffusion:** Nature of diffusion, Diffusion in a concentration gradient, diffusion Equation, diffusion systems, problems in diffusion.	
	2.5	**Ion Implantation:** Penetration range-Nuclear& Electronic stopping and Range, implantation damage, Annealing-Rapid thermal annealing, ion implantation systems.	
3.0		**Fabrication Process Part 2**	12
	3.1	**Etching &Lithography:** **Etching:** Basic concepts and Classification **Lithography:** Introduction to Lithography process, Types of Photoresist, Types of Lithography: Electron beam, Ion beam and X-ray lithography	
	3.2	**Metallization and Contacts:** Introduction to Metallization, Schottky contacts and Ohmic contacts.	
	3.3	**CMOS Process Flow:** N well, P-well and Twin tub, CMOS Latch Up	
	3.4	Design rules, Layout of MOS based circuits (gates and combinational logic), Buried	

		and Butting Contact	
4.0		**Measurement and Testing**	06
	4.1	**Semiconductor Measurements:** Conductivity type, Resistivity, Hall Effect Measurements, Drift Mobility,	
	4.2	**Testing:** Technology trends affecting testing, VLSI testing process and test	
		equipment, test economics and product quality	
		VLSI Technologies	05
	5.1	**SOI Technology:** SOI fabrication using SIMOX, Bonded SOI and Smart Cut ,PD SOI and FD SOI Device structure and their features	
	5.2	**Advanced Technologies:** low κ and high κ, BiCMOS, HκMG Stack, Strained Silicon.	
	5.3	**GaAs Technologies:** MESFET Technology, MMIC technologies, MODFET	
		Novel Devices and Materials	
	6.1	**Multigate Devices*:*** Various multigate device configurations-double gate, triple gate (FinFET) and Gate All Around (Nanowire). **Nanowire:** Concept, VLS method of fabrication, Nanowire FET, Types: Horizontal and Vertical Nanowires, III-V compound Materials in Nanowires.	05
	6.2	**2-D Materials and FET:**Graphene& CNT FET, MOS2 and Black Phosphorous.	
		Total	**48**

Recommended Books:

1. James D. Plummer, Michael D. Deal and Peter B. Griffin, "*Silicon VLSI Technology*", Pearson, Indian Edition.

2. Stephen A. Campbell, "*The Science and Engineering of Microelectronic Fabrication"*, Oxford University Press, 2nd Edition.

3. Sorab K. Gandhi, "*VLSI Fabrication Principles*", Wiley, Student Edition.
4. G. S. May and S. M. Sze, "*Fundamentals of Semiconductor Fabrication*", Wiley, First Edition.

5. Kerry Bernstein and N. J. Rohrer, "*SOI Circuit Design Concepts",* Kluwer Academic Publishers, 1st edition.

6. Jean-Pierre Colinge, "*FinFETs and Other Multigate Transistors",* Springer, 1st edition

7. M. S. Tyagi, "*Introduction to Semiconductor Materials and Devices*", John Wiley and Sons, 1st edition.

8. James E. Morris and KrzysztolIniewski, "*Nanoelectronic Device ApplicationsHandbook*", CRC Press
9. Glenn R. Blackwell, "*The electronic packaging*", CRC Press

10. Michael L. Bushnell and Vishwani D. Agrawal, "*Essentials of Electronic Testing fordigital, memory and mixed-signal VLSI circuits*", Springer

Internal Assessment (IA):
Two tests must be conducted which should cover at least 80% of the syllabus. The average marks of both the tests will be considered as final IA marks.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4: Remaining questions will be selected from all the modules.

Course Code	Course Name	Credits
ILO7011	Product Life Cycle Management	03

Objectives:

1. To familiarize the students with the need, benefits and components of PLM
2. To acquaint students with Product Data Management & PLM strategies
3. To give insights into new product development program and guidelines for designing and developing a product
4. To familiarize the students with Virtual Product Development

Outcomes: Learner will be able to…

1. Gain knowledge about phases of PLM, PLM strategies and methodology for PLM feasibility study and PDM implementation.
2. Illustrate various approaches and techniques for designing and developing products.
3. Apply product engineering guidelines / thumb rules in designing products for moulding, machining, sheet metal working etc.
4. Acquire knowledge in applying virtual product development tools for components, machining and manufacturing plant

Module	Detailed Contents	Hrs
01	**Introduction to Product Lifecycle Management (PLM):**Product Lifecycle Management (PLM), Need for PLM, Product Lifecycle Phases, Opportunities of Globalization, Pre-PLM Environment, PLM Paradigm, Importance & Benefits of PLM, Widespread Impact of PLM, Focus and Application, A PLM Project, Starting the PLM Initiative, PLM Applications **PLM Strategies:**Industrial strategies, Strategy elements, its identification, selection and implementation, Developing PLM Vision and PLM Strategy , Change management for PLM	10
02	**ProductDesign:**Product Design and Development Process, Engineering Design, Organization and Decomposition in Product Design, Typologies of Design Process Models, Reference Model, Product Design in the Context of the Product Development Process, Relation with the Development Process Planning Phase, Relation with the Post design Planning Phase, Methodological Evolution in Product Design, Concurrent Engineering, Characteristic Features of Concurrent Engineering, Concurrent Engineering and Life Cycle Approach, New Product Development (NPD) and Strategies, Product Configuration and Variant Management, The Design for X System, Objective Properties and Design for X Tools, Choice of Design for X Tools and Their Use in the Design Process	09

03	**Product Data Management (PDM):**Product and Product Data, PDM systems and importance, Components of PDM, Reason for implementing a PDM system, financial justification of PDM, barriers to PDM implementation	05
04	**Virtual Product Development Tools:**For components, machines, and manufacturing plants, 3D CAD systems and realistic rendering techniques, Digital mock-up, Model building, Model analysis, Modeling and simulations in Product Design, Examples/Case studies	05
05	**Integration of Environmental Aspects in Product Design:**Sustainable Development, Design for Environment,Need for Life Cycle Environmental Strategies, Useful Life Extension Strategies, End-of-Life Strategies, Introduction of Environmental Strategies into the Design Process, Life Cycle Environmental Strategies and Considerations for Product Design	05
06	**Life Cycle Assessment and Life Cycle Cost Analysis:**Properties, and Framework of Life Cycle Assessment, Phases of LCA in ISO Standards, Fields of Application and Limitations of Life Cycle Assessment, Cost Analysis and the Life Cycle Approach, General Framework for LCCA, Evolution of Models for Product Life Cycle Cost Analysis	05

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper.Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. John Stark, "Product Lifecycle Management: Paradigm for 21st Century Product Realisation", Springer-Verlag, 2004. ISBN: 1852338105
2. Fabio Giudice, Guido La Rosa, AntoninoRisitano, "Product Design for the environment-A life cycle approach", Taylor & Francis 2006, ISBN: 0849327229
3. SaaksvuoriAntti, Immonen Anselmie, "Product Life Cycle Management", Springer, Dreamtech, ISBN: 3540257314
4. Michael Grieve, "Product Lifecycle Management: Driving the next generation of lean thinking", TataMcGrawHill,2006,ISBN:0070636265

Course Code	Course Name	Credits
ILO7012	Reliability Engineering	03

Objectives:

1. To familiarize the students with various aspects of probability theory
2. To acquaint the students with reliability and its concepts
3. To introduce the students to methods of estimating the system reliability of simple and complex systems
4. To understand the various aspects of Maintainability, Availability and FMEA procedure

Outcomes: Learner will be able to…

1. Understand and apply the concept of Probability to engineering problems
2. Apply various reliability concepts to calculate different reliability parameters
3. Estimate the system reliability of simple and complex systems
4. Carry out a Failure Mode Effect and Criticality Analysis

Module	Detailed Contents	Hrs
01	**Probability theory:** Probability: Standard definitions and concepts; Conditional Probability, Baye's Theorem. **Probability Distributions:** Central tendency and Dispersion; Binomial, Normal, Poisson, Weibull, Exponential, relations between them and their significance. **Measures of Dispersion:** Mean, Median, Mode, Range, Mean Deviation, Standard Deviation, Variance, Skewness and Kurtosis.	08
02	**Reliability Concepts:** Reliability definitions, Importance of Reliability, Quality Assurance and Reliability, Bath Tub Curve. **Failure Data Analysis:** Hazard rate, failure density, Failure Rate, Mean Time To Failure (MTTF), MTBF, Reliability Functions. **Reliability Hazard Models:** Constant Failure Rate, Linearly increasing, Time Dependent Failure Rate, Weibull Model. Distribution functions and reliability analysis.	08
03	**System Reliability:** System Configurations: Series, parallel, mixed configuration, k out of n structure, Complex systems.	05
04	**Reliability Improvement:** Redundancy Techniques: Element redundancy, Unit redundancy, Standby redundancies. Markov analysis.	08

	System Reliability Analysis – Enumeration method, Cut-set method, Success Path method, Decomposition method.	
05	**Maintainability and Availability:** System downtime, Design for Maintainability: Maintenance requirements, Design methods: Fault Isolation and self-diagnostics, Parts standardization and Interchangeability, Modularization and Accessibility, Repair Vs Replacement. Availability – qualitative aspects.	05
06	**Failure Mode, Effects and Criticality Analysis:** Failure mode effects analysis, severity/criticality analysis, FMECA examples. Fault tree construction, basic symbols, development of functional reliability block diagram, Fau1t tree analysis and Event tree Analysis	05

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. L.S. Srinath, “Reliability Engineering”, Affiliated East-Wast Press (P) Ltd., 1985.
2. Charles E. Ebeling, “Reliability and Maintainability Engineering”, Tata McGraw Hill.
3. B.S. Dhillion, C. Singh, “Engineering Reliability”, John Wiley & Sons, 1980.
4. P.D.T. Conor, “Practical Reliability Engg.”, John Wiley & Sons, 1985.
5. K.C. Kapur, L.R. Lamberson, “Reliability in Engineering Design”, John Wiley & Sons.
6. Murray R. Spiegel, “Probability and Statistics”, Tata McGraw-Hill Publishing Co. Ltd.

Course Code	Course Name	Credits
ILO7013	Management Information System	03

Objectives:

1. The course is blend of Management and Technical field.
2. Discuss the roles played by information technology in today's business and define various technology architectures on which information systems are built
3. Define and analyze typical functional information systems and identify how they meet the needs of the firm to deliver efficiency and competitive advantage
4. Identify the basic steps in systems development

Outcomes: Learner will be able to…

1. Explain how information systems Transform Business
2. Identify the impact information systems have on an organization
3. Describe IT infrastructure and its components and its current trends
4. Understand the principal tools and technologies for accessing information from databases to improve business performance and decision making
5. Identify the types of systems used for enterprise-wide knowledge management and how they provide value for businesses

Module	Detailed Contents	Hrs
01	Introduction To Information Systems (IS): Computer Based Information Systems, Impact of IT on organizations, Imporance of IS to Society. Organizational Strategy, Competitive Advantages and IS.	4
02	Data and Knowledge Management: Database Approach, Big Data, Data warehouse and Data Marts, Knowledge Management. Business intelligence (BI): Managers and Decision Making, BI for Data analysis and Presenting Results	7
03	Ethical issues and Privacy: Information Security. Threat to IS, and Security Controls	7
04	Social Computing (SC): Web 2.0 and 3.0, SC in business-shopping, Marketing, Operational and Analytic CRM, E-business and E-commerce – B2B B2C. Mobile commerce.	7
05	Computer Networks Wired and Wireless technology, Pervasive computing, Cloud	6

	computing model.	
06	Information System within Organization: Transaction Processing Systems, Functional Area Information System, ERP and ERP support of Business Process. Acquiring Information Systems and Applications: Various System development life cycle models.	8

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Kelly Rainer, Brad Prince,Management Information Systems, Wiley
2. K.C. Laudon and J.P. Laudon, Management Information Systems: Managing the Digital Firm, 10th Ed., Prentice Hall, 2007.
3. D. Boddy, A. Boonstra, Managing Information Systems: Strategy and Organization, Prentice Hall, 2008

Course Code	Course Name	Credits
ILO7014	Design of Experiments	03

Objectives:

1. To understand the issues and principles of Design of Experiments (DOE)
2. To list the guidelines for designing experiments
3. To become familiar with methodologies that can be used in conjunction with experimental designs for robustness and optimization

Outcomes: Learner will be able to…

1. Plan data collection, to turn data into information and to make decisions that lead to appropriate action
2. Apply the methods taught to real life situations
3. Plan, analyze, and interpret the results of experiments

Module	Detailed Contents	Hrs
01	**Introduction** 1.1 Strategy of Experimentation 1.2 Typical Applications of Experimental Design 1.3 Guidelines for Designing Experiments 1.4 Response Surface Methodology	06
02	**Fitting Regression Models** 2.1 Linear Regression Models 2.2 Estimation of the Parameters in Linear Regression Models 2.3 Hypothesis Testing in Multiple Regression 2.4 Confidence Intervals in Multiple Regression 2.5 Prediction of new response observation 2.6 Regression model diagnostics 2.7 Testing for lack of fit	08

03	**Two-Level Factorial Designs and Analysis** 3.1 The 2^2 Design 3.2 The 2^3 Design 3.3 The General2^k Design 3.4 A Single Replicate of the 2^k Design 3.5 The Addition of Center Points to the 2^k Design, 3.6 Blocking in the 2^k Factorial Design 3.7 Split-Plot Designs	07
04	**Two-Level Fractional Factorial Designs and Analysis** 4.1 The One-Half Fraction of the 2^k Design 4.2 The One-Quarter Fraction of the 2^k Design 4.3 The General 2^{k-p} Fractional Factorial Design 4.4 Resolution III Designs 4.5 Resolution IV and V Designs 4.6 Fractional Factorial Split-Plot Designs	07
05	**Conducting Tests** 5.1 Testing Logistics 5.2 Statistical aspects of conducting tests 5.3 Characteristics of good and bad data sets 5.4 Example experiments 5.5 Attribute Vs Variable data sets	07
06	**Taguchi Approach** 6.1 Crossed Array Designs and Signal-to-Noise Ratios 6.2 Analysis Methods 6.3 Robust design examples	04

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Raymond H. Mayers, Douglas C. Montgomery, Christine M. Anderson-Cook, Response Surface Methodology: Process and Product Optimization using Designed Experiment, 3rd edition,John Wiley & Sons, New York, 2001
2. D.C. Montgomery, Design and Analysis of Experiments, 5th edition, John Wiley & Sons, New York, 2001
3. George E P Box, J Stuart Hunter, William G Hunter, Statics for Experimenters: Design, Innovation and Discovery, 2nd Ed. Wiley
4. W J Dimond, Peactical Experiment Designs for Engineers and Scintists, John Wiley and Sons Inc. ISBN: 0-471-39054-2
5. Design and Analysis of Experiments (Springer text in Statistics), Springer by A.M. Dean, and D. T.Voss
6. Phillip J Ross, “Taguchi Technique for Quality Engineering,” McGrawHill
7. Madhav S Phadke, “ Quality Engineering using Robust Design,” Prentice Hall

Course Code	Course Name	Credits
ILO7015	Operations Research	03

Objectives:

1. Formulate a real-world problem as a mathematical programming model.
2. Understand the mathematical tools that are needed to solve optimization problems.
3. Use mathematical software to solve the proposed models.

Outcomes: Learner will be able to…

1. Understand the theoretical workings of the simplex method, the relationship between a linear program and its dual, including strong duality and complementary slackness.
2. Perform sensitivity analysis to determine the direction and magnitude of change of a model's optimal solution as the data change.
3. Solve specialized linear programming problems like the transportation and assignment problems, solve network models like the shortest path, minimum spanning tree, and maximum flow problems.
4. Understand the applications of integer programming and a queuing model and compute important performance measures

Module	Detailed Contents	Hrs
01	**Introduction to Operations Research**: Introduction, , Structure of the Mathematical Model, Limitations of Operations Research **Linear Programming**: Introduction, Linear Programming Problem, Requirements of LPP, Mathematical Formulation of LPP, Graphical method, Simplex Method Penalty Cost Method or Big M-method, Two Phase Method, Revised simplex method, **Duality**, Primal – Dual construction, Symmetric and Asymmetric Dual, Weak Duality Theorem, Complimentary Slackness Theorem, Main Duality Theorem, Dual Simplex Method, Sensitivity Analysis **Transportation Problem**: Formulation, solution, unbalanced Transportation problem. Finding basic feasible solutions – Northwest corner rule, least cost method and Vogel's approximation method. Optimality test: the stepping stone method and MODI method. **Assignment Problem**: Introduction, Mathematical Formulation of the Problem, Hungarian Method Algorithm, Processing of n Jobs Through Two Machines and m Machines, Graphical Method of Two Jobs m Machines Problem Routing Problem,	14

	Travelling Salesman Problem **Integer Programming Problem**: Introduction, Types of Integer Programming Problems, Gomory's cutting plane Algorithm, Branch and Bound Technique. Introduction to Decomposition algorithms.	
02	**Queuing models**: queuing systems and structures, single server and multi-server models, Poisson input, exponential service, constant rate service, finite and infinite population	05
03	**Simulation**: Introduction, Methodology of Simulation, Basic Concepts, Simulation Procedure, Application of Simulation Monte-Carlo Method: Introduction, Monte-Carlo Simulation, Applications of Simulation, Advantages of Simulation, Limitations of Simulation	05
04	**Dynamic programming**. Characteristics of dynamic programming. Dynamic programming approach for Priority Management employment smoothening, capital budgeting, Stage Coach/Shortest Path, cargo loading and Reliability problems.	05
05	**Game Theory**. Competitive games, rectangular game, saddle point, minimax (maximin) method of optimal strategies, value of the game. Solution of games with saddle points, dominance principle. Rectangular games without saddle point – mixed strategy for 2 X 2 games.	05
06	**Inventory Models**: Classical EOQ Models, EOQ Model with Price Breaks, EOQ with Shortage, Probabilistic EOQ Model,	05

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Taha, H.A. "Operations Research - An Introduction", Prentice Hall, (7th Edition), 2002.
2. Ravindran, A, Phillips, D. T and Solberg, J. J. "Operations Research: Principles and Practice", John Willey and Sons, 2nd Edition, 2009.
3. Hiller, F. S. and Liebermann, G. J. "Introduction to Operations Research", Tata McGraw Hill, 2002.
4. Operations Research, S. D. Sharma, KedarNath Ram Nath-Meerut.
5. Operations Research, KantiSwarup, P. K. Gupta and Man Mohan, Sultan Chand & Sons.

Course Code	Course Name	Credits
ILO7016	Cyber Security and Laws	03

Objectives:

1. To understand and identify different types cybercrime and cyber law
2. To recognized Indian IT Act 2008 and its latest amendments
3. To learn various types of security standards compliances

Outcomes: Learner will be able to…

1. Understand the concept of cybercrime and its effect on outside world
2. Interpret and apply IT law in various legal issues
3. Distinguish different aspects of cyber law
4. Apply Information Security Standards compliance during software design and development

Module	Detailed Contents	Hrs
01	**Introduction to Cybercrime:** Cybercrime definition and origins of the world, Cybercrime andinformation security, Classifications of cybercrime, Cybercrime and the Indian ITA 2000, A global Perspective on cybercrimes.	4
02	**Cyber offenses & Cybercrime:** How criminal plan the attacks, Social Engg, Cyber stalking, Cyber café and Cybercrimes, Botnets, Attack vector, Cloud computing, Proliferation ofMobile and Wireless Devices, Trends in Mobility, Credit Card Frauds in Mobile and Wireless Computing Era, Security Challenges Posed byMobile Devices, Registry Settings for Mobile Devices, AuthenticationService Security, Attacks on Mobile/Cell Phones, Mobile Devices:Security Implications for Organizations, Organizational Measures forHandling Mobile, Devices-Related Security Issues, OrganizationalSecurity Policies and Measures in Mobile Computing Era, Laptops	9
03	**Tools and Methods Used in Cyberline** Phishing, Password Cracking, Keyloggers and Spywares, Virus and Worms, Steganography, DoS and DDoS Attacks, SQL Injection, Buffer Over Flow, Attacks on Wireless Networks, Phishing, Identity Theft (ID Theft)	6
04	**The Concept of Cyberspace** E-Commerce , The Contract Aspects in Cyber Law ,The Security Aspect of Cyber Law	8

	,The Intellectual Property Aspect in Cyber Law , The Evidence Aspect in Cyber Law , The Criminal Aspect in Cyber Law, Global Trends in Cyber Law , Legal Framework for Electronic Data Interchange Law Relating to Electronic Banking , The Need for an Indian Cyber Law	
05	**Indian IT Act.** Cyber Crime and Criminal Justice : Penalties, Adjudication and Appeals Under the IT Act, 2000, IT Act. 2008 and its Amendments	6
06	**Information Security Standard compliances** SOX, GLBA, HIPAA, ISO, FISMA, NERC, PCI.	6

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination.

In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Nina Godbole, Sunit Belapure, *Cyber Security*, Wiley India, New Delhi
2. The Indian Cyber Law by Suresh T. Vishwanathan; Bharat Law House New Delhi
3. The Information technology Act, 2000; Bare Act- Professional Book Publishers, New Delhi.
4. Cyber Law & Cyber Crimes By Advocate Prashant Mali; Snow White Publications, Mumbai
5. Nina Godbole, *Information Systems Security,* Wiley India, New Delhi
6. Kennetch J. Knapp, *Cyber Security &Global Information Assurance* Information Science Publishing.
7. William Stallings*, Cryptography and Network Security,* Pearson Publication

8. Websites for more information is available on : The Information Technology ACT, 2008- TIFR : https://www.tifrh.res.in
9. Website for more information , A Compliance Primer for IT professional : https://www.sans.org/reading-room/whitepapers/compliance/compliance-primer-professionals-33538

Course Code	Course Name	Credits
ILO7017	Disaster Management and Mitigation Measures	03

Objectives:

1. To understand physics and various types of disaster occurring around the world
2. To identify extent and damaging capacity of a disaster
3. To study and understand the means of losses and methods to overcome /minimize it.
4. To understand role of individual and various organization during and after disaster
5. To understand application of GIS in the field of disaster management
6. To understand the emergency government response structures before, during and after disaster

Outcomes: Learner will be able to…

1. Get to know natural as well as manmade disaster and their extent and possible effects on the economy.
2. Plan of national importance structures based upon the previous history.
3. Get acquainted with government policies, acts and various organizational structure associated with an emergency.
4. Get to know the simple do's and don'ts in such extreme events and act accordingly.

Module	Detailed Contents	Hrs
01	**Introduction** 1.1 Definition of Disaster, hazard, global and Indian scenario, general perspective, importance of study in human life, Direct and indirect effects of disasters, long term effects of disasters. Introduction to global warming and climate change.	03
02	**Natural Disaster and Manmade disasters:** 2.1 Natural Disaster: Meaning and nature of natural disaster, Flood, Flash flood, drought, cloud burst, Earthquake, Landslides, Avalanches, Volcanic eruptions, Mudflow, Cyclone, Storm, Storm Surge, climate change, global warming, sea level rise, ozone depletion **2.2** Manmade Disasters: Chemical, Industrial, Nuclear and Fire Hazards. Role of growing population and subsequent industrialization, urbanization and changing lifestyle of human beings in frequent occurrences of manmade disasters.	09
03	**Disaster Management, Policy and Administration** 3.1 Disaster management: meaning, concept, importance, objective of disaster management policy, disaster risks in India, Paradigm shift in disaster management.	06

	3.2 Policy and administration: Importance and principles of disaster management policies, command and co-ordination of in disaster management, rescue operations-how to start with and how to proceed in due course of time, study of flowchart showing the entire process.	
04	**Institutional Framework for Disaster Management in India:** 4.1 Importance of public awareness, Preparation and execution of emergency management programme.Scope and responsibilities of National Institute of Disaster Management (NIDM) and National disaster management authority (NDMA) in India.Methods and measures to avoid disasters, Management of casualties, set up of emergency facilities, importance of effective communication amongst different agencies in such situations. 4.2 Use of Internet and softwares for effective disaster management. Applications of GIS, Remote sensing and GPS in this regard.	06
05	**Financing Relief Measures:** 5.1 Ways to raise finance for relief expenditure, role of government agencies and NGO's in this process, Legal aspects related to finance raising as well as overall management of disasters. Various NGO's and the works they have carried out in the past on the occurrence of various disasters, Ways to approach these teams. 5.2 International relief aid agencies and their role in extreme events.	09
06	**Preventive and Mitigation Measures:** 6.1 Pre-disaster, during disaster and post-disaster measures in some events in general 6.2 Structural mapping: Risk mapping, assessment and analysis, sea walls and embankments, Bio shield, shelters, early warning and communication 6.3 Non Structural Mitigation: Community based disaster preparedness, risk transfer and risk financing, capacity development and training, awareness and education, contingency plans. 6.4 Do's and don'ts in case of disasters and effective implementation of relief aids.	06

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. 'Disaster Management' by Harsh K.Gupta, Universities Press Publications.
2. 'Disaster Management: An Appraisal of Institutional Mechanisms in India' by O.S.Dagur, published by Centre for land warfare studies, New Delhi, 2011.
3. 'Introduction to International Disaster Management' by Damon Copolla, Butterworth Heinemann Elseveir Publications.
4. 'Disaster Management Handbook' by Jack Pinkowski, CRC Press Taylor and Francis group.
5. 'Disaster management & rehabilitation' by Rajdeep Dasgupta, Mittal Publications, New Delhi.
6. 'Natural Hazards and Disaster Management, Vulnerability and Mitigation – R B Singh, Rawat Publications
7. Concepts and Techniques of GIS –C.P.Lo Albert, K.W. Yonng – Prentice Hall (India) Publications.

(Learners are expected to refer reports published at national and International level and updated information available on authentic web sites)

Course Code	Course Name	Credits
ILO 7018	Energy Audit and Management	03

Objectives:

1. To understand the importance energy security for sustainable development and the fundamentals of energy conservation.
2. To introduce performance evaluation criteria of various electrical and thermal installations to facilitate the energy management
3. To relate the data collected during performance evaluation of systems for identification of energy saving opportunities.

Outcomes: Learner will be able to…

1. To identify and describe present state of energy security and its importance.
2. To identify and describe the basic principles and methodologies adopted in energy audit of an utility.
3. To describe the energy performance evaluation of some common electrical installations and identify the energy saving opportunities.
4. To describe the energy performance evaluation of some common thermal installations and identify the energy saving opportunities
5. To analyze the data collected during performance evaluation and recommend energy saving measures

Module	Detailed Contents	Hrs
01	**Energy Scenario:** Present Energy Scenario, Energy Pricing, Energy Sector Reforms, Energy Security, Energy Conservation and its Importance, Energy Conservation Act-2001 and its Features. Basics of Energy and its various forms, Material and Energy balance	04
02	**Energy Audit Principles:** Definition, Energy audit- need, Types of energy audit, Energy management (audit) approach-understanding energy costs, Bench marking, Energy performance, Matching energy use to requirement, Maximizing system efficiencies, Optimizing the input energy requirements, Fuel and energy substitution. Elements of monitoring& targeting; Energy audit Instruments; Data and information-analysis. Financial analysis techniques: Simple payback period, NPV, Return on investment (ROI), Internal rate of return (IRR)	08
03	**Energy Management and Energy Conservation in Electrical System:** Electricity billing, Electrical load management and maximum demand Control;	10

	Power factor improvement, Energy efficient equipments and appliances, star ratings. **Energy efficiency measures in lighting system, Lighting control:** Occupancy sensors, daylight integration, and use of intelligent controllers. Energy conservation opportunities in: water pumps, industrial drives, induction motors, motor retrofitting, soft starters, variable speed drives.	
04	**Energy Management and Energy Conservation in Thermal Systems:** Review of different thermal loads; Energy conservation opportunities in: Steam distribution system, Assessment of steam distribution losses, Steam leakages, Steam trapping, Condensate and flash steam recovery system. General fuel economy measures in Boilers and furnaces, Waste heat recovery, use of insulation- types and application. HVAC system: Coefficient of performance, Capacity, factors affecting Refrigeration and Air Conditioning system performance and savings opportunities.	10
05	**Energy Performance Assessment:** On site Performance evaluation techniques, Case studies based on: Motors and variable speed drive, pumps, HVAC system calculations; Lighting System: Installed Load Efficacy Ratio (ILER) method, Financial Analysis.	04
06	**Energy conservation in Buildings:** Energy Conservation Building Codes (ECBC): Green Building, LEED rating, Application of Non-Conventional and Renewable Energy Sources	03

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Handbook of Electrical Installation Practice, Geofry Stokes, Blackwell Science
2. Designing with light: Lighting Handbook, By Anil Valia, Lighting System
3. Energy Management Handbook, By W.C. Turner, John Wiley and Sons
4. Handbook on Energy Audits and Management, edited by A. K. Tyagi, Tata Energy Research Institute (TERI).
5. Energy Management Principles, C.B.Smith, Pergamon Press
6. Energy Conservation Guidebook, Dale R. Patrick, S. Fardo, Ray E. Richardson, Fairmont Press
7. Handbook of Energy Audits, Albert Thumann, W. J. Younger, T. Niehus, CRC Press
8. www.energymanagertraining.com
9. www.bee-india.nic.in

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL701	Instrumentation System Design Laboratory	---	02	---	04	---	---	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Examination			
		Test I	Test II	Average				
ELXL701	Instrumentation System Design Laboratory	---	---	---	---	25	25	50

Term Work :-

At least 06 experiments covering entire syllabus of ELX 701 (Instrumentation System Design) should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus. Equal weightage should be given to laboratory experiments and project while assigning term work marks.

Suggested List of Experiments :-

1. Study of pneumatic single acting & double acting cylinder
2. Study of hydraulic process control valves
3. Design of stepper motor interface & controller
4. Design of instrumentation amplifier for variable voltage gain
5. Design of signal conditioning circuits for LDR / thermistor / RTD / strain gauge
6. Design of linearization circuits for transducers
7. Design of temperature P+I+D controller
8. Tuning of P+I+D controller using MATLAB / Simulink
9. Implementation of PLC ladder diagram for given application
10. Study of SCADA & HMI
11. Designing of data acquisition system (DAS)
12. Simulating a simple process using LabVIEW

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL702	Power Electronics	---	02	---	04	---	---	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Examination			
		Test I	Test II	Average				
ELXL702	Power Electronics	---	---	---	---	25	25	50

Term Work :-

At least 06 experiments covering entire syllabus of ELX 702 (Power Electronics) should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will

be based on the entire syllabus. Equal weightage should be given to laboratory experiments and project while assigning term work marks. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Suggested List of Experiments

1. Characteristics of SCR, DIAC, TRAIC.
2. Characteristics of IGBT, MOSFET and Power BJT.
3. Firing circuit for SCR using UJT.
4. Study of Half wave and Full wave rectifiers using diodes.
5. Study of Half wave and Full wave controlled rectifiers.
6. Buck converter, Boost converter and Buck-Boost converter.
7. Study of Cycloconverter.
8. Simulation of single phase Half wave and Full wave rectifier circuit.
9. Simulation of controlled rectifier with R and RL load.
10. Simulation of controlled rectifier with (i) Source Inductance (ii) Freewheeling diode.

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL703	Digital Signal Processing	---	02	---	04	---	---	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Examination			
		Test I	Test II	Average				
ELXL703	Digital Signal Processing	---	---	---	---	25	25	50

Instructions

1. Minimum 6 experiments and one course project must be submitted by each student.
2. Simulation tools like Matlab/Scilab can be used.
3. Processor based experiments/mini projects can be included.
   The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced

Tentative List of Experiments:

1. Study of Convolution, Series and Parallel Systems
2. Generation of Basic Signals
3. Computation of DFT and it's inverse
4. Computation of FFT and comparison of frequency response of DFT and FFT
5. Computation of DFT
6. IIR Butterworth filter design using IIT technique
7. IIR Chebyshev filter design using BLT technique
8. Design of FIR filter using hamming and hanning window, low pass and high pass filter

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDOLO7031	NEURAL NETWORKS & FUZZY LOGIC	---	02	---	04	---	---	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Examination			
		Test I	Test II	Average				
ELXDOLO7031	NEURAL NETWORKS & FUZZY LOGIC	---	---	---	---	25	25	50

Term Work:

The term work shall consist of

1. At least ***six experiments*** using MATLAB Or C/C++ or Java covering the whole of syllabus, duly recorded and graded.
2. ***One seminar and Two assignments*** to be included covering at least 60% of the syllabus.

The distribution of marks for term work shall be as follows:

The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced *The final certification and acceptance of term-work ensures the satisfactory performance of laboratory work and minimum passing in the term-work.*

Suggested List of experiments: using C/C++ or Matlab or java

- Activation functions
- McCulloch Pitts Neuron Model
- Hebbian learning
- Single layer perceptron neural network
- Multi-layer perceptron neural network

- Error Back propagation neural network
- Kohonen Self-organizing Feature Maps
- Associative memory network
- Fuzzy relations
- Defuzzification methods

Suggested List of seminar :

- Classification of upper case and lower case letters.
- Classification of numbers 0-9.
- BPN for training a hidden layer.
- Implement a heteroassociative memory network to implement any pattern.
- Implement discrete Hopfield network for letters A-E.
- Implement BAM for a pattern of 5X3 array.
- Fuzzy Logic controller design – washing machine / vehicle speed control.

Oral Examination:

Oral will be based on any experiment performed from the list of experiment given in the syllabus and the entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXLDLO7032	Advanced Networking Technologies Laboratory	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXLDLO7032	Advanced Networking Technologies Laboratory	-	-	-	-	25	--	25	50

Course Objectives:

Lab session includes **seven experiments plus one presentation** on any one of the suggested topics The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced

Suggested Experiments:

1. Evaluation of home/campus network
2. GSM-GPS protocol implementation
3. Bluetooth protocol implementation
4. ZigBee protocol implementation
5. Wi-Fi protocol implementation
6. Study of NMAP
7. Study of SNMP
8. Study of Ethernet.

Suggested topics for presentation:

1. MANET
2. VOFR
3. VOIP
4. X.25
5. Body area network
6. RFID
7. Web Security
8. Compression Techniques
9. Security attacks
10. NAT
11. College campus network

12. Fiber Optics types, advantages disadvantages
13. WSN

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXLDLO7033	Robotics	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXLDLO7033	Robotics	-	-	-	-	25	--	25	50

Term Work:

The term work shall consist of

3. At least ***eight experiments*** using MATLAB / Scilab covering the whole of syllabus, duly recorded and graded.
4. ***Two assignments*** to be included covering at least 60% of the syllabus.

The distribution of marks for term work shall be as follows:

The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced *The final certification and acceptance of term-work ensures the satisfactory performance of laboratory work and minimum passing in the term-work.*

Suggested List of experiments: using Matlab / Scilab

- Forward kinematics
- Inverse kinematic
- Dynamic analysis
- Joint-space trajectory
- Cartesian-space trajectory
- Template matching
- Iterative processing
- Segmentation

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXLDLO7034	IC Technology	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXLDLO7034	IC Technology	-	-	-	-	25	--	25	50

Course Objectives:

Lab session includes **seven experiments plus one presentation** on any one of the suggested topics. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced

Suggested Experiments:

Following list of experiments covers the complete syllabus prescribed in IC Technology course. It is formulated in such a way that it allows student to explore various process, layout and device simulation tools. Detail analysis of observations should be recorded in the project book. Tools to be used are Microwind, SUPREME, Electric, Visual TCAD, Mentor Graphics Pyxis and tools available on nanohub. Linux based operating system is preferred to do simulations.

1. Draw and simulate layout for the CMOS inverter. Carry out static as well as transient simulation. Analyze CMOS inverter for i) $(W/L)_{pmos}>(W/L)_{nmos}$ ii) $(W/L)_{pmos}=(W/L)_{nmos}$ iii) $(W/L)_{pmos}<(W/L)_{nmos}$. Do parasitic extraction. Feed these parasitic in circuit simulator and do layout versus schematic verification.

2. Draw and simulate layout for the following circuits. Size them with respect to reference inverter.

   a. CMOS NAND
   b. CMOS NOR

Also observe the effect of different types of design rules on above circuits and tabulate the comparative results.

3. Draw and simulate layout for the given equation (each student will get different equation [y= $\overline{A.B + C.D}$]) with the following design style

   a. Static CMOS
   b. Transmission gate
   c. Dynamic Logic

4. Draw and simulate layout for 6T SRAM cell. Size the SRAM cell for 1) lowest area 2) high reliability

5. Draw and simulate layout for the following circuits.

a. SR latch

b. D flip Flop

6. Simulate oxidation process with Deal-Grove model for different conditions (e.g. oxidation type, orientation, time, temperature, thickness etc.) and comment on the results obtained.

7. Simulate diffusion process for different conditions (e.g. source, time, temperature, dopant etc.) and comment on the results obtained.

8. Simulate Si PN junction for various structure and environmental conditions and comment on the results obtained. Repeat the entire simulation for Ge diode.

9. Simulate MOS capacitor (Classical Simulation) for single gate device for a typical value of fixed charge density and interface trap charge density in gate insulator. Do the AC analysis and comment on the results obtained.

10. Simulate MOS capacitor (Quantum Simulation) for single gate device for a typical value of fixed charge density and interface trap charge density in gate insulator. Do the AC analysis and comment on the results obtained.

Suggested topics for presentation:

Presentation on any Novel device or process.

B.E. (Electronics Engineering) – Semester VIII

`Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELX 801	Internet of Things	4	2	--	4	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELX 801	Internet of Things	20	20	20	80	-	--	--	100

Course Pre-requisite: ELX 501 :- Micro-controllers and Applications
ELX 601:- Embedded System and RTOS
ELX602:- Computer Communication Network
ELXDLO-2 Wireless Communication

Course Objectives:

The objectives of this course are to:

1. Understand the design features of Internet of Things(IoT)
2. Understand importance of data handling in IoT Way.
3. Introduce multiple way of data communication and networking.
4. Understand design issue in IoT

Course Outcomes:

On successful completion of the course the students will be able to:

1. Understand the concepts of Internet of Things
2. Analyze basic web connectivity in IoT
3. Understand Data handling in IoT
4. Design basic applications based on IoT using specific components

Module No.	Unit No.	Topics	Hrs.
1.		**Introduction to IoT**	**08**
	1.1	**Introduction;-**Defining IoT, Characteristics of IoT, Physical design of IoT, Logical design of IoT, Functional blocks of IoT, Sources of IoT, and M2MCommunication.	
	1.2	**Iot and M2m:-** IoT/M2M System layers and Design Standardization, Difference between IoT and M2M	
2.		**Network & Communication aspects**	**10**

	2.1	**Design Principles & Web Connectivity:-** Web Communication Protocols for connected devices, Web connectivity using Gateway, SOAP, REST, HTTP, RESTful and WebSockets (Publish –Subscribe),MQTT, AMQP, CoAP Protocols	
	2.2	**Internet Connectivity: -** Internet connectivity, Internet based communication, IP addressing in IoT, Media Access Control, Application Layer Protocols. LPWAN Fundamentals :LORA ,NBIoT,CAT LTE M1,SIGFOX	
3.0		**IoT Platforms and Design Methodology**	**08**
	3.1	**Defining Specifications About:-** Purpose & requirements, process, domain model, information model, service, IoT level, Functional view, Operational view, Device and Component Integration, (case studies)	
	3.2	**IoT Levels:-**IoT Levels and Deployment Templates	
4.0		**Data Handling in IoT**	**10**
	4.1	**Data Acquiring, Organizing, Processing:-** Data acquiring and storage, Organizing the data, Transactions, Business Processes, Integration and Enterprise Systems, Analytics.	
	4.2	**Data Collection and Storage:-** Cloud Computing Paradigm for Data Collection, storage and computing, Cloud Service Models, Xively Cloud for IoT (AWS ,Google APP engine ,Dweet.IO, Firebase)	
5.0		**Components of IoT**	**06**
	5.1	**Exemplary Devices:-** Raspberry Pi, R-Pi Interfaces, Programming R-Pi, Sensor Technology, Sensor Data Communication Protocols, RFID, WSN Technology, Intel Galileo	
6.0		**IoT Case Studies**	**06**
	6.1	Design Layers, complexity, IoT Applications in Premises, Supply Chain and Customer Monitoring.	
	6.2	Home Automation, Smart Cities, Environment, Agriculture, IoT Printer	
		Total	**48**

Recommended Text Books:

5. ArshdeepBahga and Vijay Madisetti, “Internet of Things: A Hands-on Approach, Universities Press.
6. Raj Kamal, “ Internet of Things: Architecture and Design Principles”, McGraw Hill Education ,First edition
7. David Hanes ,Gonzalo salgueiro“IoT Fundamentals Networking Technologies,Protocols and Use Cases for Internet of Things”, Cisco Press, Kindle 2017 Edition
8. Andrew Minteer ,”Analytics for the Internet of Things(IoT)”,Kindle Edition

Reference Books:

1. Adrian McEwen, Hakim Cassimally, : Designing the Internet of Things”, Paperback, First Edition
2. Yashavant Kanetkar , Shrirang Korde :Paperback “21 Internet of Things (IOT) Experiments”
    a. BPB Publications

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of thesyllabus. The average marks of both the tests will be considered as final IA marks.

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4: Remaining questions will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	T/W Practical	Tutorial	Total
ELX802	Analog and Mixed VLSI Design	04	02	-	04	-	-	04

		Examination Scheme								
		Theory Marks				Exam Duration (Hrs)	Term work	Practical	Oral	Total
		Internal Assessment Marks			End Sem Exam (Marks)					
		Test 1	Test 2	Average						
ELX802	Analog and Mixed VLSI Design	20	20	20	80	03	-	-	-	100

Course Pre-requisite:

- ELX302: Electronic Devices and Circuits I
- ELX303: Digital Circuit Design
- ELX402: Electronic Devices and Circuits II
- ELX504: Design With Linear Integrated Circuits
- ELX603: VLSI Design
- ELX DLO-3: IC Technology

Course Objectives:

1. To teach analysis and design of building blocks of CMOS Analog VLSI Circuits.
2. To highlight the issues associated with the CMOS analog VLSI circuit design.
3. To emphasize upon the issues related to mixed signal layout design.

Course Outcomes:

After successful completion of the course student will be able to

1. Discuss tradeoffs involved in analog VLSI Circuits.
2. Analyze building blocks of CMOS analog VLSI circuits.
3. Design building blocks of CMOS analog VLSI circuits
4. Carry out verifications of issues involved in analog and mixed signal circuits

Module No	Unit No	Topics	Hrs
1. 0		**Analog building blocks**	8
	1.1	Need for CMOS analog and mixed signal designs, MOS Transistor as sampling switch, active resistances, current source and sinks,current mirror.	
	1.2	**Voltage References:** Band Gap References, General Considerations, Supply-independent biasing, Temperature independent references, PTAT	

		current generation and Constant Gm biasing	
2.0		**Amplifier Fundamentals**	
	2.1	**Single Stage Amplifiers**: Basic concepts, Gain Bandwidth (GBW), Common-source stage (with resistive load, diode connected load, current-source load, triode load, source degeneration), source follower, common-gate stage, cascode stage, folded cascade stage.	12
	2.2	**Differential Amplifiers:** Single ended and differential operation, Basic differential pair, large signal and small signal behaviours, Common-mode response, Differential pair with MOS loads.	
	2.3	**Noise:** Statistical Characteristics of Noise, Types of Noise, Representation of Noise in circuits, Noise in Single stage amplifiers (CS, CD, CG stages), noise in differential pairs, noise bandwidth, noise figure, noise temperature.	
3.0		**MOS Operational Amplifiers**	
	3.1	**Stability and Frequency Compensation:** General Considerations, Multipole systems, Phase margin, Frequency compensation, compensation of two stage op- amps	8
	3.2	**Op-amp Design:** General Considerations, performance parameters, One-stage op- amps, Two-stage op-amps, Gain Boosting, Common-mode feedback, Input range limitations(ICMR), Slew Rate, Power supply rejection, Noise in op-amps. Design of single ended and double ended two stage Op-amps	
4.0		**Mixed Signal Circuits**	
	4.1	**Basic Concepts:** AMS design flow, ASIC, Full custom design, Semi-custom design, System on Chip, System in package, Hardware software co-design, and mixed signal layout issues.	8
	4.2	**Oscillators:** General considerations, Ring oscillators, LC oscillators, VCO,	
	4.3	**Phase-Locked Loop:** Simple PLL, Charge pump PLL, Non-ideal effects in PLL, Delay locked loops and applications of PLL in integrated circuits	
5.0		**Data Converter Fundamentals**	
	5.1	**Switch Capacitor Circuits:** MOSFETs as switches, Speed considerations, Precision Considerations, Charge injection cancellation, Unity gain buffer, Non- inverting amplifier and integrator.	4
	5.2	Basic CMOS comparator Design, Adaptive biasing, Analog multipliers.	
6.0		**Data Converter Fundamentals and Architectures**	
	6.1	**Fundamentals:** Analog versus discrete time signals, converting analog signals to data signals, sample and hold characteristics. DAC specifications, ADC specifications.	8
	6.2	**DAC architectures:** Digital input code, resistors string, R-2R ladder networks, current steering, charge scaling DACs, Cyclic DAC, pipeline DAC **ADC architectures:** Flash, Two Step Flash, Pipeline ADC, Integrating ADCs, Successive approximation ADCs	
		Total	**48**

Recommended Books:

1. B Razavi, "*Design of Analog CMOS Integrated Circuits*", Tata McGraw Hill, 1st Edition.
2. R. Jacaob Baker, Harry W. Li, David E. Boyce, "*CMOS Circuit Design, Layout, and Simulation*", Wiley, Student Edition
3. P. E. Allen and D. R. Holberg, "*CMOS Analog Circuit Design*", Oxford University Press, 3rd Edition.
4. Gray, Meyer, Lewis, Hurst, "*Analysis and design of Analog Integrated Circuits*", Willey, 5th Edition

Internal Assessment (IA)

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4: Remaining questions will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX DLO8041	Advanced Power Electronics	04	02	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ELX DLO8041	Advanced Power Electronics	20	20	20	80	03	--	--	--	100

Course Pre-requisite:

4. Power Electronics.
5. Linear Control System.
6. BEE

Course Objectives:

3. To enhance the ideas of students for more complex power electronic system.
4. To teach the analytical methods in power electronic systems.
5. To expose the students to various applications of power electronics in electronics equipment, drives and non-conventional energy systems.

Course Outcomes:

After successful completion of the course students will be able to:

1. Thoroughly understand the modern methods of analysis and control of power electronic systems.
2. Carry out the theoretical analysis of the power electronic systems from the 'Systems Theory' point of view.
3. Appreciate the ubiquity of power electronic systems in engineering fields.
4. Simulate and analyse power electronic systems.

Module No.	Unit No.	Contents	Hrs.
1		**Three-phase Rectifiers**	8
	1.1	3-phase half-wave and full-wave controlled rectifiers with R and RL load, Effect of source inductance,	
	1.2	Distortion in line current, calculation of performance parameters.	
2		**Three-phase inverters and control**	8
	2.1	Three phase bridge inverters (120° and 180° conduction mode) with R and RL load	
	2.2	PWM for 3-phase voltage source inverters, Space Vector Modulation (SVM) technique for 3-phase voltage source inverters, hysteresis control.	
3		**DC-DC Converters**	10
	3.1	Average model, linearized and transfer function models, state-space average models of basic buck, boost and buck-boost converters.	
	3.2	Feedback control of these converters (PI and PID).	
4		**Power Electronic Applications in DC Drives**	8
	4.1	Introduction to DC motors, speed control of DC motor, drives with semi converters, full converters and dual converters.	
	4.2	Chopper-based drive.	
	4.3	Electric braking of DC motors.	
5		**Power Electronic Applications in AC Drives**	10
	5.1	Introduction to three-phase induction motor, speed control methods for three-phase induction motor : i) Stator voltage ii) Variable frequency iii) Rotor resistance iv) V/f control v) Slip power recovery schemes	
6		**Power Electronic Applications**	4
	6.1	Induction heating, dielectric heating, solid state relays,	

	6.2	Energy conversion interface in renewable energy system.	
Total			**48**

Recommended Books:

1. M. Rashid, Power Electronics: Circuits, Devices, and Applications, PHI, 3rd Edition.
2. R. W. Erickson, D. Maksimovic, Fundamentals of Power Electronics, Springer, 2nd Edition.
3. Mohan, Undeland and Robbins, Power Electronics: Converters, Applications and Design, Wiley (Student Edition), 2nd Edition.
4. P. S. Bimbhra, Power Electronics, Khanna Publishers, 2012.
5. M. D. Singh, K. B. Khanchandani, Power Electronics, Tata McGraw Hill, 2nd Edition.
6. J. P. Agrawal, Power Electronics Systems: Theory and Design, Pearson Education, 2002.

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4: Remaining questions will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ELX DLO8042	MEMS Technology	04	02	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme								
		Theory Marks					Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam	Exam duration Hours				
		Test 1	Test 2	Avg of Test 1 and Test 2						
ELX DLO8042	MEMS Technology	20	20	20	80	03	--	--	--	100

Course Pre –requisite: VLSI Design an IC Technology

Course Objectives:

1. To provide knowledge of MEMS processing steps and processing modules
2. To provide knowledge of MEMS Materials with respect to applications.
3. To demonstrate the use of semiconductor based processing modules used in the fabrication of variety of sensors and actuators (e.g. pressure sensors, accelerometers, etc.) at the micro-scale.
4. To provide an understanding of basic design and operation of MEMS sensors, actuators and structures.

Course Outcomes:

1. Understand the underlying fundamental principles of MEMS devices including physical operation and material properties.
2. Design and simulate MEMS devices using standard simulation tools.
3. Develop different concepts of micro system sensors and actuators for real-world applications.
4. Understand the rudiments of Micro-fabrication techniques.

Module No.	Unit No.	Contents	Hrs.
1		**Introduction to MEMS**	4
	1.1	Introduction to MEMS, Comparison with Micro Electronics Technology,	
	1.2	Real world examples (Air-Bag, DMD, Pressure Sensors), MEMS Challenges, MEMS Sensors in Internet of Things (IoT), Bio-medical applications	
2		**MEMS Materials and Their Properties**	8
	2.1	Materials (eg. Si, SiO_2, SiN, SiC, Cr, Au, Al, Ti, SU8, PMMA, Pt)	
	2.2	Important properties: Young modulus, Poisson's ratio, density, piezoresistive coefficients, TCR, Thermal Conductivity, Material Structure.	
3		**MEMS Sensors, Actuators and Structures**	8
	3.1	MEMS Sensing (Capacitive, Piezo electric Piezo resistive)	
	3.2	Micro Actuation Techniques (Thermal, Piezo electric, Electro static, Shape Memory Alloys, **LORENTZ FORCE ACTUATION**), Micro Grippers, Micro Gears, Micro Motors, Micro Valves, Micro Pumps.	
4		**MEMS Fab Processes**	10
	4.1	MEMS Processes & Process parameters: Bulk & Surface Micromachining, High Aspect Ratio Micro	
	4.2	Machining (LIGA, Laser), X-Ray Lithography, Photolithography, PVD techniques, Wet, Dry, Plasma	
	4.3	etching, DRIE, Etch Stop Techniques. Die, Wire & Wafer Bonding, Dicing, Packaging(with Metal	
5		**MEMS Devices**	12
	5.1	Architecture, working and basic behaviour of Cantilevers, Micro heaters, Accelerometers, Pressure Sensor types, Micromirrors in DMD, Inkjet printer-head. Steps involved in Fabricating above devices	
6		**MEMS Device Characterization**	6

	6.1	Piezo-resistance, TCR, Stiffness, Adhesion, Vibration, Resonant frequency, & importance of these measurements in studying device behavior	
	6.2	MEMS Failure Mechanisms and Reliability.	
Total			**48**

Recommended Books:

1. MEMS and MICROSYSTEMS Design and Manufacture by Tai Ran Hsu : McGraw Hill Education
2. An Introduction to Micro-electromechanical Systems Engineering; 2nd Ed - by N. Maluf, K Williams; Publisher: Artech House Inc
3. Micro machined Transducers Sourcebook - by G. Kovacs; Publisher: McGraw-Hill
4. Practical MEMS - by Ville Kaajakari; Publisher: Small Gear Publishing
5. Micro-system Design - by S. Senturia; Publisher: Springer
6. Analysis and Design Principles of MEMS Devices - MinhangBao; Publisher: Elsevier Science
7. Fundamentals of Micro-fabrication - by M. Madou; Publisher: CRC Press; 2 edition
8. Micro machined Transducers Sourcebook - by G. Kovacs; Publisher: McGraw-Hill

Internal Assessment (IA):

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the test will be considered as final IA marks

End Semester Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. Total 4 questions need to be solved.
3: Question No.1 will be compulsory and based on entire syllabus wherein sub questions of 2 to 5 marks will be asked.
4.Remaining questions will be selected from all the modules.

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO 8043	Virtual Instrumentation	04	--	--	04	--	--	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Examination			
		Test I	Test II	Average				
ELXDLO8043	Virtual Instrumentation	20	20	20	80	-	-	100

Rationale :- Virtual instrumentation combines mainstream commercial technologies such as the PC, with flexible software and a wide variety of measurement hardware, so one can create user-defined systems that meet their exact application needs. Virtual instrumentation has led to a simpler way of looking at measurement systems. Instead of using several stand-alone instruments for multiple measurement types and performing rudimentary analysis by hand, engineers now can quickly and cost-effectively create a system equipped with analysis software and a single measurement device that has the capabilities of a multitude of instruments for various applications & measurements.

Course Objectives :-

1. To understand virtual instrumentation (VI) & to realize its architecture
2. To familiarize with VI software & learn programming in VI
3. To study various instruments interfacing & data acquisition methods
4. To understand various analysis tools & develop programs for different measurement applications

Course Outcomes :-

At the end of the course, students should gain the ability to :-

- **CO-1 :-** Explain the concepts of virtual instrumentation
- **CO-2 :-** Select the proper data acquisition hardware
- **CO-3 :-** Configure the data acquisition hardware using LabVIEW
- **CO-4 :-** Use LabVIEW to interface related hardware like transducers
- **CO-5 :-** Design virtual instruments for practical applications

Module No.	Topics	Hours
1	**INTRODUCTION TO VIRTUAL INSTRUMENTATION (VI)**	**06**
1.1	Historical perspective – Need for VI – Advantages of VI – Definition of VI – Block diagram & architecture of VI – Data flow techniques – Graphical programming in data flow – Comparison with conventional programming	
2	**PROGRAMMING TECHNIQUES**	**08**
2.1	VI & sub-VI – Loops & charts – Arrays – Clusters – Graphs – Case & sequence structures – Formula nodes – Local & global variables – String & files inputs	
3	**APPLICATION DEVELOPMENT SOFTWARE (LabVIEW)**	**10**
3.1	Creating virtual instrument in LabVIEW – Implementing dataflow programming in LabVIEW – VI, sub-VI & modular code creation in LabVIEW – Arrays & file I/O in LabVIEW – Textual math integration in LabVIEW – Interfacing external instruments to PC using LabVIEW	
4	**DATA ACQUISITION BASICS**	**08**
4.1	Digital I/O – Counters & timers – PC hardware structure – Timing – Interrupts – DMA – Software & hardware installation – IEEE GPIB 488 concepts – Embedded system buses – PCI – EISA – CPCI	
5	**COMMON INSTRUMENT INTERFACES**	**08**
5.1	Current loop – RS 232C / RS 485 – Interface basics – USB – PCMCIA – VXI – SCXI – PXI – Networking basics for office & industrial application VISA & IVI – Image acquisition & process – Motion control – Digital multimeter (DMM) – Waveform generator	
6	**USING ANALYSIS TOOLS & APPLICATION OF VI**	**08**
6.1	Fourier transform – Power spectrum – Correlation method – Windowing & filtering – Pressure control system – Flow control system – Level control system – Temperature control system – Motion control employing stepper motor – PID controller toolbox	
1 – 6	**TOTAL**	**48**

Recommended Books :-

1. Dr. Sumathi S. & Surekha P, LabVIEW Based Advanced Instrumentation System, PHI, 2nd edition (2007)
2. Gary Johnson, LabVIEW Graphical Programming, McGraw Hill, 2nd edition (2006)
3. Lisa K. Wells & Jeffrey Travis, LabVIEW for Everyone, PHI, 3rd edition (2009)

4. Robert H. Bishop, Learning with LabVIEW 7 Express, Pearson Education, 1st edition (2005)
5. Jovitha Jerome, Virtual Instrumentation using LabVIEW, PHI, 2nd edition (2010)

Internal Assessment (IA) :-

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered as final IA marks.

End Semester Examination :-

1. Question paper will comprise of 6 questions, each carrying 20 marks.

2. The students need to solve total 4 questions.

3. Q.1 will be compulsory and based on entire syllabus.

4. Remaining questions (Q.2 to Q.6) will be set from all modules.

5. Weightage of each module in question paper will be proportional to the number of respective lecture hours mentioned in the syllabus.

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO 8044	Digital Image Processing	04	--	--	04	--	--	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Examination			
		Test I	Test II	Average				
ELXDLO 8044	Digital Image Processing	20	20	20	80	-	-	100

Course Pre-requisite:

- Applied Mathematics
- Signals and Systems

Course Objectives:

1. To learn the fundamental concepts of Digital Image Processing through basic spatial and frequency domain techniques.
2. To learn Image Compression and Decompression Techniques and compression standards.

Course Outcomes:

After successful completion of the course student will be able to

1. Understand the fundamentals of Digital Image representation and simple pixel relations.
2. Explain spatial domain and frequency domain techniques for digital image enhancement.
3. Perform segmentation and morphological operations.
4. Apply compression and decompression techniques to different digital images.

Module No.	Unit No.	Topics	Hrs.
1		**Digital Image Processing Fundamentals**	04
	1.1	**Introduction:** Background, Representation of a Digital Image, Fundamental Steps in Image Processing, Elements of a Digital Image Processing System	
	1.2	**Digital Image Fundamentals:** Elements of Visual Perception, A Simple Image Model, Two dimensional Sampling and Quantization, Tonal and Spatial Resolutions, Some Basic Relationships between Pixels, Image File Formats : BMP, TIFF and JPEG. Color Models (RGB, HSI, YUV)	
2		**Image Enhancement in Spatial Domain**	08
	2.1	Enhancement in the spatial domain: Some Simple Intensity Transformations, Histogram Processing, Image Subtraction, Image Averaging, Spatial domain filters: Smoothing Filters, Sharpening Filters,High boost filter	
3		**Image Segmentation and Representation**	08
	3.1	Detection of Discontinuities, Edge Linking using Hough Transform, Thresholding, Region based Segmentation, Split and Merge Technique	
	3.2	Image Representation and Description, Chain Code, Polygonal Representation, Shape Number, Two Dimensional Moments.	
4		**Binary Image Processing**	06
	4.1	Binary Morphological Operators, Dilation and Erosion, Opening and Closing, Hit-or-Miss Transformation, Boundary Extraction, Region Filling, Thinning and Thickening, Medial Axis Transform, Connected Component Labeling	
5		**Image Transforms and frequency domain processing**	12
	5.1	Introduction to 2 Dimensional Fourier Transform, Discrete Fourier Transform, Properties of the Two-Dimensional Fourier Transform, Fast Fourier Transform(FFT), Computation of 2 DFFT	
	5.2	Discrete Hadamard Transform(DHT), Fast Hadamard Transform(FHT), Discrete	

		Cosine Transform(DCT), Introduction to Discrete Wavelet Transform (DWT)	
	5.3	Enhancement in the frequency domain: Frequency Domain Filtering Lowpass Filtering, Highpass Filtering, Homomorphic Filtering, Generation of Spatial Masks from Frequency Domain Specifications	
6		**Image Compression:**	10
	6.1	Fundamentals :Coding Redundancy, Interpixel Redundancy, Psycho visual	
		Redundancy	
	6.2	Image Compression Models :The Source Encoder and Decoder, Lossless	
		Compression Techniques : Run Length Coding, Arithmetic Coding, Huffman	
		Coding, Differential PCM,	
	6.3	Lossy Compression Techniques: Predictive Coding, Delta modulation, Improved Gray Scale Quantization, Transform Coding, JPEG, MPEG-1. , Fidelity Criteria.	
Total			**48**

Text Books:

1. Rafel C. Gonzalez and Richard E. Woods, 'Digital Image Processing', Pearson Education Asia, Third Edition, 2009,
2. Anil K. Jain, "Fundamentals and Digital Image Processing", Prentice Hall of India Private Ltd, Third Edition

Reference Books:

1. S. Jayaraman, E.Esakkirajan and T.Veerkumar, "Digital Image Processing" TataMcGraw Hill Education Private Ltd, 2009,
2. Milan Sonka, Vaclav Hlavac, and Roger Boyle, "Image Processing, Analysis, and Machine Vision", Second Edition, Thomson Learning, 2001
3. William K. Pratt, "Digital Image Processing", Third Edition, John Wiley & Sons, Inc., 2001

Internal Assessment (IA) :-

Two tests must be conducted which should cover at least 80% of syllabus. The average marks of both the tests will be considered as final IA marks.

End Semester Examination :-

1. Question paper will comprise of 6 questions, each carrying 20 marks.

2. The students need to solve total 4 questions.

3. Q.1 will be compulsory and based on entire syllabus.

4. Remaining questions (Q.2 to Q.6) will be set from all modules.

5. Weightage of each module in question paper will be proportional to the number of respective lecture hours mentioned in the syllabus.

e Code	Course Name	Credits
ILO8021	Project Management	03

Objectives:

1. To familiarize the students with the use of a structured methodology/approach for each and every unique project undertaken, including utilizing project management concepts, tools and techniques.
2. To appraise the students with the project management life cycle and make them knowledgeable about the various phases from project initiation through closure.

Outcomes: Learner will be able to…

1. Apply selection criteria and select an appropriate project from different options.
2. Write work break down structure for a project and develop a schedule based on it.
3. Identify opportunities and threats to the project and decide an approach to deal with them strategically.
4. Use Earned value technique and determine & predict status of the project.
5. Capture lessons learned during project phases and document them for future reference

Module	Detailed Contents	Hrs
01	**Project Management Foundation:** Definition of a project, Project Vs Operations, Necessity of project management, Triple constraints, Project life cycles (typical & atypical) Project phases and stage gate process. Role of project manager. Negotiations and resolving conflicts. Project management in various organization structures. PM knowledge areas as per Project Management Institute (PMI).	5
02	**Initiating Projects:** How to get a project started, Selecting project strategically, Project selection models (Numeric /Scoring Models and Non-numeric models), Project portfolio process, Project sponsor and creating charter; Project proposal. Effective project team, Stages of team development & growth (forming, storming, norming & performing), team dynamics.	6
03	**Project Planning and Scheduling:** Work Breakdown structure (WBS) and linear responsibility chart, Interface Co-ordination and concurrent engineering, Project cost estimation and budgeting, Top down and bottoms up budgeting, Networking and Scheduling techniques. PERT, CPM,	8

	GANTT chart. Introduction to Project Management Information System (PMIS).	
04	**Planning Projects:** Crashing project time, Resource loading and leveling, Goldratt's critical chain, Project Stakeholders and Communication plan. Risk Management in projects: Risk management planning, Risk identification and risk register. Qualitative and quantitative risk assessment, Probability and impact matrix. Risk response strategies for positive and negative risks	6
05	**5.1 Executing Projects:** Planning monitoring and controlling cycle. Information needs and reporting, engaging with all stakeholders of the projects. Team management, communication and project meetings. **5.2 Monitoring and Controlling Projects:** Earned Value Management techniques for measuring value of work completed; Using milestones for measurement; change requests and scope creep. Project audit. **5.3 Project Contracting** Project procurement management, contracting and outsourcing,	8
06	**6.1 Project Leadership and Ethics:** Introduction to project leadership, ethics in projects. Multicultural and virtual projects. **6.2 Closing the Project:** Customer acceptance; Reasons of project termination, Various types of project terminations (Extinction, Addition, Integration, Starvation), Process of project termination, completing a final report; doing a lessons learned analysis; acknowledging successes and failures; Project management templates and other resources; Managing without authority; Areas of further study.	6

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Jack Meredith & Samuel Mantel, Project Management: A managerial approach, Wiley India, 7th Ed.
2. A Guide to the Project Management Body of Knowledge (PMBOK® Guide), 5th Ed, Project Management Institute PA, USA
3. Gido Clements, Project Management, Cengage Learning.
4. Gopalan, Project Management, , Wiley India
5. Dennis Lock, Project Management, Gower Publishing England, 9 th Ed.

Course Code	Course Name	Credits
ILO8022	Finance Management	03

Objectives:

1. Overview of Indian financial system, instruments and market
2. Basic concepts of value of money, returns and risks, corporate finance, working capital and its management
3. Knowledge about sources of finance, capital structure, dividend policy

Outcomes: Learner will be able to…

1. Understand Indian finance system and corporate finance
2. Take investment, finance as well as dividend decisions

Module	Detailed Contents	Hrs
01	**Overview of Indian Financial System:** Characteristics, Components and Functions of Financial System. **Financial Instruments:** Meaning, Characteristics and Classification of Basic Financial Instruments — Equity Shares, Preference Shares, Bonds-Debentures, Certificates of Deposit, and Treasury Bills. **Financial Markets:** Meaning, Characteristics and Classification of Financial Markets — Capital Market, Money Market and Foreign Currency Market **Financial Institutions:** Meaning, Characteristics and Classification of Financial Institutions — Commercial Banks, Investment-Merchant Banks and Stock Exchanges	06
02	**Concepts of Returns and Risks:** Measurement of Historical Returns and Expected Returns of a Single Security and a Two-security Portfolio; Measurement of Historical Risk and Expected Risk of a Single Security and a Two-security Portfolio. **Time Value of Money:** Future Value of a Lump Sum, Ordinary Annuity, and Annuity Due; Present Value of a Lump Sum, Ordinary Annuity, and Annuity Due; Continuous Compounding and Continuous Discounting.	06
03	**Overview of Corporate Finance:** Objectives of Corporate Finance; Functions of Corporate Finance—Investment Decision, Financing Decision, and Dividend Decision. **Financial Ratio Analysis:** Overview of Financial Statements—Balance Sheet, Profit and Loss Account, and Cash Flow Statement; Purpose of Financial Ratio Analysis;	09

	Liquidity Ratios; Efficiency or Activity Ratios; Profitability Ratios; Capital Structure Ratios; Stock Market Ratios; Limitations of Ratio Analysis.	
04	**Capital Budgeting:** Meaning and Importance of Capital Budgeting; Inputs for Capital Budgeting Decisions; Investment Appraisal Criterion—Accounting Rate of Return, Payback Period, Discounted Payback Period, Net Present Value(NPV), Profitability Index, Internal Rate of Return (IRR), and Modified Internal Rate of Return (MIRR) **Working Capital Management:** Concepts of Meaning Working Capital; Importance of Working Capital Management; Factors Affecting an Entity's Working Capital Needs; Estimation of Working Capital Requirements; Management of Inventories; Management of Receivables; and Management of Cash and Marketable Securities.	10
05	**Sources of Finance:** Long Term Sources—Equity, Debt, and Hybrids; Mezzanine Finance; Sources of Short Term Finance—Trade Credit, Bank Finance, Commercial Paper; Project Finance. **Capital Structure:** Factors Affecting an Entity's Capital Structure; Overview of Capital Structure Theories and Approaches— Net Income Approach, Net Operating Income Approach; Traditional Approach, and Modigliani-Miller Approach. Relation between Capital Structure and Corporate Value; Concept of Optimal Capital Structure	05
06	**Dividend Policy:** Meaning and Importance of Dividend Policy; Factors Affecting an Entity's Dividend Decision; Overview of Dividend Policy Theories and Approaches—Gordon's Approach, Walter's Approach, and Modigliani-Miller Approach	03

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Fundamentals of Financial Management, 13th Edition (2015) by Eugene F. Brigham and Joel F. Houston; Publisher: Cengage Publications, New Delhi.
2. Analysis for Financial Management, 10th Edition (2013) by Robert C. Higgins; Publishers: McGraw Hill Education, New Delhi.
3. Indian Financial System, 9th Edition (2015) by M. Y. Khan; Publisher: McGraw Hill Education, New Delhi.
4. Financial Management, 11th Edition (2015) by I. M. Pandey; Publisher: S. Chand (G/L) & Company Limited, New Delhi.

Course Code	Course Name	Credits
ILO8023	Enterpreneurship Development and Management	03

Objectives:

1. To acquaint with entrepreneurship and management of business
2. Understand Indian environment for entrepreneurship
3. Idea of EDP, MSME

Outcomes: Learner will be able to…

1. Understand the concept of business plan and ownerships
2. Interpret key regulations and legal aspects of entrepreneurship in India
3. Understand government policies for entrepreneurs

Module	Detailed Contents	Hrs
01	**Overview Of Entrepreneurship:** Definitions, Roles and Functions/Values of Entrepreneurship, History of Entrepreneurship Development, Role of Entrepreneurship in the National Economy, Functions of an Entrepreneur, Entrepreneurship and Forms of Business Ownership Role of Money and Capital Markets in Entrepreneurial Development: Contribution of Government Agencies in Sourcing information for Entrepreneurship	04
02	**Business Plans And Importance Of Capital To Entrepreneurship:** Preliminary and Marketing Plans, Management and Personnel, Start-up Costs and Financing as well as Projected Financial Statements, Legal Section, Insurance, Suppliers and Risks, Assumptions and Conclusion, Capital and its Importance to the Entrepreneur **Entrepreneurship And Business Development:** Starting a New Business, Buying an Existing Business, New Product Development, Business Growth and the Entrepreneur Law and its Relevance to Business Operations	09
03	Women's Entrepreneurship Development, Social entrepreneurship-role and need, EDP cell, role of sustainability and sustainable development for SMEs, case studies, exercises	05
04	**Indian Environment for Entrepreneurship:** key regulations and legal aspects , MSMED Act 2006 and its implications, schemes and policies of the Ministry of MSME, role and responsibilities of various government organisations, departments, banks etc., Role of State governments in terms of infrastructure developments and support etc.,	08

	Public private partnerships, National Skill development Mission, Credit Guarantee Fund, PMEGP, discussions, group exercises etc	
05	**Effective Management of Business:** Issues and problems faced by micro and small enterprises and effective management of M and S enterprises (risk management, credit availability, technology innovation, supply chain management, linkage with large industries), exercises, e-Marketing	08
06	**Achieving Success In The Small Business:** Stages of the small business life cycle, four types of firm-level growth strategies, Options – harvesting or closing small business Critical Success factors of small business	05

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Poornima Charantimath, Entrepreneurship development- Small Business Enterprise, Pearson
2. Education Robert D Hisrich, Michael P Peters, Dean A Shapherd, Entrepreneurship, latest edition, The McGrawHill Company
3. Dr TN Chhabra, Entrepreneurship Development, Sun India Publications, New Delhi
4. Dr CN Prasad, Small and Medium Enterprises in Global Perspective, New century Publications, New Delhi
5. Vasant Desai, Entrepreneurial development and management, Himalaya Publishing House
6. Maddhurima Lall, Shikah Sahai, Entrepreneurship, Excel Books
7. Rashmi Bansal, STAY hungry STAY foolish, CIIE, IIM Ahmedabad
8. Law and Practice relating to Micro, Small and Medium enterprises, Taxmann Publication Ltd.
9. Kurakto, Entrepreneurship- Principles and Practices, Thomson Publication
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11. www.msme.gov.in
12. www.dcmesme.gov.in
13. www.msmetraining.gov.in

Course Code	Course Name	Credits
ILO8024	Human Resource Management	03

Objectives:

1. To introduce the students with basic concepts, techniques and practices of the human resource management.
2. To provide opportunity of learning Human resource management (HRM) processes, related with the functions, and challenges in the emerging perspective of today's organizations.
3. To familiarize the students about the latest developments, trends & different aspects of HRM.
4. To acquaint the student with the importance of inter-personal & inter-group behavioral skills in an organizational setting required for future stable engineers, leaders and managers.

Outcomes: Learner will be able to…

1. Understand the concepts, aspects, techniques and practices of the human resource management.
2. Understand the Human resource management (HRM) processes, functions, changes and challenges in today's emerging organizational perspective.
3. Gain knowledge about the latest developments and trends in HRM.
4. Apply the knowledge of behavioral skills learnt and integrate it with in inter personal and intergroup environment emerging as future stable engineers and managers.

Module	Detailed Contents	Hrs
01	**Introduction to HR** • Human Resource Management- Concept, Scope and Importance, Interdisciplinary Approach Relationship with other Sciences, Competencies of HR Manager, HRM functions. • Human resource development (HRD): changing role of HRM – Human resource Planning, Technological change, Restructuring and rightsizing, Empowerment, TQM, Managing ethical issues.	5
02	**Organizational Behavior (OB)** • Introduction to OB Origin, Nature and Scope of Organizational Behavior, Relevance to Organizational Effectiveness and Contemporary issues • Personality: Meaning and Determinants of Personality, Personality development, Personality Types, Assessment of Personality Traits for Increasing Self Awareness • Perception: Attitude and Value, Effect of perception on Individual Decision-	7

	making, Attitude and Behavior. • Motivation: Theories of Motivation and their Applications for Behavioral Change (Maslow, Herzberg, McGregor); • Group Behavior and Group Dynamics: Work groups formal and informal groups and stages of group development. Team Effectiveness: High performing teams, Team Roles, cross functional and self-directed team. • Case study	
03	**Organizational Structure &Design** • Structure, size, technology, Environment of organization; Organizational Roles & conflicts: Concept of roles; role dynamics; role conflicts and stress. • Leadership: Concepts and skills of leadership, Leadership and managerial roles, Leadership styles and contemporary issues in leadership. • Power and Politics: Sources and uses of power; Politics at workplace, Tactics and strategies.	6
04	**Human resource Planning** • Recruitment and Selection process, Job-enrichment, Empowerment - Job-Satisfaction, employee morale. • Performance Appraisal Systems: Traditional & modern methods, Performance Counseling, Career Planning. • Training & Development: Identification of Training Needs, Training Methods	5
05	**Emerging Trends in HR** • Organizational development; Business Process Re-engineering (BPR), BPR as a tool for organizational development , managing processes & transformation in HR. Organizational Change, Culture, Environment • Cross Cultural Leadership and Decision Making**:** Cross Cultural Communication and diversity at work**,** Causes of diversity, managing diversity with special reference to handicapped, women and ageing people, intra company cultural difference in employee motivation.	6
06	**HR & MIS** Need, purpose, objective and role of information system in HR, Applications in HRD in various industries (e.g. manufacturing R&D, Public Transport, Hospitals, Hotels and service industries **Strategic HRM** Role of Strategic HRM in the modern business world, Concept of Strategy, Strategic Management Process, Approaches to Strategic Decision Making; Strategic Intent – Corporate Mission, Vision, Objectives and Goals	10

	Labor Laws & Industrial Relations Evolution of IR, IR issues in organizations, Overview of Labor Laws in India; Industrial Disputes Act, Trade Unions Act, Shops and Establishments Act	

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Stephen Robbins, Organizational Behavior, 16th Ed, 2013
2. V S P Rao, Human Resource Management, 3rd Ed, 2010, Excel publishing
3. Aswathapa, Human resource management: Text & cases, 6th edition, 2011
4. C. B. Mamoria and S V Gankar, Dynamics of Industrial Relations in India, 15th Ed, 2015, Himalaya Publishing, 15thedition, 2015
5. P. Subba Rao, Essentials of Human Resource management and Industrial relations, 5th Ed, 2013, Himalaya Publishing
6. Laurie Mullins, Management & Organizational Behavior, Latest Ed, 2016, Pearson Publications

Course Code	Course Name	Credits
ILO8025	Professional Ethics and Corporat Social Responsibility (CSR)	03

Objectives:

1. To understand professional ethics in business
2. To recognized corporate social responsibility

Outcomes: Learner will be able to…

1. Understand rights and duties of business
2. Distinguish different aspects of corporate social responsibility
3. Demonstrate professional ethics
4. Understand legal aspects of corporate social responsibility

Module	Detailed Contents	Hrs
01	**Professional Ethics and Business:** The Nature of Business Ethics; Ethical Issues in Business; Moral Responsibility and Blame; Utilitarianism: Weighing Social Costs and Benefits; Rights and Duties of Business	04
02	**Professional Ethics in the Marketplace:** Perfect Competition; Monopoly Competition; Oligopolistic Competition; Oligopolies and Public Policy **Professional Ethics and the Environment:** Dimensions of Pollution and Resource Depletion; Ethics of Pollution Control; Ethics of Conserving Depletable Resources	08
03	**Professional Ethics of Consumer Protection:** Markets and Consumer Protection; Contract View of Business Firm's Duties to Consumers; Due Care Theory; Advertising Ethics; Consumer Privacy **Professional Ethics of Job Discrimination:** Nature of Job Discrimination; Extent of Discrimination; Reservation of Jobs.	06
04	**Introduction to Corporate Social Responsibility:** Potential Business Benefits—Triple bottom line, Human resources, Risk management, Supplier relations; Criticisms and concerns—Nature of business; Motives; Misdirection. Trajectory of Corporate Social Responsibility in India	05
05	**Corporate Social Responsibility:** Articulation of Gandhian Trusteeship	08

	Corporate Social Responsibility and Small and Medium Enterprises (SMEs) in India, Corporate Social Responsibility and Public-Private Partnership (PPP) in India	
06	**Corporate Social Responsibility in Globalizing India:** Corporate Social Responsibility Voluntary Guidelines, 2009 issued by the Ministry of Corporate Affairs, Government of India, Legal Aspects of Corporate Social Responsibility—Companies Act, 2013.	08

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Business Ethics: Texts and Cases from the Indian Perspective (2013) by Ananda Das Gupta; Publisher: Springer.
2. Corporate Social Responsibility: Readings and Cases in a Global Context (2007) by Andrew Crane, Dirk Matten, Laura Spence; Publisher: Routledge.
3. Business Ethics: Concepts and Cases, 7th Edition (2011) by Manuel G. Velasquez; Publisher: Pearson, New Delhi.
4. Corporate Social Responsibility in India (2015) by BidyutChakrabarty, Routledge, New Delhi.

Course Code	Course Name	Credits
ILO8026	Research Methodology	03

Objectives:

1. To understand Research and Research Process
2. To acquaint students with identifying problems for research and develop research strategies
3. To familiarize students with the techniques of data collection, analysis of data and interpretation

Outcomes: Learner will be able to…

1. Prepare a preliminary research design for projects in their subject matter areas
2. Accurately collect, analyze and report data
3. Present complex data or situations clearly
4. Review and analyze research findings

Module	Detailed Contents	Hrs
01	**Introduction and Basic Research Concepts** **1.1** Research – Definition; Concept of Construct, Postulate, Proposition, Thesis, Hypothesis, Law, Principle.Research methods vs Methodology **1.2** Need of Research in Business and Social Sciences **1.3** Objectives of Research **1.4 Issues** and Problems in Research **1.5** Characteristics of Research:Systematic, Valid, Verifiable, Empirical and Critical	**09**
02	**Types of Research** **2.1**. Basic Research **2.2**. Applied Research **2.3.** Descriptive Research **2.4.** Analytical Research **2.5**. Empirical Research **2.6** Qualitative and Quantitative Approaches	**07**

03	**Research Design and Sample Design** **3.1** Research Design – Meaning, Types and Significance **3.2** Sample Design – Meaning and Significance Essentials of a good sampling Stages in Sample Design Sampling methods/techniques Sampling Errors	**07**
04	**Research Methodology** **4.1** Meaning of Research Methodology **4.2**. Stages in Scientific Research Process: **a.** Identification and Selection of Research Problem **b.** Formulation of Research Problem **c.** Review of Literature **d.** Formulation of Hypothesis **e.** Formulation of research Design **f**. Sample Design **g.** Data Collection **h.** Data Analysis **i.** Hypothesis testing and Interpretation of Data **j.** Preparation of Research Report	**08**
05	**Formulating Research Problem** **5.1** Considerations: Relevance, Interest, Data Availability, Choice of data, Analysis of data, Generalization and Interpretation of analysis	**04**
06	**Outcome of Research** **6.1** Preparation of the report on conclusion reached **6.2** Validity Testing & Ethical Issues **6.3** Suggestions and Recommendation	**04**

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or at least 6 assignment on complete syllabus or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Dawson, Catherine, 2002, Practical Research Methods, New Delhi, UBS Publishers Distributors.
2. Kothari, C.R.,1985, Research Methodology-Methods and Techniques, New Delhi, Wiley Eastern Limited.
3. Kumar, Ranjit, 2005, Research Methodology-A Step-by-Step Guide for Beginners, (2^{nd}ed), Singapore, Pearson Education

Course Code	Course Name	Credits
ILO8027	IPR and Patenting	03

Objectives:

1. To understand intellectual property rights protection system
2. To promote the knowledge of Intellectual Property Laws of India as well as International treaty procedures
3. To get acquaintance with Patent search and patent filing procedure and applications

Outcomes: Learner will be able to…

1. understand Intellectual Property assets
2. assist individuals and organizations in capacity building
3. work for development, promotion, protection, compliance, and enforcement of Intellectual Property and Patenting

Module	Detailed Contents	Hr
01	**Introduction to Intellectual Property Rights (IPR)**: Meaning of IPR, Different category of IPR instruments - Patents, Trademarks,Copyrights, Industrial Designs, Plant variety protection, Geographical indications,Transfer of technology etc. **Importance of IPR in Modern Global Economic Environment:** Theories of IPR, Philosophical aspects of IPR laws, Need for IPR, IPR as an instrument of development	05
02	**Enforcement of Intellectual Property Rights:** Introduction, Magnitude of problem, Factors that create and sustain counterfeiting/piracy, International agreements, International organizations (e.g. WIPO, WTO) activein IPR enforcement **Indian Scenario of IPR:**Introduction, History of IPR in India, Overview of IP laws in India, Indian IPR, Administrative Machinery, Major international treaties signed by India, Procedure for submitting patent and Enforcement of IPR at national level etc.	07
03	**Emerging Issues in IPR:**Challenges for IP in digital economy, e-commerce, human genome,biodiversity and traditional knowledge etc.	05
04	**Basics of Patents:**Definition of Patents, Conditions of patentability, Patentable and non-patentable inventions, Types of patent applications (e.g. Patent of addition etc), Process Patent and Product Patent, Precautions while patenting, Patent specification Patent claims, Disclosures and non-disclosures, Patent rights and infringement, Method	07

	of getting a patent	
05	**Patent Rules:** Indian patent act, European scenario, US scenario, Australia scenario, Japan scenario, Chinese scenario, Multilateral treaties where India is a member (TRIPS agreement, Paris convention etc.)	08
06	**Procedure for Filing a Patent (National and International):** Legislation and Salient Features, Patent Search, Drafting and Filing Patent Applications, Processing of patent, Patent Litigation, Patent Publicationetc, Time frame and cost, Patent Licensing, Patent Infringement **Patent databases:** Important websites, Searching international databases	07

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or at least 6 assignments on complete syllabus or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

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4. Only Four question need to be solved.

REFERENCE BOOKS:

1. Rajkumar S. Adukia, 2007, A Handbook on Laws Relating to Intellectual Property Rights in India, The Institute of Chartered Accountants of India
2. Keayla B K, Patent system and related issues at a glance, Published by National Working Group on Patent Laws
3. T Sengupta, 2011, Intellectual Property Law in India, Kluwer Law International
4. Tzen Wong and Graham Dutfield, 2010, Intellectual Property and Human Development: Current Trends and Future Scenario, Cambridge University Press
5. Cornish, William Rodolph & Llewelyn, David. 2010, Intellectual Property: Patents, Copyrights, Trade Marks and Allied Right, 7th Edition, Sweet & Maxwell
6. Lous Harns, 2012, The enforcement of Intellactual Property Rights: A Case Book, 3rd Edition, WIPO
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8. R Radha Krishnan & S Balasubramanian, 2012, Intellectual Property Rights, 1st Edition, Excel Books

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10. Kompal Bansal and Praishit Bansal, 2012, Fundamentals of IPR for Engineers, 1st Edition, BS Publications
11. Entrepreneurship Development and IPR Unit, BITS Pilani, 2007, A Manual on Intellectual Property Rights,
12. Mathew Y Maa, 2009, Fundamentals of Patenting and Licensing for Scientists and Engineers, World Scientific Publishing Company
13. N S Rathore, S M Mathur, Priti Mathur, Anshul Rathi**,** IPR: Drafting,Interpretation of Patent Specifications and Claims**,** New India Publishing Agency
14. Vivien Irish, 2005, Intellectual Property Rights for Engineers,IET
15. Howard B Rockman, 2004, Intellectual Property Law for Engineers and scientists, Wiley-IEEE Press

Course Code	Course Name	Credits
ILO8028	**Digital Business Management**	**03**

Objectives:

1. To familiarize with digital business concept
2. To acquaint with E-commerce
3. To give insights into E-business and its strategies

Outcomes: The learner will be able to …..

1. Identify drivers of digital business
2. Illustrate various approaches and techniques for E-business and management
3. Prepare E-business plan

Module	Detailed content	Hours
1	**Introduction to Digital Business-** Introduction, Background and current status, E-market places, structures, mechanisms, economics and impacts Difference between physical economy and digital economy, **Drivers of digital business**- Big Data & Analytics, Mobile, Cloud Computing, Social media, BYOD, and Internet of Things(digitally intelligent machines/services) Opportunities and Challenges in Digital Business,	09
2	**Overview of E-Commerce** **E-Commerce-** Meaning, Retailing in e-commerce-products and services, consumer behavior, market research and advertisement B2B-E-commerce-selling and buying in private e-markets, public B2B exchanges and support services, e-supply chains, Collaborative Commerce, Intra business EC and Corporate portals Other E-C models and applications, innovative EC System-From E-government and learning to C2C, mobile commerce and pervasive computing EC Strategy and Implementation-EC strategy and global EC, Economics and Justification of EC, Using Affiliate marketing to promote your e-commerce business, Launching a successful online business and EC project, Legal, Ethics and Societal impacts of EC	06

3	**Digital Business Support services**: ERP as e –business backbone, knowledge Tope Apps, Information and referral system **Application Development:** Building Digital business Applications and Infrastructure	06
4	**Managing E-Business**-Managing Knowledge, Management skills for e-business, Managing Risks in e –business Security Threats to e-business -Security Overview, Electronic Commerce Threats, Encryption, Cryptography, Public Key and Private Key Cryptography, Digital Signatures, Digital Certificates, Security Protocols over Public Networks: HTTP, SSL, Firewall as Security Control, Public Key Infrastructure (PKI) for Security, Prominent Cryptographic Applications	06
5	**E-Business Strategy**-E-business Strategic formulation- Analysis of Company's Internal and external environment, Selection of strategy, E-business strategy into Action, challenges and E-Transition (Process of Digital Transformation)	04
6	**Materializing e-business: From Idea to Realization**-Business plan preparation **Case Studies and presentations**	08

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or at least 6 assignment on complete syllabus or course project.

End Semester Theory Examination:

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4. Only Four question need to be solved.

References:

1. A textbook on E-commerce**,** Er Arunrajan Mishra, Dr W K Sarwade,Neha Publishers & Distributors, 2011
2. E-commerce from vision to fulfilment, Elias M. Awad, PHI-Restricted, 2002
3. Digital Business and E-Commerce Management, 6th Ed, Dave Chaffey, Pearson, August 2014
4. Introduction to E-business-Management and Strategy, Colin Combe, ELSVIER, 2006
5. Digital Business Concepts and Strategy, Eloise Coupey, 2nd Edition, Pearson
6. Trend and Challenges in Digital Business Innovation, VinocenzoMorabito, Springer
7. Digital Business Discourse Erika Darics, April 2015, Palgrave Macmillan
8. E-Governance-Challenges and Opportunities in : Proceedings in 2nd International Conference theory and practice of Electronic Governance
9. Perspectives the Digital Enterprise –A framework for Transformation, TCS consulting journal Vol.5
10. Measuring Digital Economy-A new perspective -DOI:10.1787/9789264221796-enOECD Publishing

Course Code	Course Name	Credits
ILO8029	Environmental Management	03

Objectives:

1. Understand and identify environmental issues relevant to India and global concerns
2. Learn concepts of ecology
3. Familiarise environment related legislations

Outcomes: Learner will be able to…

1. Understand the concept of environmental management
2. Understand ecosystem and interdependence, food chain etc.
3. Understand and interpret environment related legislations

Module	Detailed Contents	Hrs
01	Introduction and Definition of Environment: Significance of Environment Management for contemporary managers, Career opportunities. Environmental issues relevant to India, Sustainable Development, The Energy scenario.	10
02	Global Environmental concerns : Global Warming, Acid Rain, Ozone Depletion, Hazardous Wastes, Endangered life-species, Loss of Biodiversity, Industrial/Man-made disasters, Atomic/Biomedical hazards, etc.	06
03	Concepts of Ecology: Ecosystems and interdependence between living organisms, habitats, limiting factors, carrying capacity, food chain, etc.	05
04	Scope of Environment Management, Role & functions of Government as a planning and regulating agency. Environment Quality Management and Corporate Environmental Responsibility	10
05	Total Quality Environmental Management, ISO-14000, EMS certification.	05
06	General overview of major legislations like Environment Protection Act, Air (P & CP) Act, Water (P & CP) Act, Wildlife Protection Act, Forest Act, Factories Act, etc.	03

Assessment:

Internal:

Assessment consists of two tests out of which; one should be compulsory class test and the other is either a class test or assignment on live problems or course project.

End Semester Theory Examination:

Some guidelines for setting up the question paper. Minimum 80% syllabus should be covered in question papers of end semester examination. **In question paper weightage of each module will be proportional to number of respective lecture hours as mention in the syllabus.**

1. Question paper will comprise of total six question
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

REFERENCES:

1. Environmental Management: Principles and Practice, C J Barrow, Routledge Publishers London, 1999
2. A Handbook of Environmental Management Edited by Jon C. Lovett and David G. Ockwell, Edward Elgar Publishing
3. Environmental Management,T V Ramachandra and Vijay Kulkarni, TERI Press
4. Indian Standard Environmental Management Systems — Requirements With Guidance For Use, Bureau Of Indian Standards, February 2005
5. Environmental Management: An Indian Perspective, S N Chary and Vinod Vyasulu, Maclillan India, 2000
6. Introduction to Environmental Management, Mary K Theodore and Louise Theodore, CRC Press
7. Environment and Ecology, Majid Hussain, 3rd Ed. Access Publishing.2015

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL 801	Internet of Things Laboratory	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXL 801	Internet of Things Laboratory	-	-	-	-	25	--	25	50

Course Objectives:

Lab session includes **seven experiments plus one presentation on case study.** The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Suggested Experiments:
(Programming using C, Embedded C, Pyhton is to be encouraged)

1. Minimum two Experiments using any hardware platform (Arduino/Raspberry Pi/BeagleBone/Galileo) for data handling and storage.
2. Minimum three experiments using any hardware platform (Arduino/Raspberry Pi/BeagleBone/Galileo) for interfacing various sensors and communicating data using Internet using various Protocols.
3. Minimum two experiments using any hardware platform (Arduino/Raspberry Pi/BeagleBone/Galileo) and wireless communication protocol (802.11 and 802.14.5 IEEE standard)
4. Minimum one experiment using Cloud Storage.

Suggested topics for Case Study:
Faculty members can suggest topics pertaining above syllabus and ask students to submit complete report covering design issues, hardware and software details and applications.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL 802	Analog and Mixed VLSI Design	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXL 802	Analog and Mixed VLSI Design	-	-	-	-	25	--	25	50

Course Objectives:

Lab session includes **seven experiments plus one presentation on case study.** The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Suggested Experiments:

Use of Online Tools to study analog VLSI circuits

2. Analysis of MOSFETs for analog performance

3. Design and simulate various types of current mirror circuits

4. Design and simulate various common source amplifier circuits

5. Design and simulate various types of single stage amplifiers

6. Design and simulate differential amplifier

7. Design and simulate operational tran-sconductance amplifier

8. Design and simulate switch capacitor circuits

9. Design and simulate various types of oscillators

10. Design and simulate mixed mode circuit

11. Generate layout for the simple and cascode current mirror

12. Generate layout for common source amplifier

13. Generate layout for the differential amplifier

14. Generate layout for the Oscillator

15. Generate layout for Phase Detector

Suggested topics for Case Study:
Faculty members can suggest topics pertaining above syllabus and ask students to submit proper report covering the latest advances in the field of Mixed VLSI Design.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO 8041	Advanced Power Electronics Lab.	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXDLO 8041	Advanced Power Electronics Lab.	-	-	-	-	25	--	25	50

Course Objectives:

Lab session includes **seven experiments plus one presentation on case study.** The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Suggested Experiments:

1. Single Phase Full Controlled Bridge Rectifier.
2. Speed control of Separately excited DC motor using Armature Voltage Control
3. Speed control of 3-phase Induction Motor using V/F control.
4. Simulation of 3-phase fully controlled Bridge rectifier with R and RL load.
5. Simulation of 1-phase fully controlled Bridge rectifier and study of various parameters.
6. Simulation of 1-phase Inverter and study of various Performance parameters.
7. Simulation of SVM Inverter.
8. Simulation of Closed loop dc-dc converter
9. Study High Frequency Induction heating & Dielectric heating.
10. Study of operation and control of solid state relays.

Suggested topics for Case Study:

Faculty members can suggest topics pertaining above syllabus and ask students to submit complete report covering design issues, hardware and software details and applications.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO 8042	MEMS Technology Lab.	-	2	--	-	01	--	01

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test 2	Ave. Of Test 1 and Test 2					
ELXDLO 8042	MEMS Technology Lab.	-	-	-	-	25	--	25	50

Course Objectives:

Lab session includes **seven experiments plus one presentation on case study.** The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Suggested Experiments:

1. Design electro-statically actuated cantilever
2. Design bimorph cantilever which act as pressure sensor.
3. Dynamic analysis of Beam
4. Find the tip deflection of the cantilever with different types of load
5. Find the tip deflection of the cantilever in sweep analysis
6. Model and simulate Electro-mechanical actuator. Do dc and transient analysis
7. Design the geometry of MEMS and find performance characteristics such as resonant frequency, deflection per voltage or temperature
8. Simulate the harvested electrical power from mechanical vibrations using piezoelectric cantilever beam
9. Model and simulate of accelerometer
10. Case study of MEMS based device

Suggested topics for Case Study:
Faculty members can suggest topics pertaining above syllabus and ask students to submit complete report covering fabrication issues, materials, characterization and applications of the MEMS devices.

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO8043	Virtual Instrumentation Laboratory	--	02	--	04	--	--	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Exam			
		Test I	Test II	Average				
ELXDLO8043	Virtual Instrumentation Laboratory	--	--	--	--	25	25	50

Term Work :-

At least 6 experiments covering entire syllabus of ELXDLO8043 (Virtual Instrumentation) should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. One presentation on a case study based on the topic in Virtual Instrumentation need to be submitted. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced

Suggested List of Experiments :-

1. Verification of arithmetic operations
2. Verification of Boolean Expressions / half-adder & full-adder
3. Implementation of array functions
4. Program to convert Celsius into Fahrenheit & vice-versa
5. Program for implementing seven segment display
6. Program for calculating body mass index (BMI) using cluster

7. Program to control temperature using thermistor / RTD & DAQ
8. Program to control liquid flow using DAQ
9. Program to control liquid level using DAQ
10. Program to control pressure using DAQ
11. Program for DC motor speed control using PID toolbox

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXDLO8044	Digital Image Processing	--	02	--	04	--	--	04

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Oral & Practical	Total
		Internal Assessment (IA)			End Semester Exam			
		Test I	Test II	Average				
ELXDLO8044	Digital Image Processing	--	--	--	--	25	25	50

Term Work :-

At least 7 experiments covering entire syllabus of ELXDLO8044 (Digital Image Processing) should be set to have well predefined inference and conclusion. The experiments should be student centric and attempt should be made to make experiments more meaningful, interesting. Simulation experiments are also encouraged. Experiment must be graded from time to time. One presentation on a case study based on the topic in Digital Image Processing need to be submitted. The grades should be converted into marks as per the Credit and Grading System manual and should be added and averaged. The grading and term work assessment should be done based on this scheme. The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum passing marks in term work. Practical and Oral exam will be based on the entire syllabus. The Term work assessment can be carried out based on the different tools and the rubrics decided by the concerned faculty members and need to be conveyed to the students well in advanced.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Practical	Tutorial	Total
ELXL704 ELXL803	Project I Project II	-	06 12	--	-	03 06	--	09

Objectives:

1. To acquaint with the process of undertaking literature survey/industrial visit and identifying the problem
2. To familiarize the process of problem solving in a group
3. To acquaint with the process of applying basic engineering fundamental in the domain of practical applications
4. To inculcate the process of research Outcomes

Outcome:

Learner will be able to:

1. Do literature survey/industrial visit and identify the problem
2. Apply basic engineering fundamental in the domain of practical applications
3. Cultivate the habit of working in a team
4. Attempt a problem solution in a right approach
5. Correlate the theoretical and experimental/simulations results and draw the proper inferences
6. Prepare report as per the standard guidelines.

Students should do literature survey/visit industry/analyse current trends and identify the problem for Project and finalize in consultation with Guide/Supervisor Students should use multiple literatures and understand the problem. Students should attempt solution to the problem by experimental/simulation methods. The solution is to be validated with proper justification and the report needs to be compiled in standard format.

Guidelines for Assessment of Project I

Project I should be assessed based on following points

a) Quality of problem selected
b) Clarity of Problem definition and Feasibility of problem solution
c) Relevance to the specialization
d) Clarity of objective and scope
e) Breadth and depth of literature survey

Project I should be assessed through a presentation by the student project group to a panel of Internal examiners appointed by the Head of the Department/Institute of respective Programme.

Guidelines for Assessment of Project II

Project II should be assessed based on following points

a) Quality of problem selected
b) Clarity of Problem definition and Feasibility of problem solution
c) Relevance to the specialization / Industrial trends
d) Clarity of objective and scope
e) Quality of work attempted
f) Validation of results
g) Quality of Written and Oral Presentation

Project Report has to be prepared strictly as per University of Mumbai report writing guidelines. Project II should be assessed through a presentation by the student project group to a panel of Internal and External Examiner approved by the University of Mumbai Students should be motivated to publish a paper in Conferences/students competitions based on the work